\$1 0 X 0 1 0 1 1 1 X 0 XX XX XX X	\$2 0 X 0 0 0 X 0 X 0 XX 0,I(14) X	1 0 1 0 0 X 0 X 0 0 X X 0 0 X X 0 1 X X	\$4 0 X 0 0 0 X 0 X 0 0 1 XX	\$40 0 1 1 1 0 0 0 X 0 XX XX XX	\$5 0 X 0 0 0 X 0 X 0 00 X 1	\$6 0 X 0 0 1 0 0 0 1 XX		\$8 0 X 0 0 0 X 0 X 0 X 0 X X	\$9 0 X 0 0 0 X 0 X 0 X	\$10 0 X 0 0 0 X 0 1 1	s11 0 X 0 1 0 0 0 0 X 0 X	\$12 0 X 0 0 0 X 0 0 1	\$13 0 X 0 0 0 0 X 0 1 1	s1 () () () () () () () () () () () () ()
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0 1 0 1 1 X 0 XX XX XX	0 0 0 X 0 X 0 XX 0,I(14)	1 0 0 X 0 X 0 X 0 X 0 X	0 0 0 X 0 X 0 0 1	1 1 0 0 0 X 0 XX	0 0 0 X 0 X 0 0 00 XX	0 0 1 0 0 0 1 1	0 0 0 X 0 X 0 X	0 0 0 X 0 X 0 X	0 0 0 X 0 X 0	0 0 0 X 0 1 1	0 1 0 0 0 X	0 0 0 X 0 0	0 0 0 X 0 1	
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1 1 X 0 XX XX XX X	X 0 X 0 XX 0,I(14)	X 0 X 0 X XX 01 XX	0 X 0 0 01 XX	0 0 X 0 XX XX	X 0 X 0 00 XX	0 0 0 1 XX	X 0 X 0 XX	X 0 X 0 XX	X 0 X 0 01	X 0 1 1 XX	0 0 X 0	X 0 0	X 0 1	
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XX X X	0,I(14) X	01 X	XX	XX	XX						XX	10	XX	
X X	Х	X				XX	XX	YY				. •		Х
Х			1	Х	4			^^	XX	01	XX	XX	01	1
	1				1	X	Х	Х	1	Х	Х	0	Х	
0		X	X	Х	1	Х	1	Х	~l(12)	1	Х	X	Х	
3	0	0	1	0	1	0	0	0	1	0	0	1	0	
0	1	0	0	0	1	0	1	0	1	1	0	0	0	
XX	0,I(12)+I(14).(~I(15)) 00	XX	XX	XX	XX	XX	XX	XX	XX	10	XX	XX	00
0	1	1	0	0	0	0	0	0	0	0	1	0	0	
Х	1	X	Х	Х	Х	Х	X	0	Х	Х	Х	X	Х	
0	1	0	0	0	0	0	0	1	0	0	0	0	0	
XX	00	01	XX	XX	00	10	00	00	00	00	XX	10	XX	Х
XX	00	10	XX	XX	00	00	0,~I(13)	00	10	00	XX	00	XX	Х
XX	XX	01	00	00	XX		XX	01	XX	10	XX	01	10	Х
0	0	1	1	1	0	1	0	1	0	1	0	1	1	
_decoder	S2_decoder	S3_decoder	S1	S4	S1	S6_decoder	S1	S9	S1	S11	S12	S12_decoder	S14	S
:=Alı	u_lowermux_ctrl(0).(~Alu_lowermu	ıx_ctrl(1))											
	X 0 XX XX XX 0 decoder	X 1 0 1 XX 00 XX 00 XX XX 00 XX XX 00 0 0 0 0 0	X 1 X 0 1 0 XX 00 01 XX 00 01 XX 00 10 XX 01 0 0 10 0 52_decoder S3_decoder	X 1 X X 0 1 0 0 XX 00 01 XX XX 00 10 XX XX XX 01 00 0 0 1 1 decoder S2_decoder S3_decoder S1	X 1 X X X X 0 0 1 0 0 0 0 0 0 0 0 0 0 0	X 1 X X X 0 1 0 0 0 0 XX 00 01 XX XX 00 XX 00 10 XX XX 00 XX XX 01 00 00 XX 0 0 1 1 1 0 decoder S2_decoder S3_decoder S1 S4 S1	X 1 X X X X X 0 1 0 0 0 0 0 XX 00 01 XX XX 00 10 XX 00 10 XX XX 00 00 XX XX 01 00 00 XX 0 0 1 1 1 0 1 decoder S2_decoder S3_decoder S1 S4 S1 S6_decoder	X 1 X X X X X X X X X X X X X X X X X X	X 1 X X X X X X X 0 0 0 0 0 1 0 0 1 1 0 0 0 0	X 1 X X X X X X X X 0 X 0 1 0 0 0 0 0 0 0 0 1 0 XX 00 01 XX XX 00 10 00 00 XX 00 10 XX XX 00 10 00 00 XX 00 10 XX XX 00 00 0,~I(13) 00 10 XX XX 01 00 00 XX XX 01 XX 0 0 0 1 1 1 1 0 1 0 1 0 decoder S2_decoder S3_decoder S1 S4 S1 S6_decoder S1 S9 S1	X 1 X	X 1 X	X 1 X	X 1 X X X X X X X X X X X X X X X X X X