# Srivarun Reddy Vanga

#### **UNDER-GRADUATE STUDENT**

Guntur, Andhra Pradesh India

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https://varunreddyvanga.github.io/resume



# **Objective:**

Seeking for a responsible position, Eager to kick-start my career with a reputable company that offers opportunities for skill development and career advancement. I aim to leverage my skills to contribute positively to the organization's objectives and foster personal growth.

#### **Education Details:**

2022 - Current (Pursuing)

Bachelor's in technology | ELECTRONICS AND COMMUNICATION ENGINEERING (SPECIALIZED IN VLSI)
KL University, Guntur.

• 2019 – 2022

**Diploma | MECHATRONICS ENGINEERING AND SMART FACTORY** 

Nettur Technical Training Foundation, Electronic City, Bangalore.

• 2018 - 2019

High Schooling | SSC

Dr.KKR Gowtham Concept School, Guntur.

## Skills:

Programming: Verilog System Design, C, Java, Python, HTML, CSS, PLC Ladder Logic, Embedded C.
Industrial Automation: Industrial Robotics (Teach Pendant), CNC, Electro (Pneumatics & Hydraulics), PLC Controller.

Tools & Software: Cadance, Siemens TIA Portal, Cadance, NI Multisim, MATLAB, Autodesk Fusion 360, MPLAB IDE.

Machine & Equipment: Siemens S7(300, 500), FANUC LR MATE 300(IC, ID), CNC Machine, E-Pneumatic Controller, HMI.

Material Handling Work: Machine Tool Works, Lathe Machine, Workshop Management and Safety, Welding.

Soft Skills: Leadership Skills, Effective Communication, Quick Learner, Responsible.

## **Projects & Accomplishments:**

## KL – SAT 2(KL ZAC Module(2A), CAN Module(2B)):

Ongoing\*

- Two modules of Ballon Satellites will be launched, KL ZAC and CAN Module.
- Designed a website for satellite launch and data panel broadcasting over 1 lakh viewers
- Developed Frontend web development and satellite communication.
- Travels for an Altitude of 35kms and vertically 80 kms and studies Atmospheric parameters and equipped with GPS positioning and biological sensors.
- My Design: https://varunreddyvanga.github.io/KL-SAT2

#### **VERILOG BY VARUN WEB:**

Ongoing\*

- Designed a website for helping students to learn Verilog System Design and Verilog Programming, created with complete Verilog learning course.
- Designed using HTML, CSS, web page Front- end development and Verilog System designs.
- Web-Portal: https://varunreddyvanga.github.io/verilogbyvarun

#### **Publications:**

# KL SAT – (A MISSION ASSISTED FOR STUDY OF ATMOSPHERIC PARAMETERS AT STRATOSPHERE):

## Research Paper - eCF Paper id #1571025661

- Launched KLSAT Ballon Satellite that travels horizontally 35kms, defines the complete process of design.
- Focused on Satellite Electronics Commucation and Web designing, Space Research.
- Accepted for Presentation at the Conference and for forwarding for publications in the Proceedings of 2024
   IEEE Space on IEEE Xplore.

# **Corporate Experience:**

Apr 2022 – May 2022 (2 Months)

# Addverb Technologies Pvt Ltd | DIPLOMA ENGINEER TRAINEE

Noida, Uttar Pradesh

- After Sales Team, Responsible for the maintenance of Warehouse Automation System (WAS), Warehouse Control System (WCS).
- On-site at Customer B2B Worksites Reliance Retail, Bangalore.
- PLC Controller Electronic and Software Programming Management.

## **Certifications:**

- Tessolve Semiconductors | Embedded & IOT Programming
- Tessolve Semiconductors | VLSI Design & Verification
- Tessolve Semiconductors | Artificial Intelligence level 1
- INTERNATIONAL DIPLOMA | MECHATRONICS ENGINEERING AND SMART FACTORY

2019 – 2022 (Diploma) | NATIONAL OPEN COLLEGE NETWORK(NOCN), Bangalore.

- Collaborated with NTTF and Virtual Diploma.
- International Diploma Credits from NOCN.
- Organization with UNITED KINGDOM Government approval and Internationally approved credits.

# Languages:

**ENGLISH:** Full Professional Proficiency

**HINDI:** Limited Working Professional

TELUGU: Native and bilingual Language