

# Operational NMOS amplifier with NMOS differential pair

February 11, 2024

## **1 current bias generation**

Fig.?? represents the minimum voltage allowed for the current reference to work as supposed.

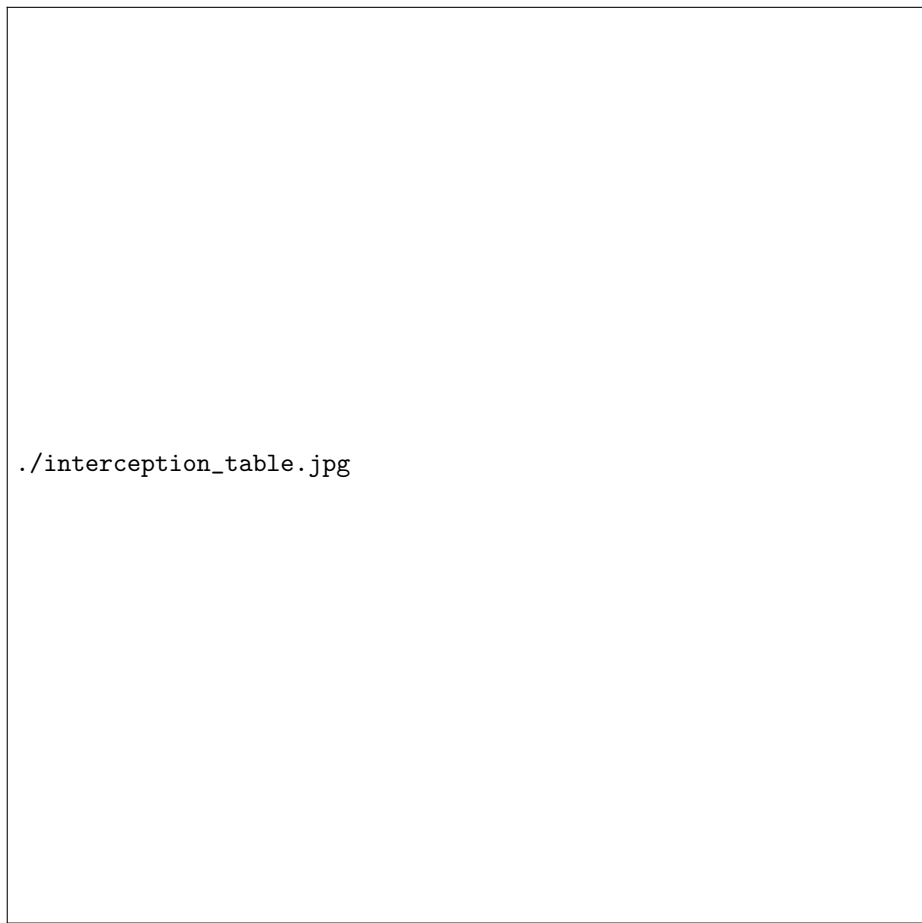


Figure 1: minimum voltage

Fig.?? represent the output current characteristic fo nominal, worst and best case scenario

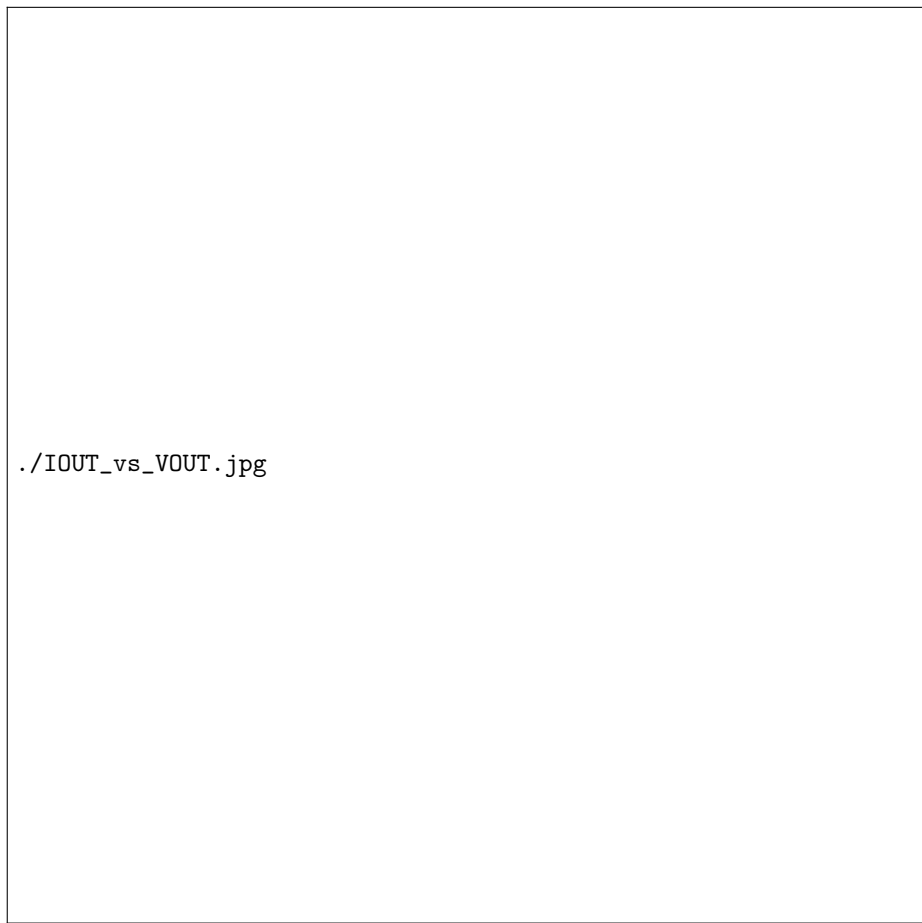


Figure 2: IOUT vs VOUT

Fig.?? represent the output impedance characteristic fo nominal, worst and best case scenario.

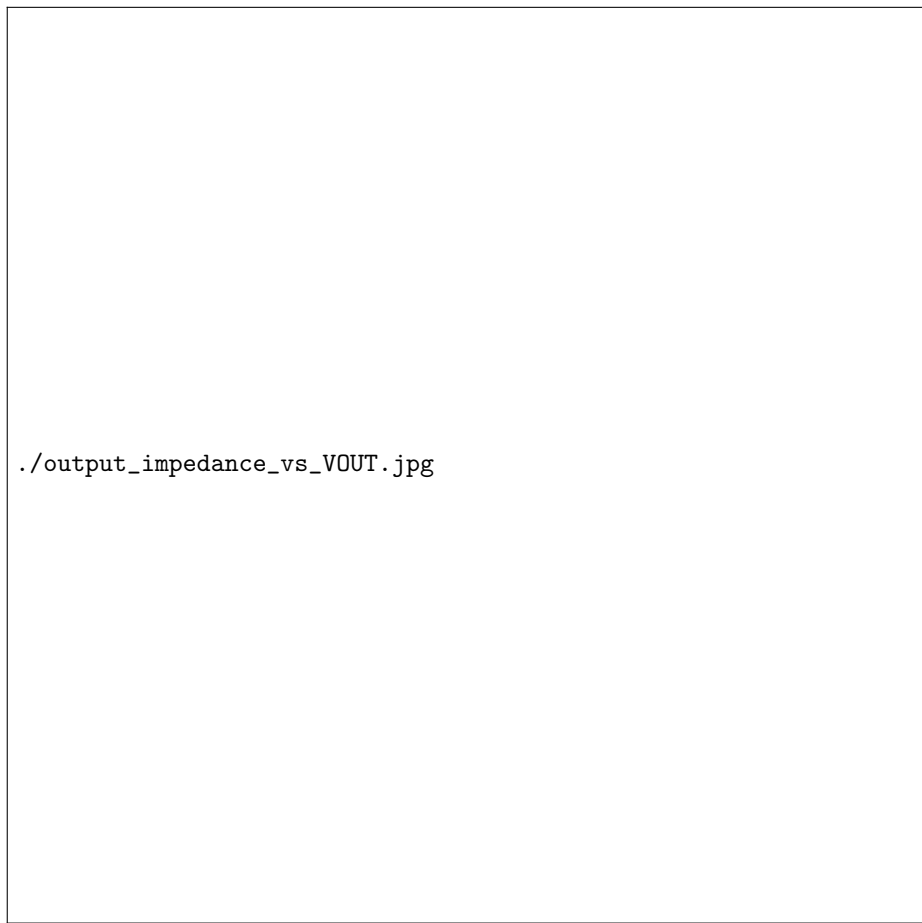


Figure 3: output impedance

Fig.?? represent the current variation face an ac signal fo nominal, worst and best case scenario

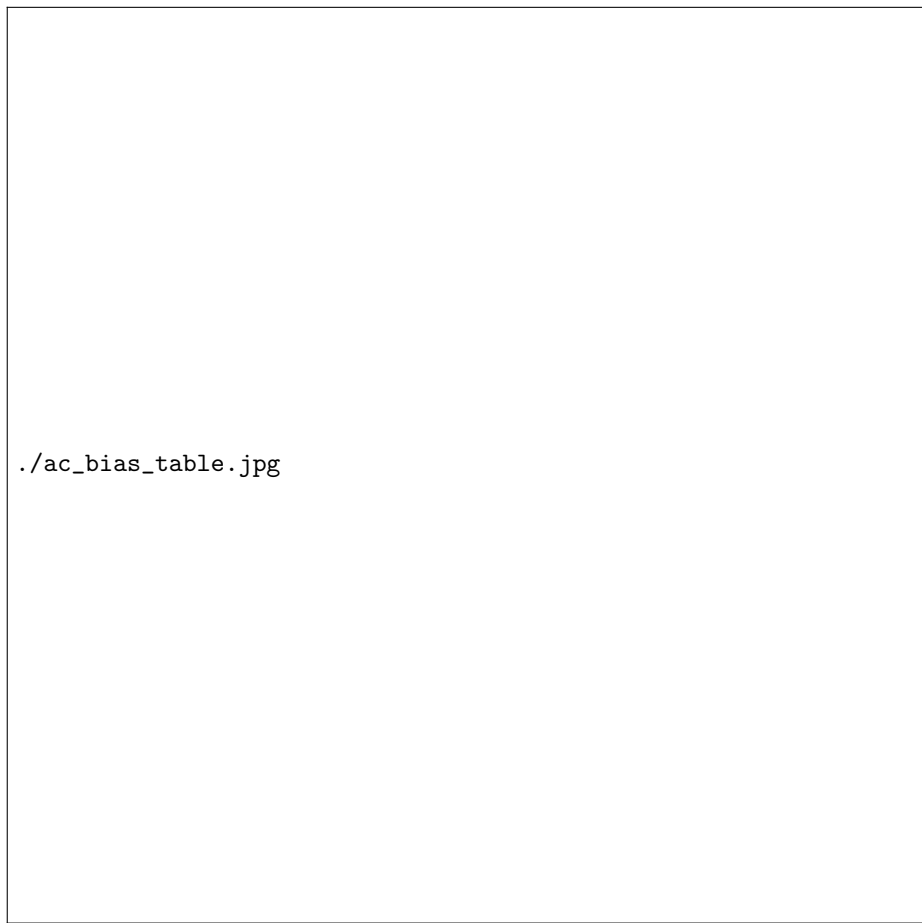


Figure 4: ac variation

Fig.?? represent the current variation face an ac signal fo nominal, worst and best case scenario



Figure 5: ac variation