Vasilii Matrenin
C/C++ Developer

Projects

- **Bachelors second-year projects:**

  There is *repository* with my Intel ILAB projects. There are good projects written in C++ and built with CMake in this repository.

  GitHub: VasiliyMatr/ILAB_2ndYEAR .

- **Bachelors first-year projects:**

  There are two repositories with my bachelors first-year projects:

  First-term *repository* - With interesting toy programming language compiler *T08* and CPU simulator *T04* which executes compiler outputs. There are also other interesting projects.

  GitHub: VasiliyMatr/MIPT_PROG_1stTERM .

  Second-term *repository* - With optimization projects *T05* , *T06*; assembly tryouts and there is also T04CPU executable files to x86 executable files translator - *T07*.

  GitHub: VasiliyMatr/MIPT_PROG_2ndTERM .

My skills

- **C/C++**

- Good practice skills in **CMake** and **Make**.

- Advanced **CPU architecture** knowledge.

- Basic skills in **Bash**.

- B1 **English** level.

- Good practice skills in **Tex**.

- Basic skills in **Python**.

Working experience

I have worked in Intel corporation as a summer intern for two months. My manager and mentor highly valued my work and sent good *feedback*.

Higher education

Second-year bachelors student in MIPT; Department of Radio Engineering and Cybernetics (DREC); Applied physics and mathematics.

Completed additional courses

- Industrial programming cource - during bachelors first-year in MIPT.
  Lecturer - I. Dedinsky.

- HiSilicon OS and CPU simulation cource - during bachelors second-year in MIPT.
  Lecturer - I. Petushkov.

- Intel ILAB C++ cource - during bachelors second-year in MIPT.
  Lecturer - K. Vladimirov.

- Intel ILAB micro architecture cource - during bachelors second-year in MIPT.
  Lecturer - K. Korolev.

- DREC microcontrollers course.

- DREC FPGA course.