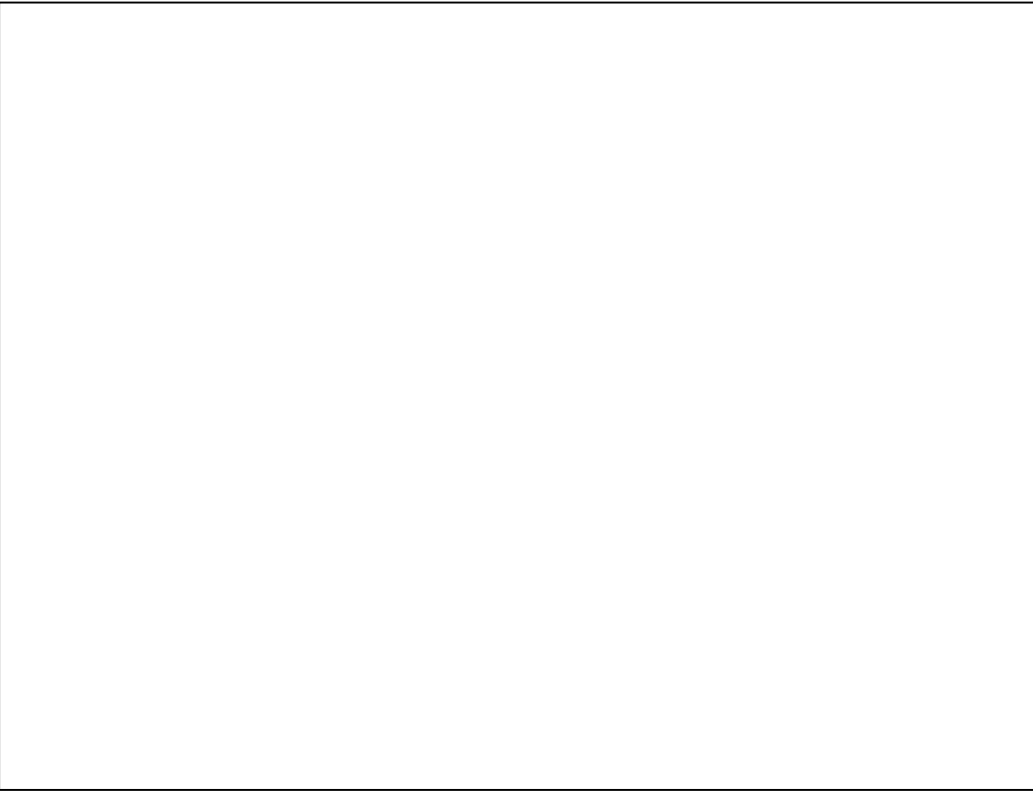


# M2 & ihere interface Board Schematics

Block Diagram

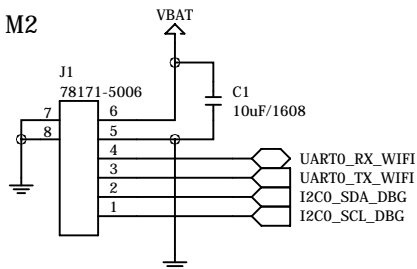


Revision history

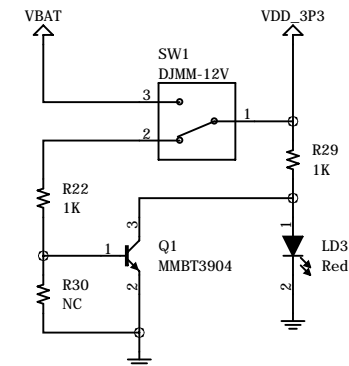
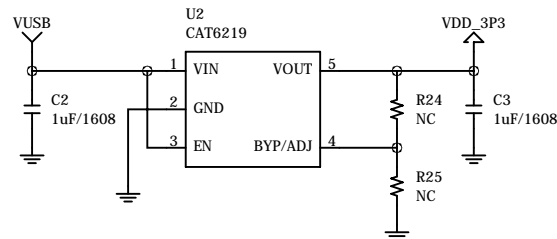
COMPANY:			WISOL		
TITLE:			M2 & ihere interface B'd		
DRAWING NO:		REV:		SIZE:	
M2_ihere_sub		0.0		A4	
Date:	11/7/2017	SHEET:		1	OF 3

# [ DL / Debugging ]

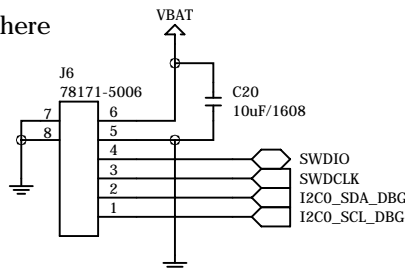
Connect to M2



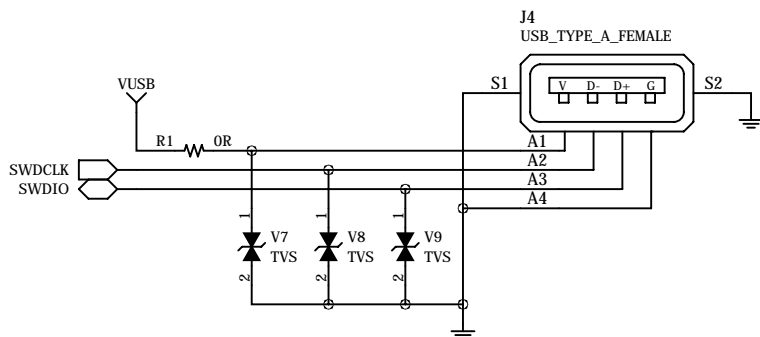
USB bridge interface level power



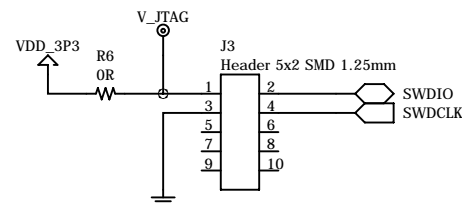
Connect to ihere



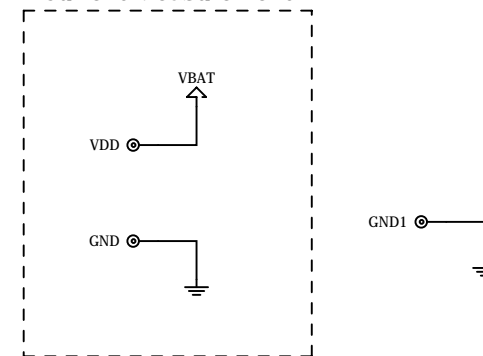
Connect to M2



Connect to JTAG



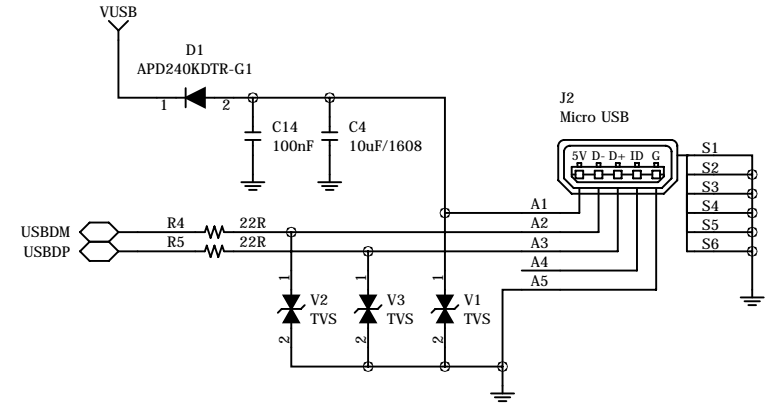
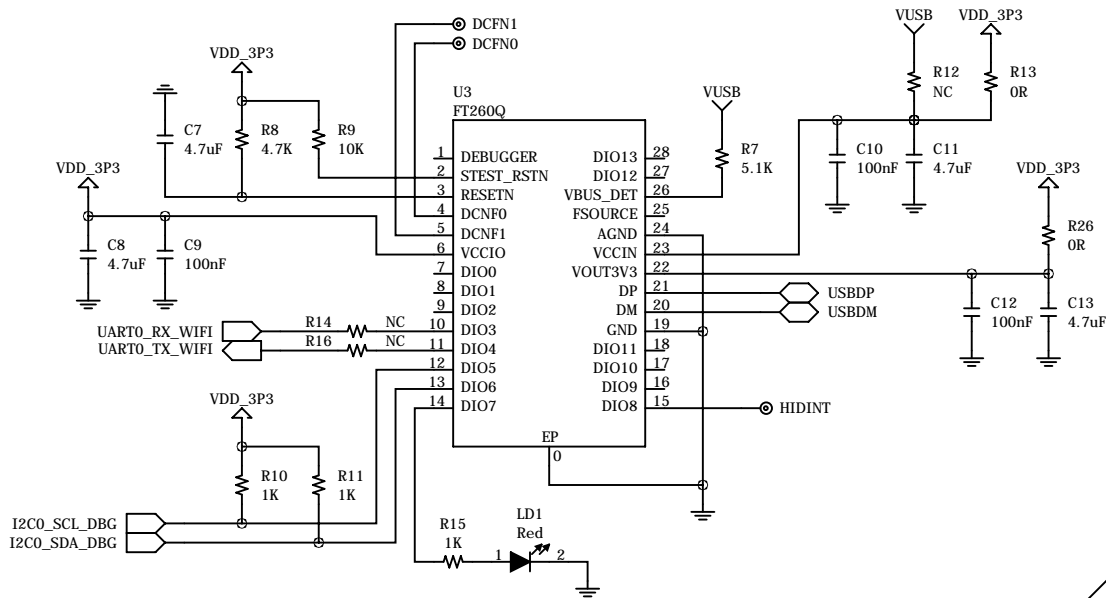
Current Measurement



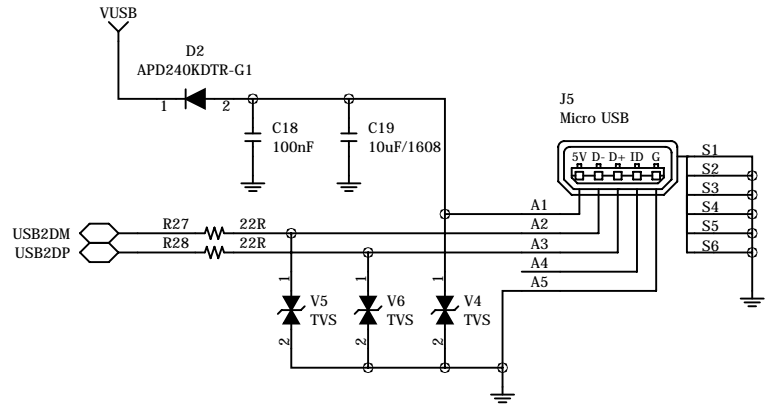
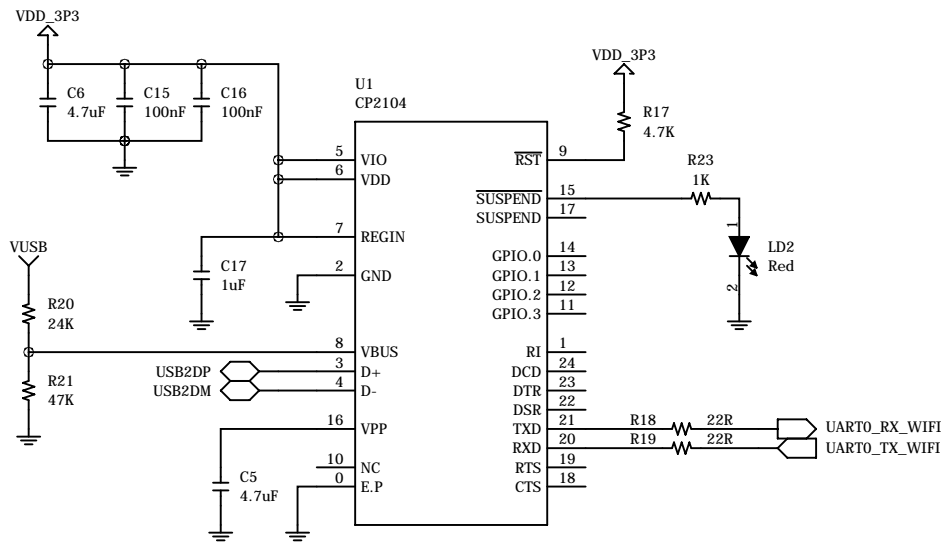
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TITLE: M2 & ihere interface B'd			
DRAWING NO: M2_ihere_sub		REV: 0.0	SIZE: A4
Date: 11/7/2017	SHEET: 2		OF 3

X1 NTH-3.2 X2 NTH-3.2 X3 NTH-3.2

# [ USB to I2C / UART - HID ]



# [ USB to UART ]



COMPANY: WISOL			
TITLE: M2 & ihere interface B'd			
DRAWING NO: M2_ihere_sub	REV: 0.0	SIZE: A4	
Date: 11/7/2017	SHEET: 3	OIB	