


U_Master_Block
Master_Block.SchDoc



U_Slave_Block
Slave_Block.SchDoc

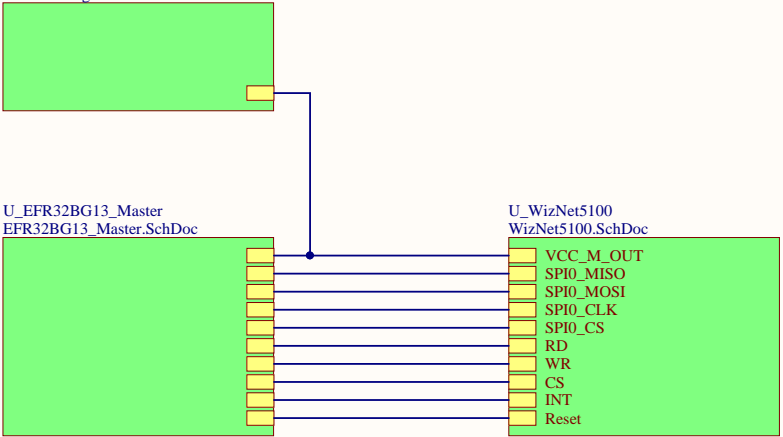



Title Block Diagram			University of Colorado Boulder Bhishma Low Power Embedded Design Techniques	
Size: A4	Number:.	Revision:		
Date: 10/20/2019	Time: 6:52:28 AM	Sheet 1 of 9		
File: C:\Users\Public\Documents\Altium\Projects\Bhishma\Block Diagram.SchDoc				

U_PowerManagementMaster
PowerManagementMaster.SchDoc

U_EFR32BG13_Master
EFR32BG13_Master.SchDoc

U_WizNet5100
WizNet5100.SchDoc



Title Master Block Diagram			University of Colorado Boulder Bhishma Low Power Embedded Design Techniques	
Size: A4	Number.:	Revision:		
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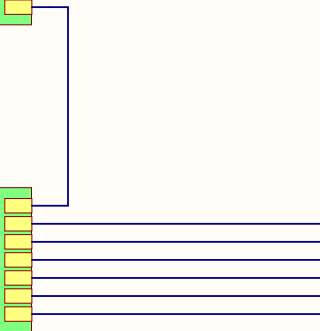
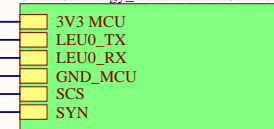
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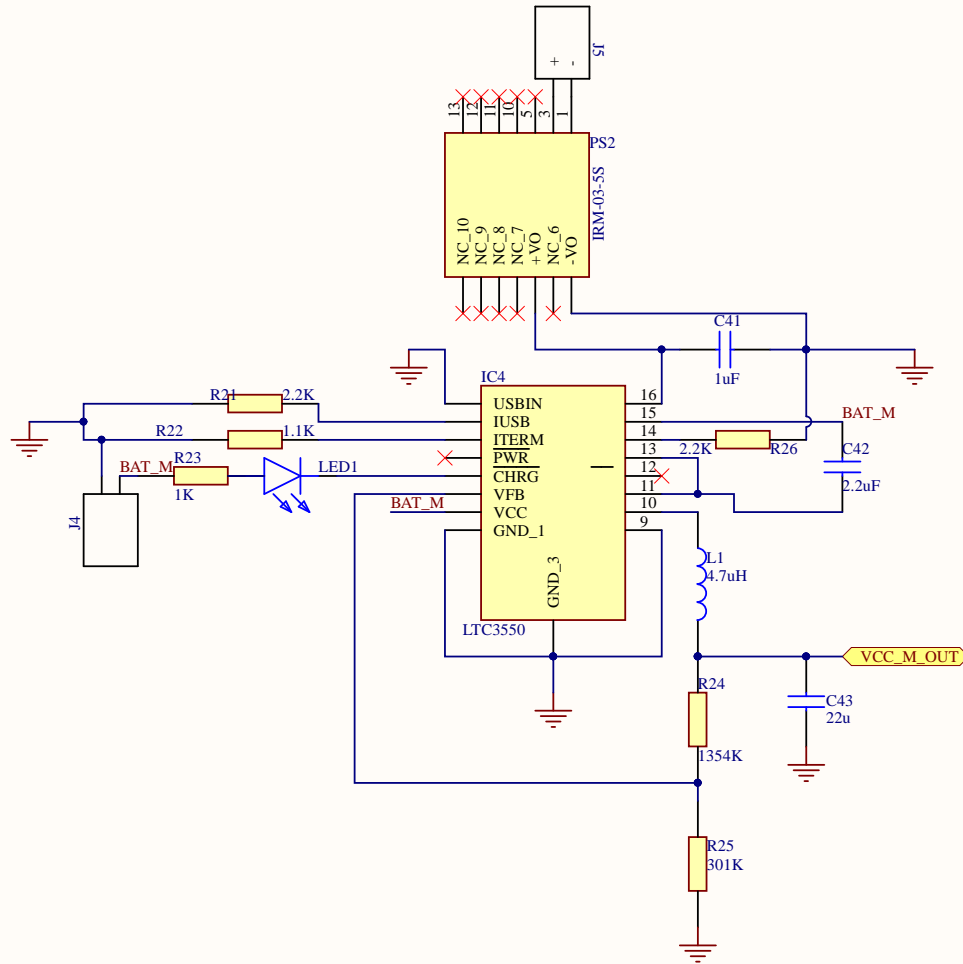
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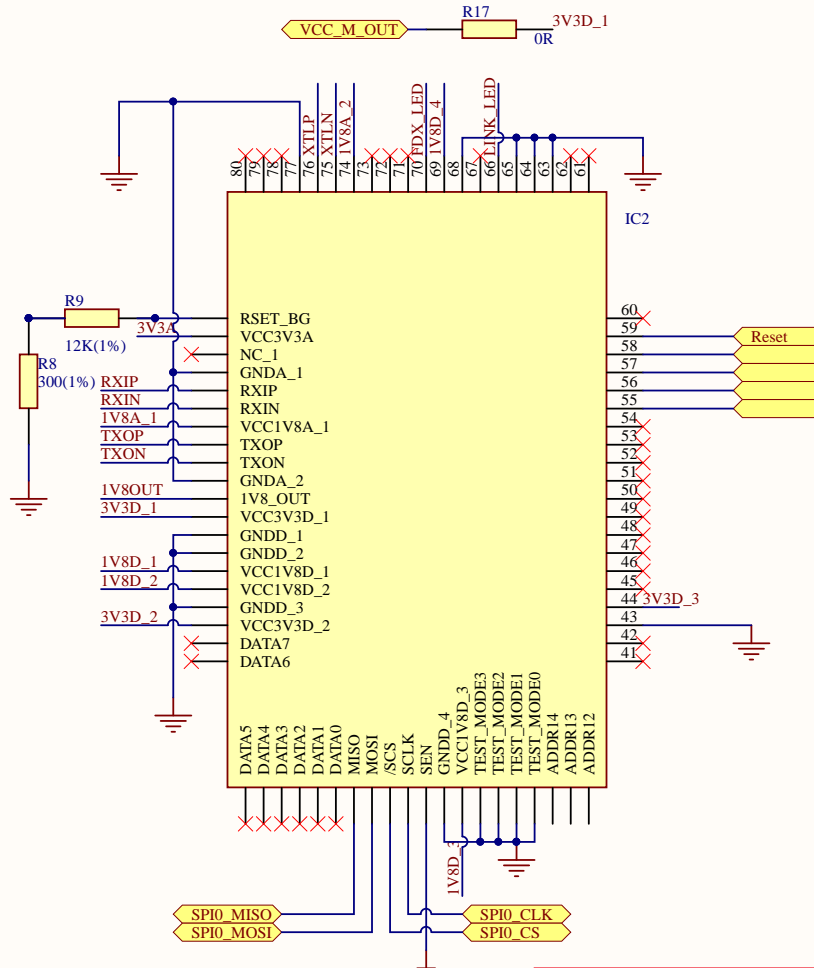
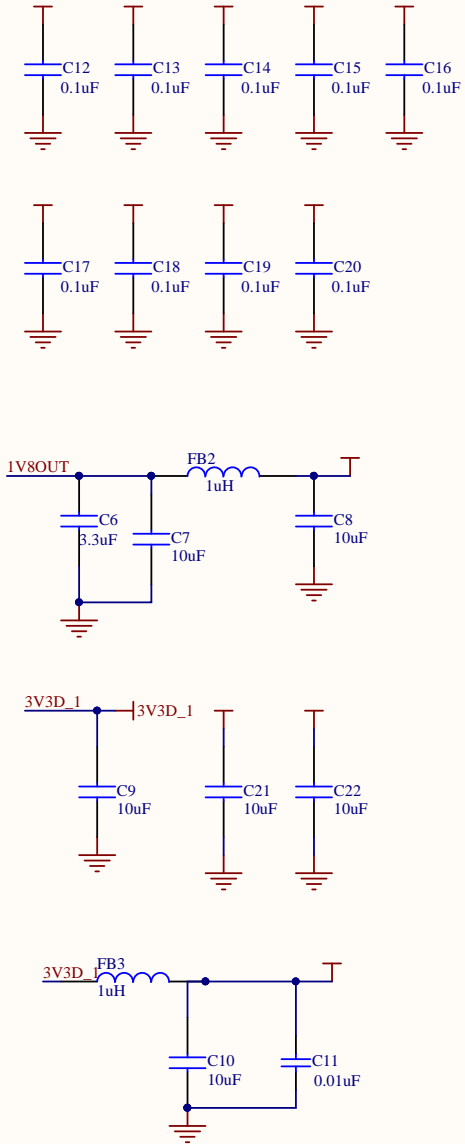
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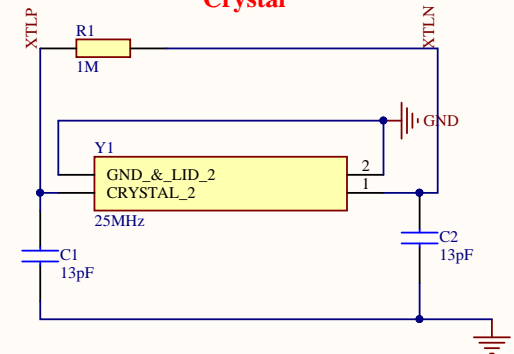
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Size: A4	Number:.	Revision:		
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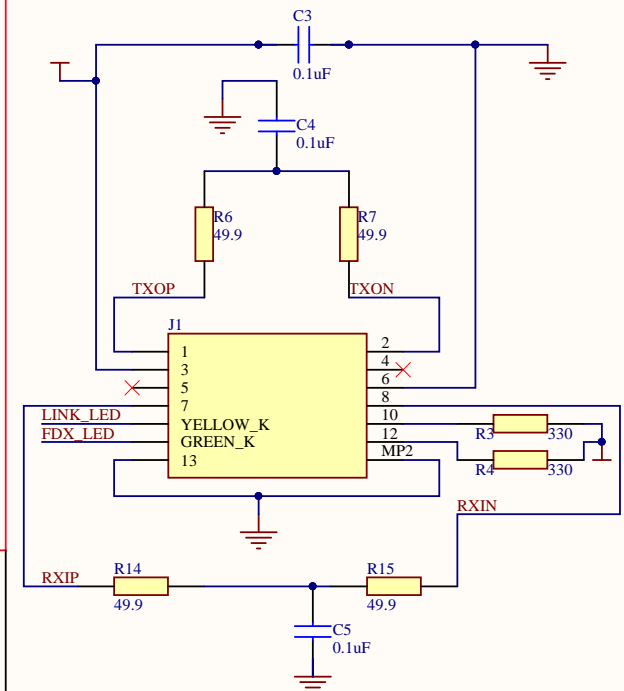
Decoupling Capacitors



Crystal



Ethernet Connector



Title **Wiznet 5100 Ethernet**

Size: **A4**

Number: *

Revision: 0.1

Date: 10/20/2019

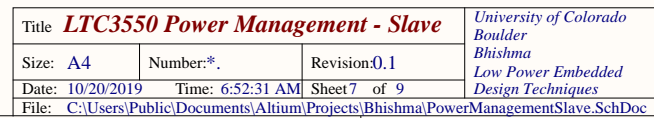
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Sheet 6 of 9

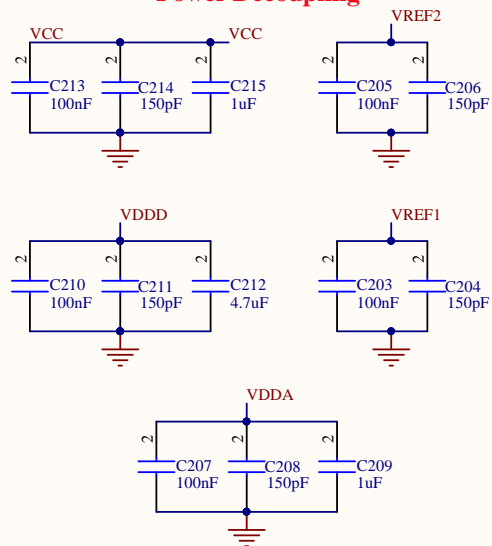
University of Colorado
Boulder
Bhishma
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Design Techniques

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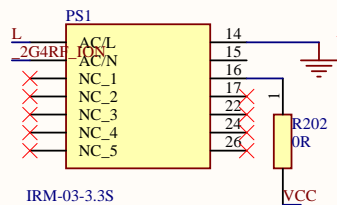
Altium



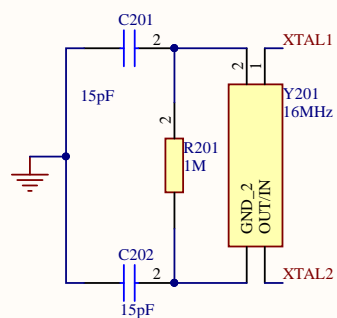
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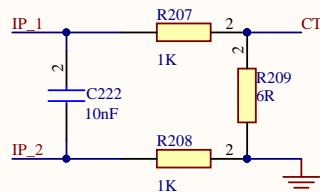
AC/DC converter



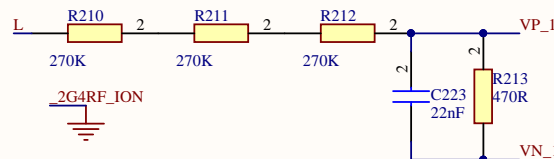
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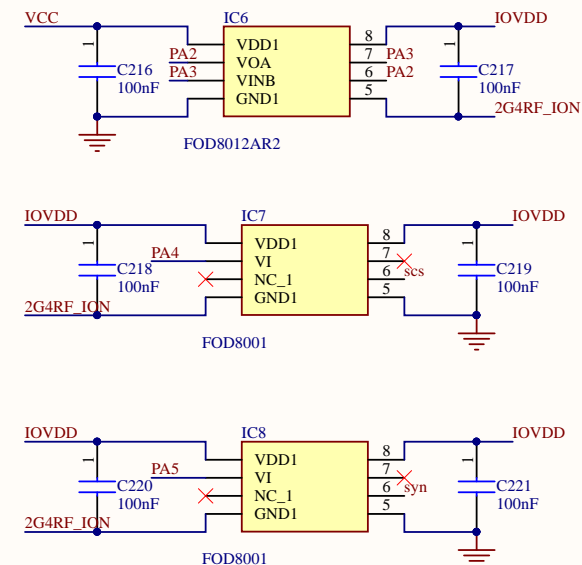
Current sensing



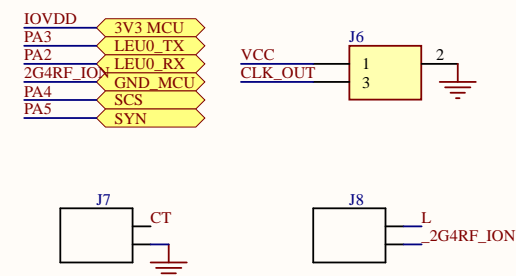
Voltage Sensing




Optoisolator circuit



Headers & Connectors



Title STPM34 Power Measurement			University of Colorado Boulder Bhishma Low Power Embedded Design Techniques	
Size: A4	Number:*	Revision:0.1		
Date: 10/20/2019	Time: 6:52:33 AM	Sheet 9 of 9		
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