Midterm



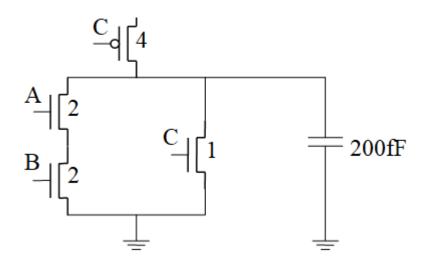
Time Limit: 1:30:00

Time Left:Time Exceeded

Vatsal Shreekant: Attempt 1

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S VS Q 2.pdf (1.06 MB)

X

a. Calculate the optimum path delay for the following circuit and the value of x, y, and z in order to achieve the optimum delay. (inverter: g=1, p=1; 2-input nor: g=5/3, p=2; 2-input nand: g=4/3, p=2; 4-input nand: g=6/3, p=4). (8 marks)