VeSPA – PCIE INTERPOSER GEN2

EDSCH0003–V2

REVISION	DESCRIPTION	DATE
A	Initial version	07/10/2019

MASTER_SHEET

File: master_sheet.kicad_sch

VeSPA – PCIe INTERPOSER

Sheet: /
File: EDSCH0003–V2–A.kicad_sch

Title: EDSCH0003–V2

Size: A4	Date: 07/10/2019	Rev: A
KiCad E.D.A. kicad (5.99.0–2858–ga3fc028c9b)		Id: 1/5

CONNECTORS

File: connectors.kicad_sch

AMPLIFIERS

File: amplifiers.kicad_sch

REDRIVER

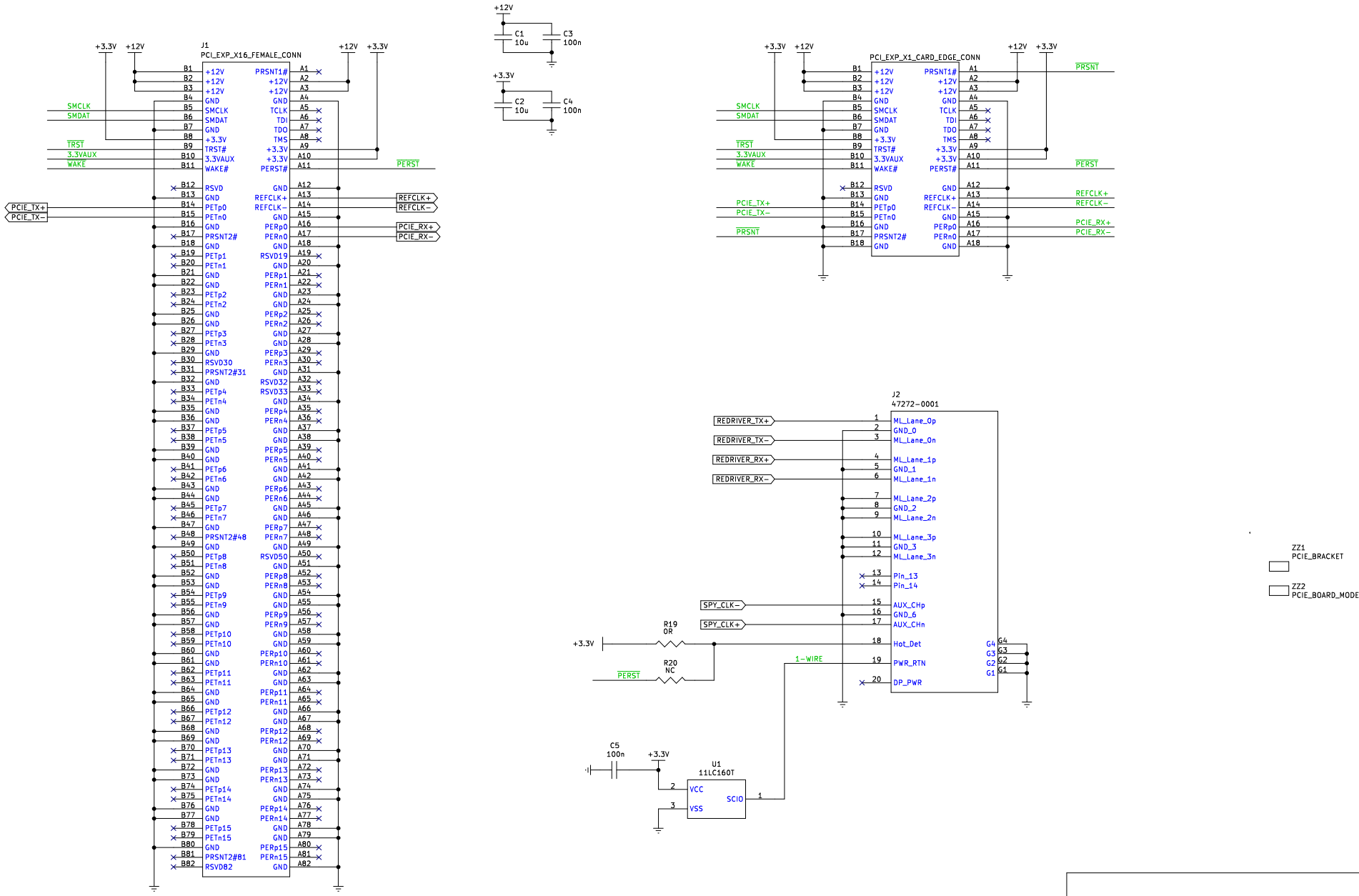
File: redriver.kicad_sch

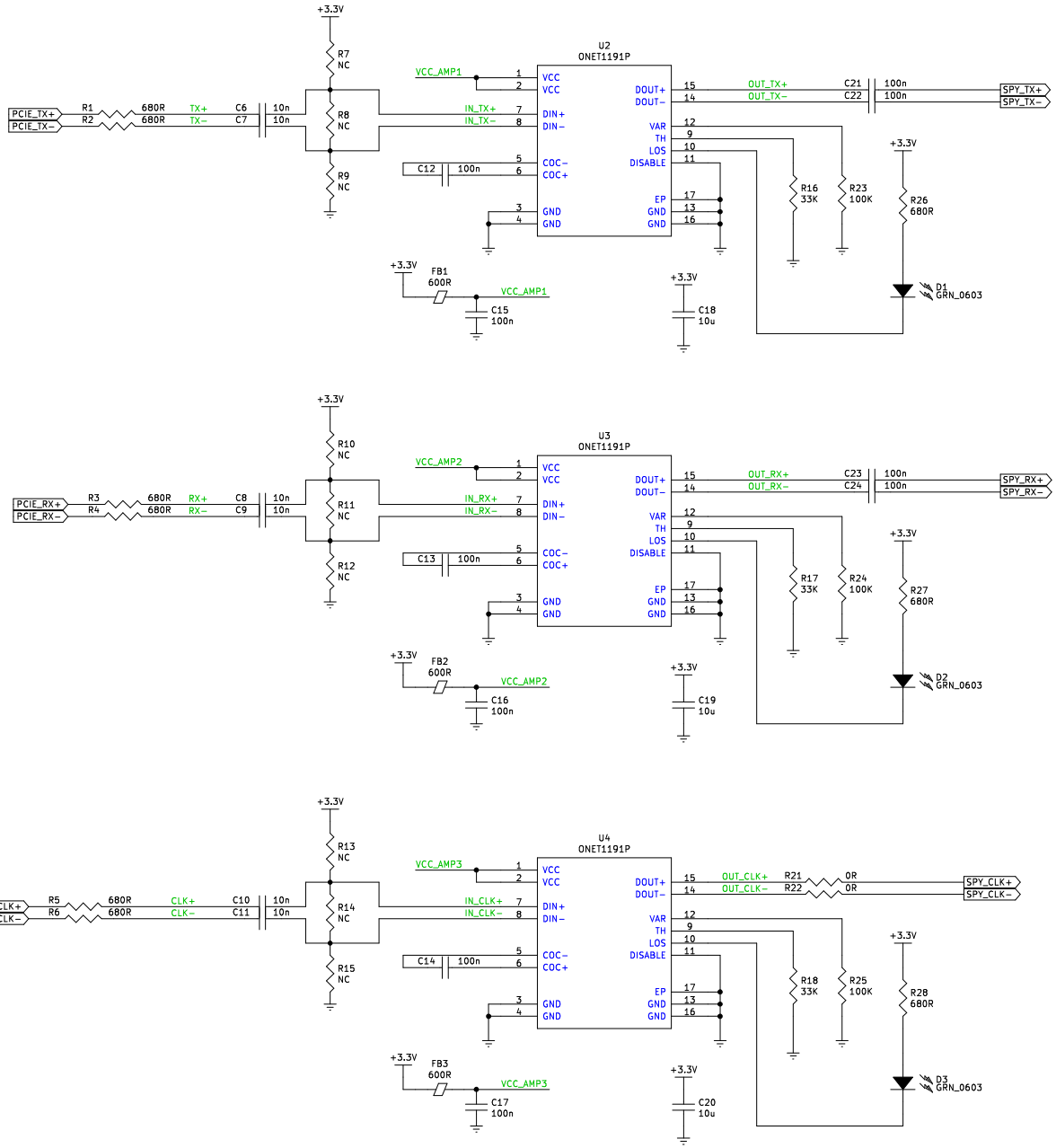
VeSPA – PCIe INTERPOSER

Sheet: /MASTER_SHEET/
File: master_sheet.kicad_sch

Title: EDSCH0003-V2

Size: A4	Date: 07/10/2019	Rev: A
KiCad E.D.A. kicad (5.99.0-2858-ga3fc028c9b)		Id: 2/5


J1
PCI_EXP_X16_FEMALE_CONN
+3.3V
+12V
SMCLK
SMDAT
TRST
3.3VAUX
WAKE
PERST
PCIE_TX+
PCIE_TX−
REFCLK+
REFCLK−
PCIE_RX+
PCIE_RX−
C1
10u
C3
100n
C2
10u
C4
100n
PCI_EXP_X1_CARD_EDGE_CONN
PRSNT
PCIE_TX+
PCIE_TX−
PRSNT
J2
47272−0001
REDRIVER_TX+
REDRIVER_TX−
REDRIVER_RX+
REDRIVER_RX−
ML_Lane_0p
GND_0
ML_Lane_0n
ML_Lane_1p
GND_1
ML_Lane_1n
ML_Lane_2p
GND_2
ML_Lane_2n
ML_Lane_3p
GND_3
ML_Lane_3n
Pin_13
Pin_14
SPY_CLK−
SPY_CLK+
AUX_CHp
GND_6
AUX_CHn
Hot_Det
PWR_RTN
DP_PWR
R19
0R
R20
NC
1−WIRE
C5
100n
U1
11LC160T
VCC
VSS
SCIO
ZZ1
PCIE_BRACKET
ZZ2
PCIE_BOARD_MODEL
VeSPA − PCIe INTERPOSER
Sheet: /MASTER_SHEET/CONNECTORS/
File: connectors.kicad_sch
Title: EDSCH0003−V2
Size: A3
Date: 07/10/2019
Rev: A
KiCad E.D.A. kicad (5.99.0−2858−ga3fc028c9b)
Id: 3/5

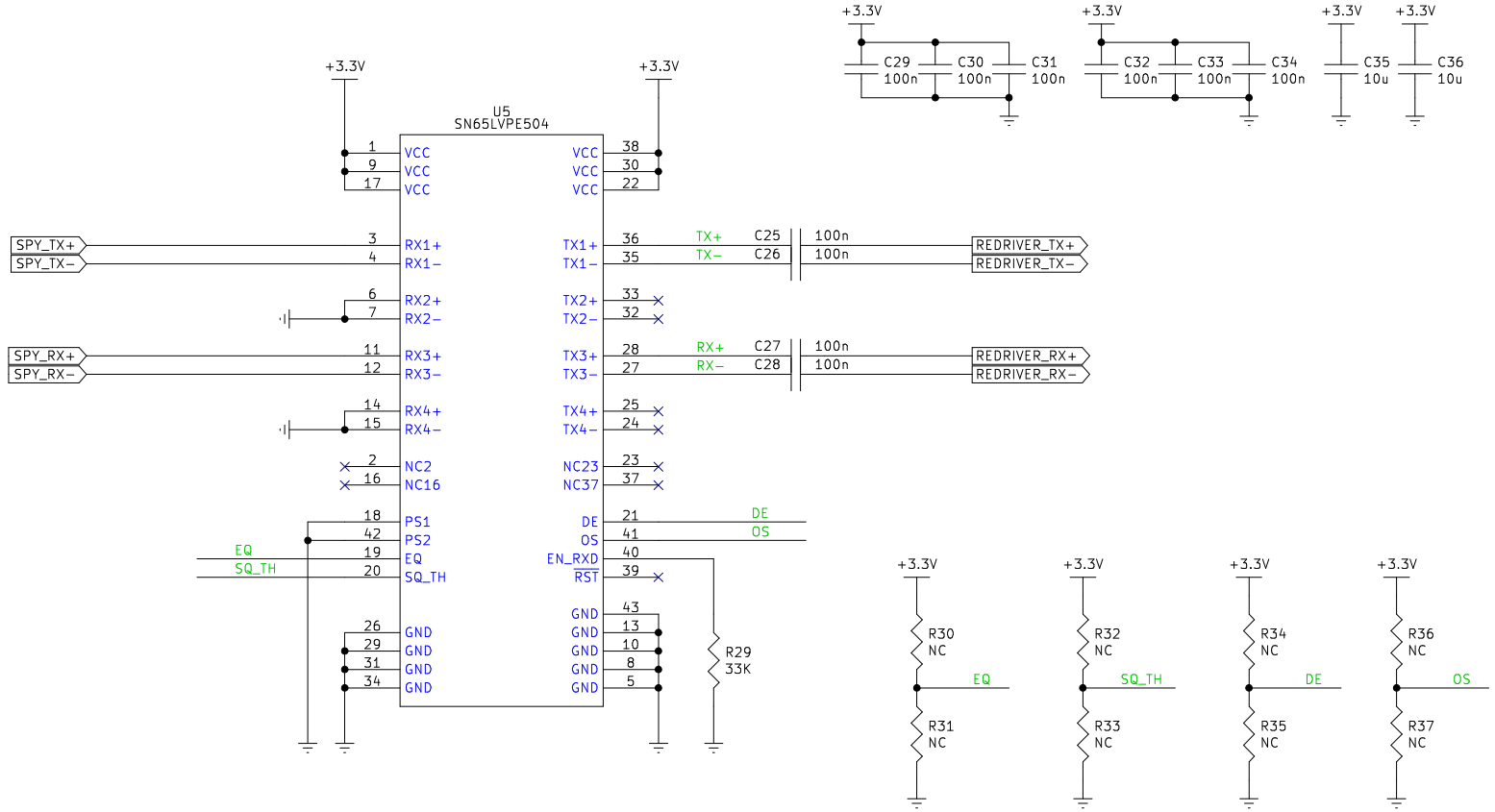


VeSPA – PCIe INTERPOSER

Sheet: /MASTER_SHEET/AMPLIFIERS/
File: amplifiers.kicad_sch

Title: EDSCH0003-V2

Size: A3	Date: 07/10/2019	Rev: A
KiCad E.D.A. kicad (5.99.0-2858-ga3fc028c9b)		Id: 4/5



VeSPA – PCIe INTERPOSER

Sheet: /MASTER_SHEET/REDRIVER/
File: redriver.kicad_sch

Title: EDSCH0003-V2

Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.99.0-2858-ga3fc028c9b)		Id: 5/5