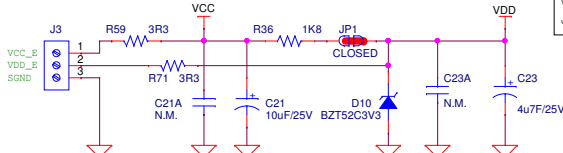


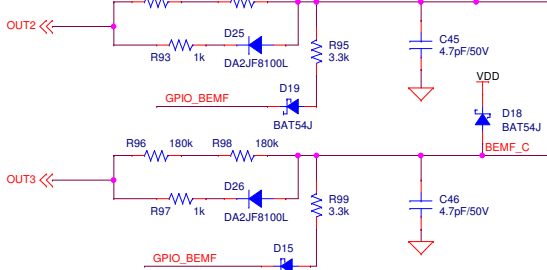
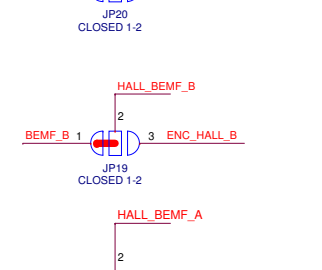
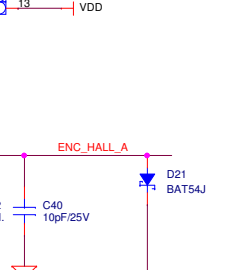
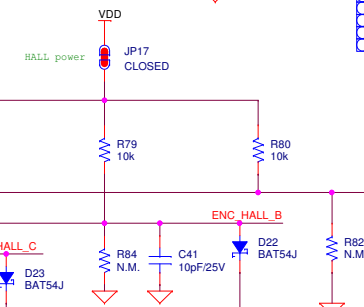
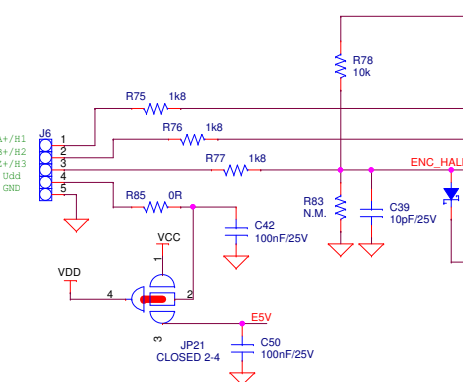
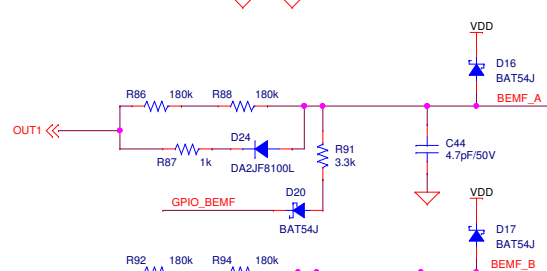
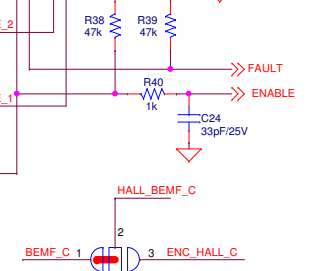
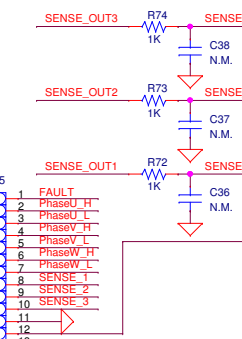
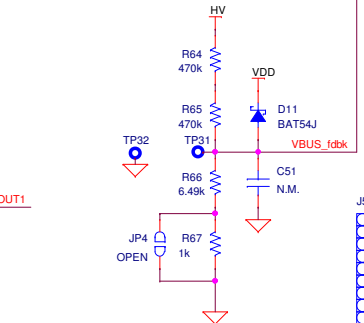
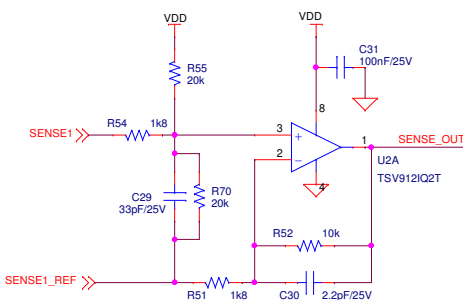
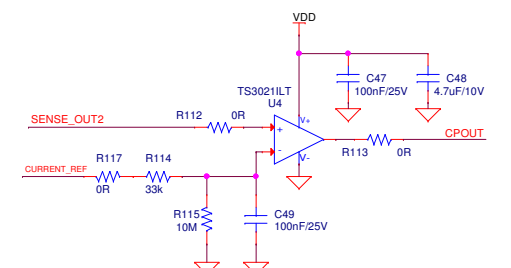
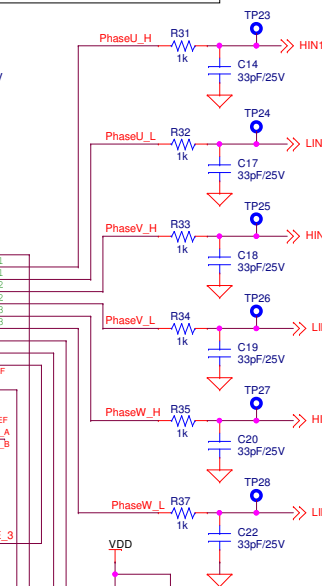
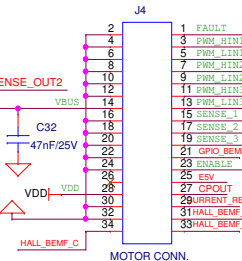
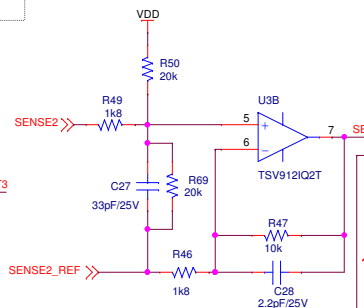
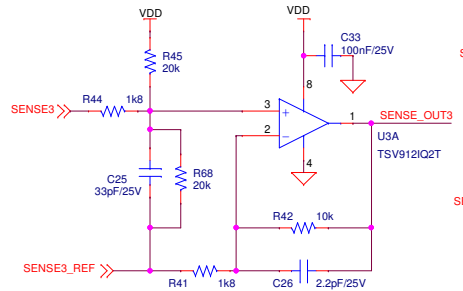
VCC\_E min 9V max 20V  
VDD\_E 3V3


# DEFAULT CONFIGURATION

F.O.C. 3 shunt  
VCC=15  
JP3 closed (VDD=3V3)



6step  
R45, R50, R55 D.N.M.



 life.augmented	Board Title EVALSTDRIVE601_R1_1				This drawing and the information herein are property of STMicroelectronics and are intended for evaluation purpose only
	Sheet Title CONNECTORS				
	Doc No	Date	Rev	Sheet	
		Tuesday, February 04, 2020	1.1	1.1.2	

