

System Software Developer System Software Developer Graduate with a comprehensive background in ASIC/Verification seeking to leverage knowledge into practical experience. San Jose, CA Graduate Electrical Engineering student, with a strong comprehensive and experiential background in ASIC Design/Verification/SoC/Physical Design/Computer Architecture. Seeking to leverage my academic knowledge of VLSI, RTL, Microprocessor and Circuit Designing into practical experience. Sponsorship required to work in the US Work Experience System Software Developer Tata Consultancy Services Ltd - Mumbai, Maharashtra October 2014 to June 2017 Conceptualized and designed applications using C programming. Communicated designs with team members and interacted with client to identify business process and implement optimal solutions. Performed modification and bug fixes. Education Master's in Electrical Engineering University of Cincinnati, College of Engineering and Applied Science - Cincinnati, OH August 2017 to May 2019 Bachelor's in Electronics and Telecommunication University of Mumbai - Mumbai, Maharashtra August 2011 to June 2014 Skills ASIC, SoC, UVM, Verilog, SystemVerilog, C, C++, Perl, Python, Synopsys Design Compiler, Synopsys VCS, TCL, CMOS Links <http://www.linkedin.com/in/sabah-parkar> Additional Information TECHNICAL SKILLS: Hardware Description Language Verilog, System Verilog, UVM, VHDL Scripting Language Python Scripting, Perl, TCL, MATLAB Programming Language C, C++, Embedded C, Assembly Language, JAVA EDA Tools: Synopsys Design Compiler, Synopsys VCS, TetraMax, HSpice, IRSIM, Altera Quartus Prime, Magic, Modelsim Online Course and Udemy Courses: Computer Architecture, Python for Beginners with Examples, Getting Started with Perl for Beginners, Hardware Modeling using Verilog Concepts: OOPs, Timing analysis, Clock Distribution, Clock Domain crossing, BIST, JTAG Relevant Courses Introduction to VLSI, VLSI Design Automation, VLSI Test and Low Power, Topics in VLSI CAD, Embedded System, Advanced Algorithm, Microelectronics, Digital Logic Design, Digital Signal Processing, Microprocessor and Microcontroller, Wireless Communication

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