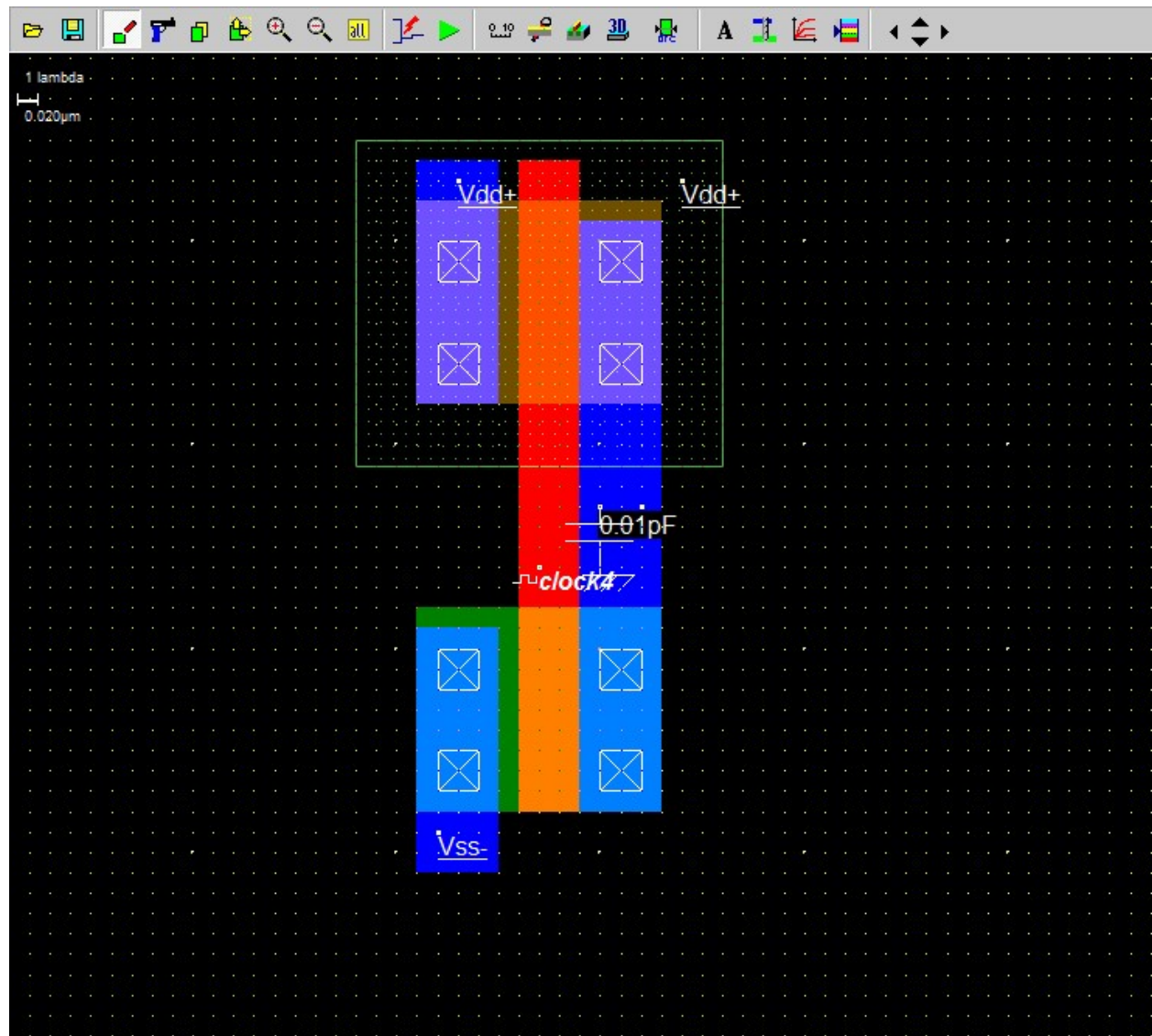
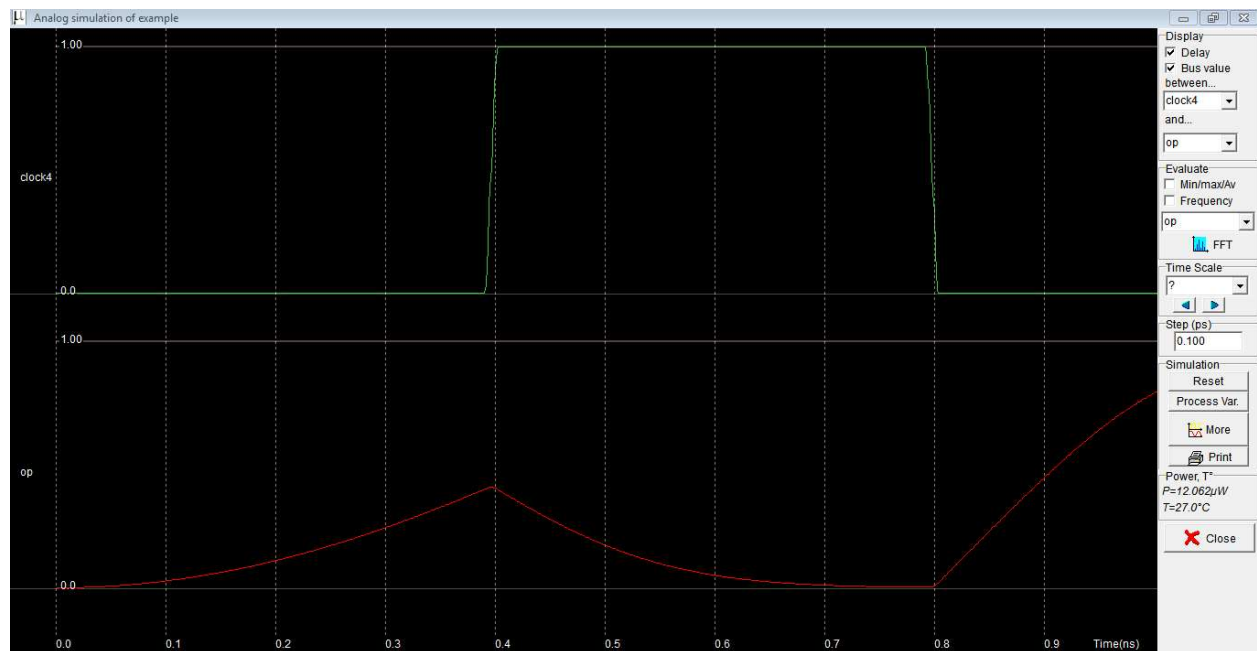


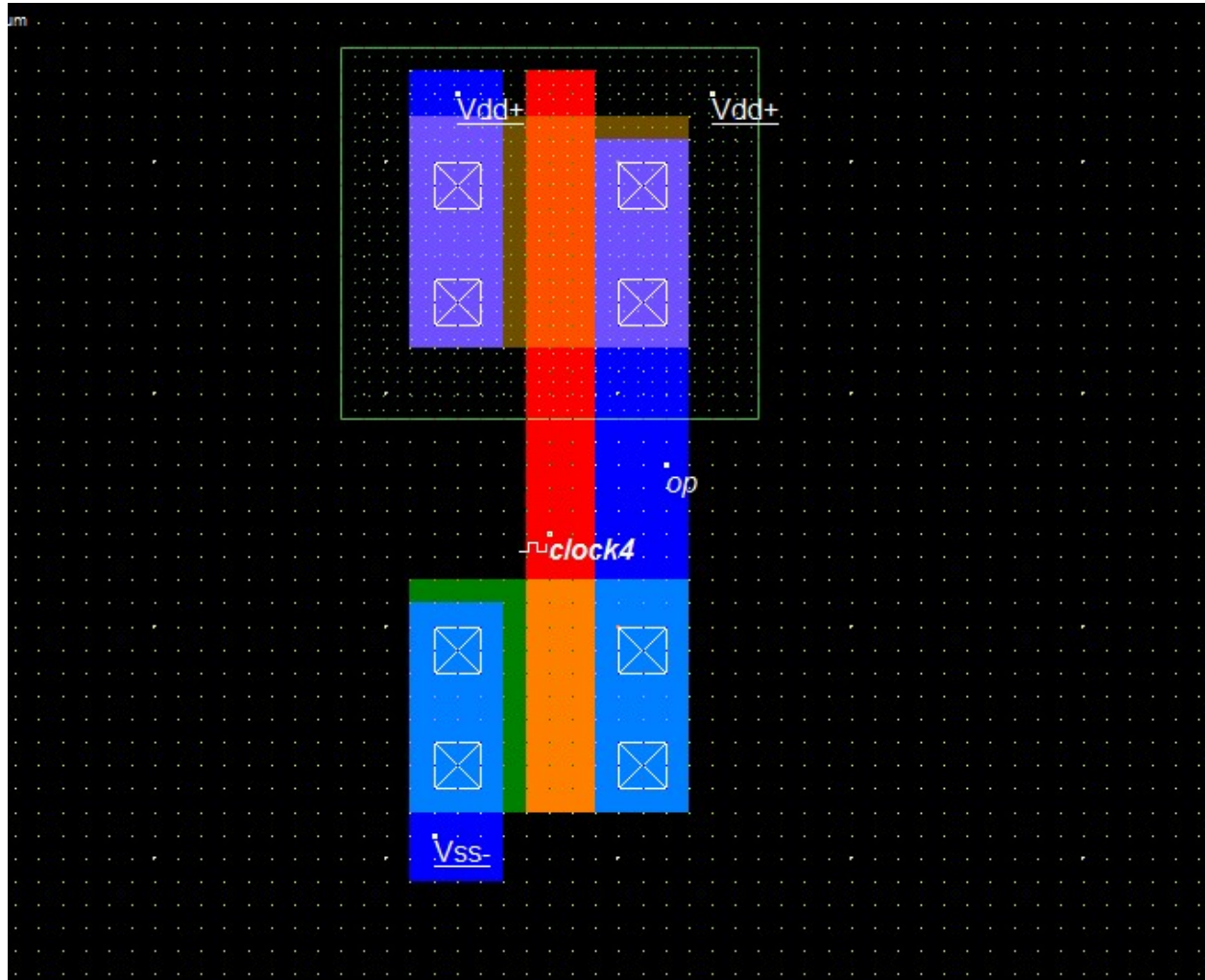
Inverter using P-MOS & N-MOS

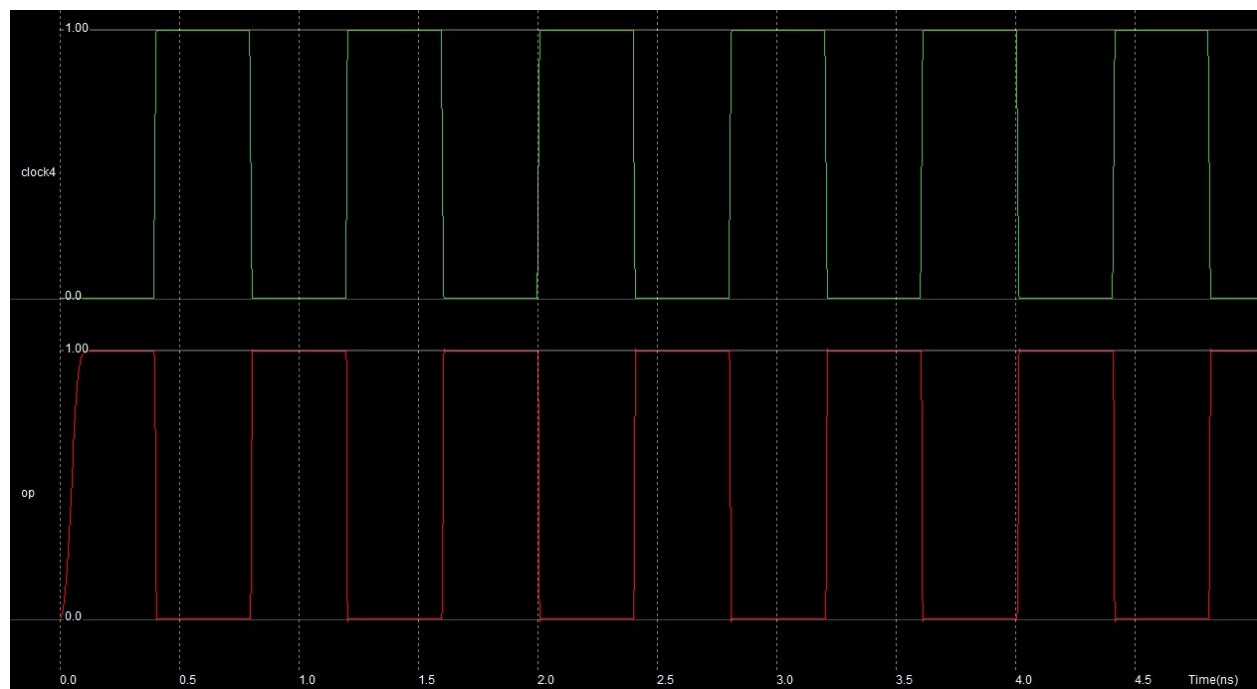
With capacitor:-





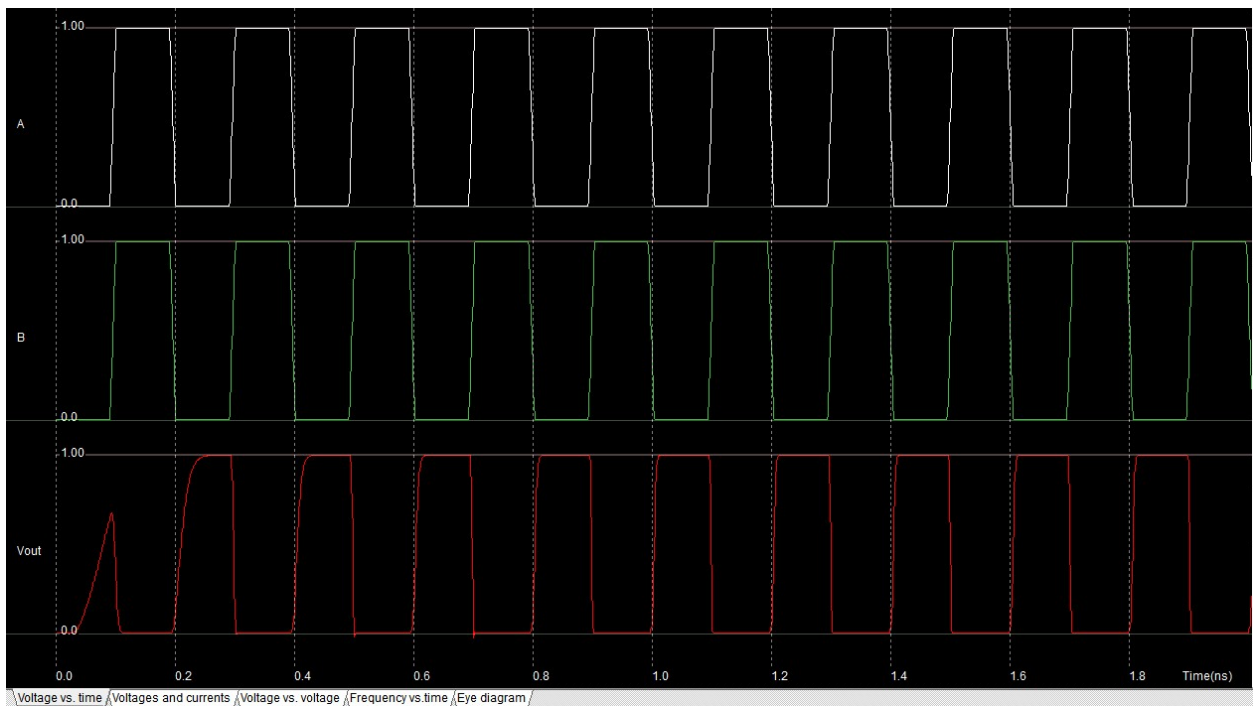
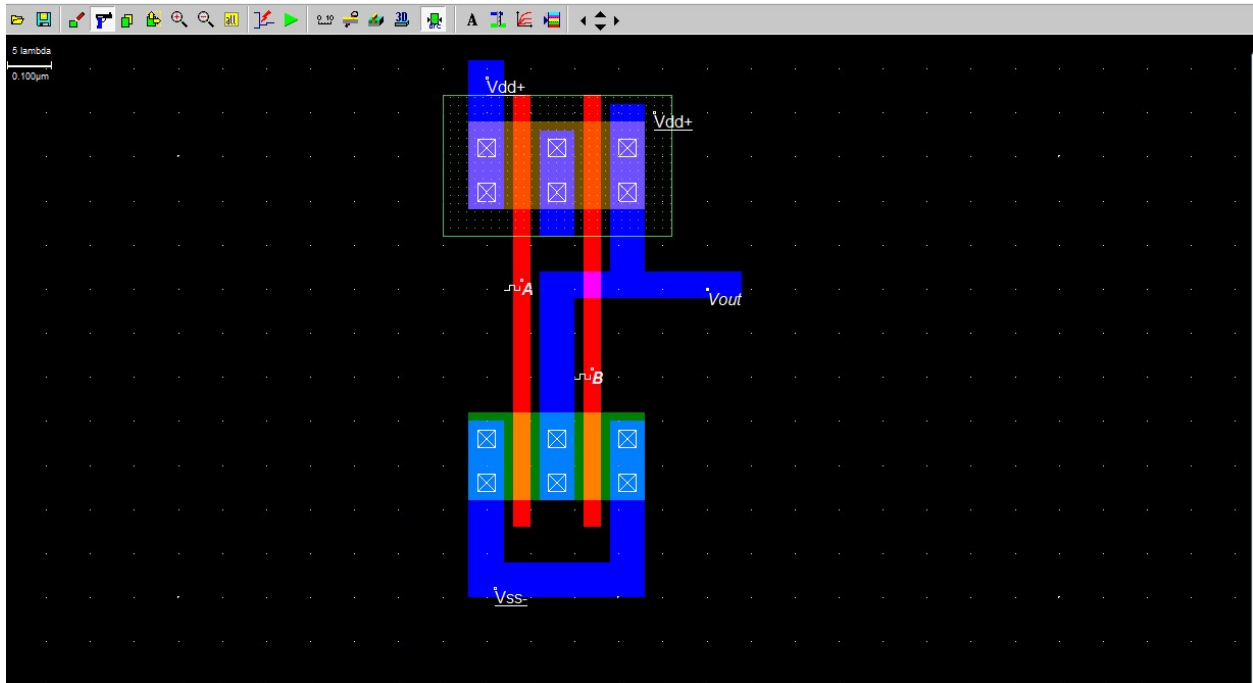
Without Capacitor



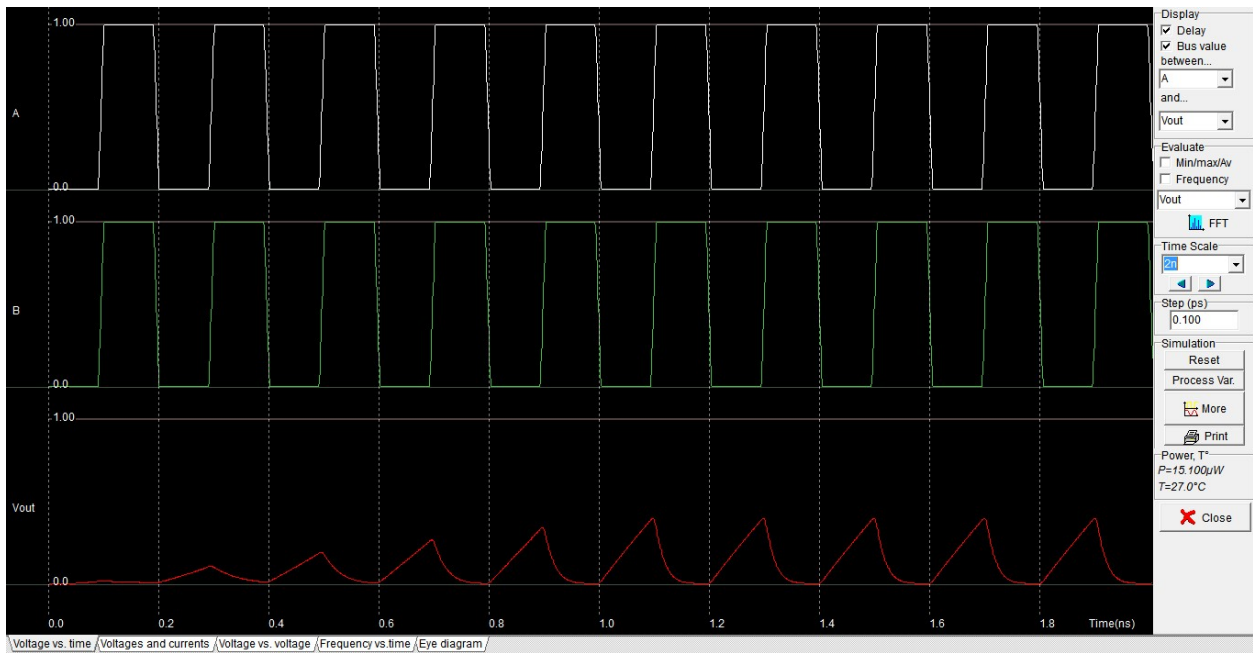
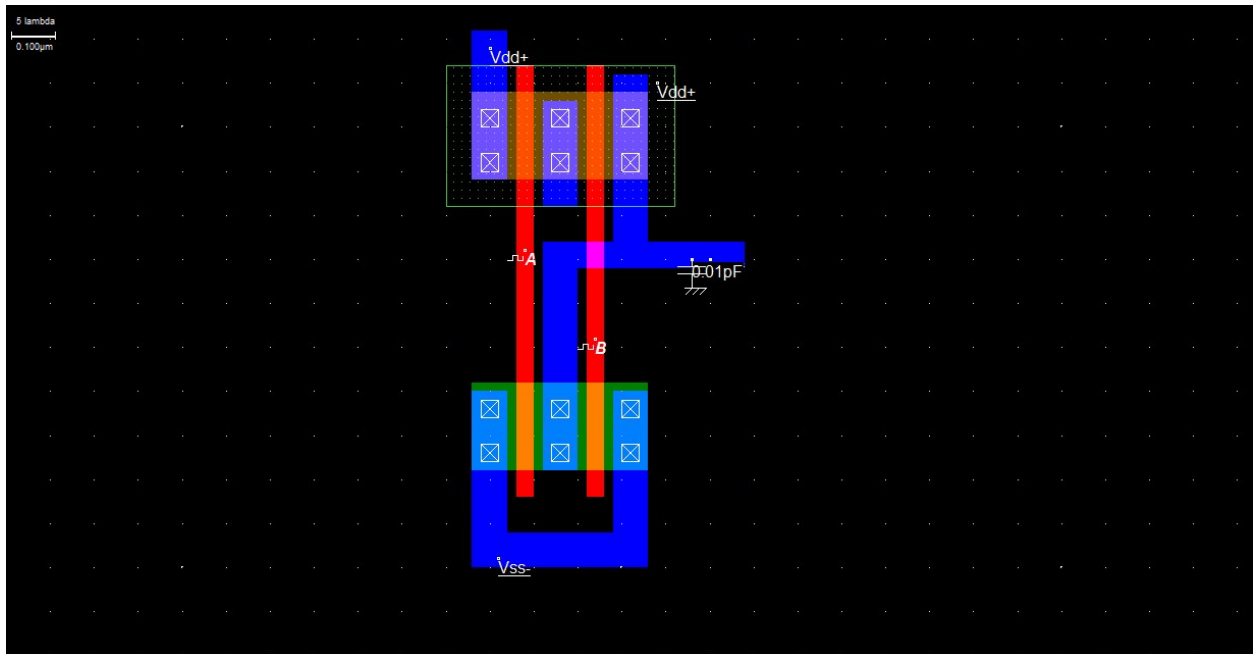


CMOS using NOR Gate

Without capacitor

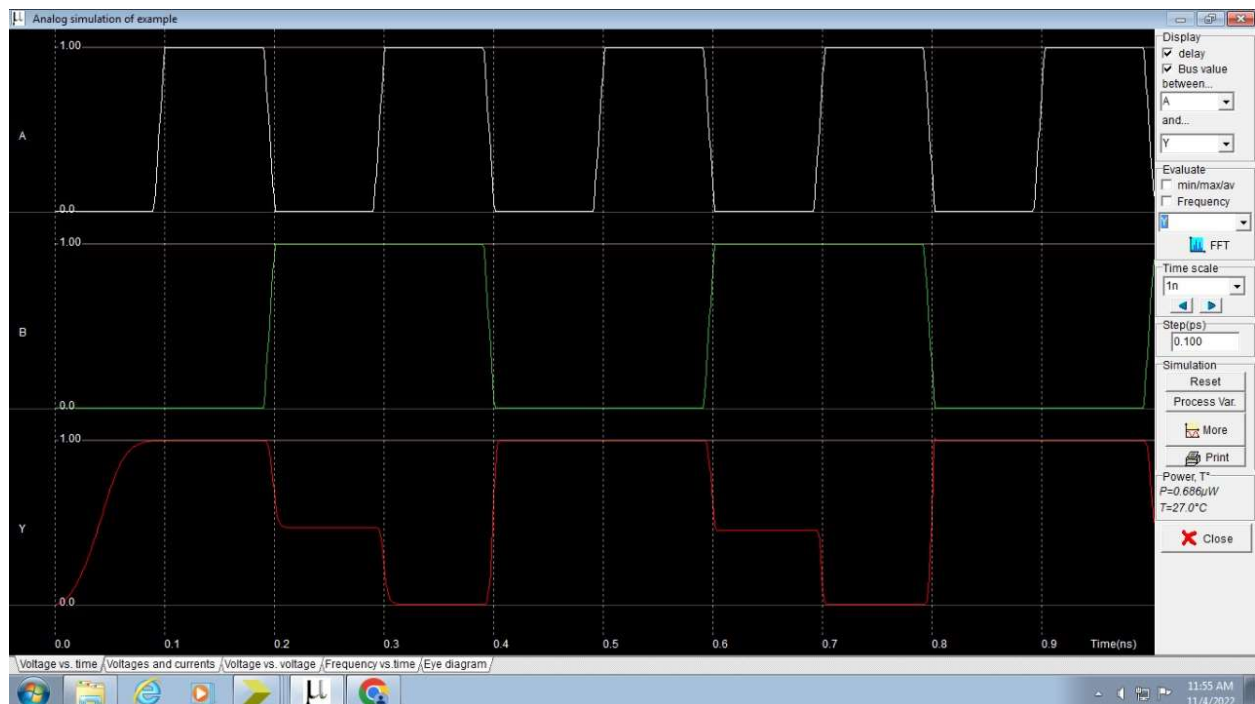
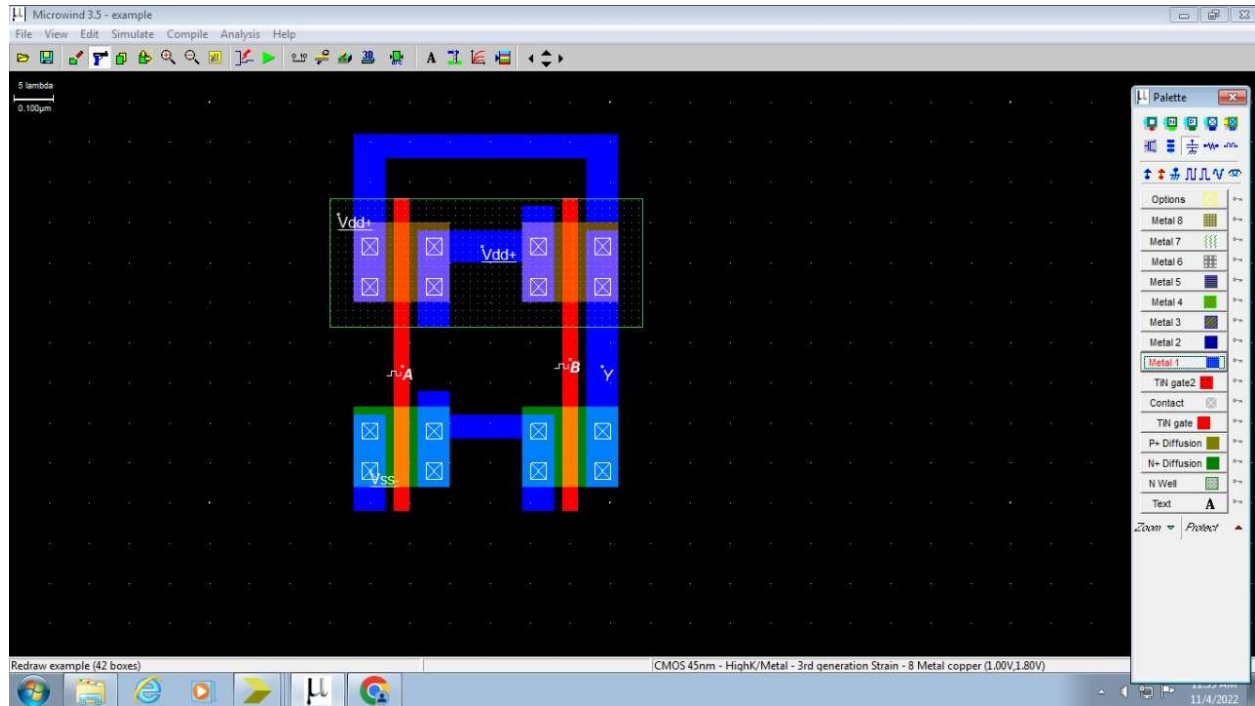


With Capacitor

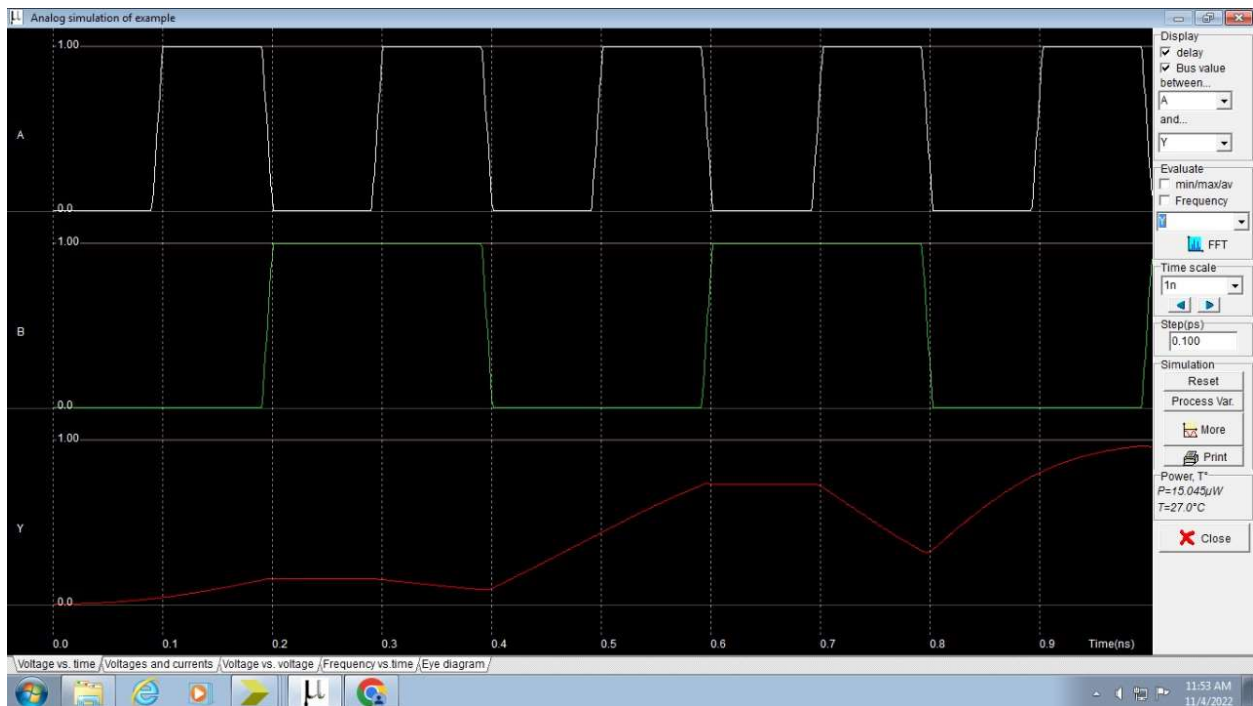
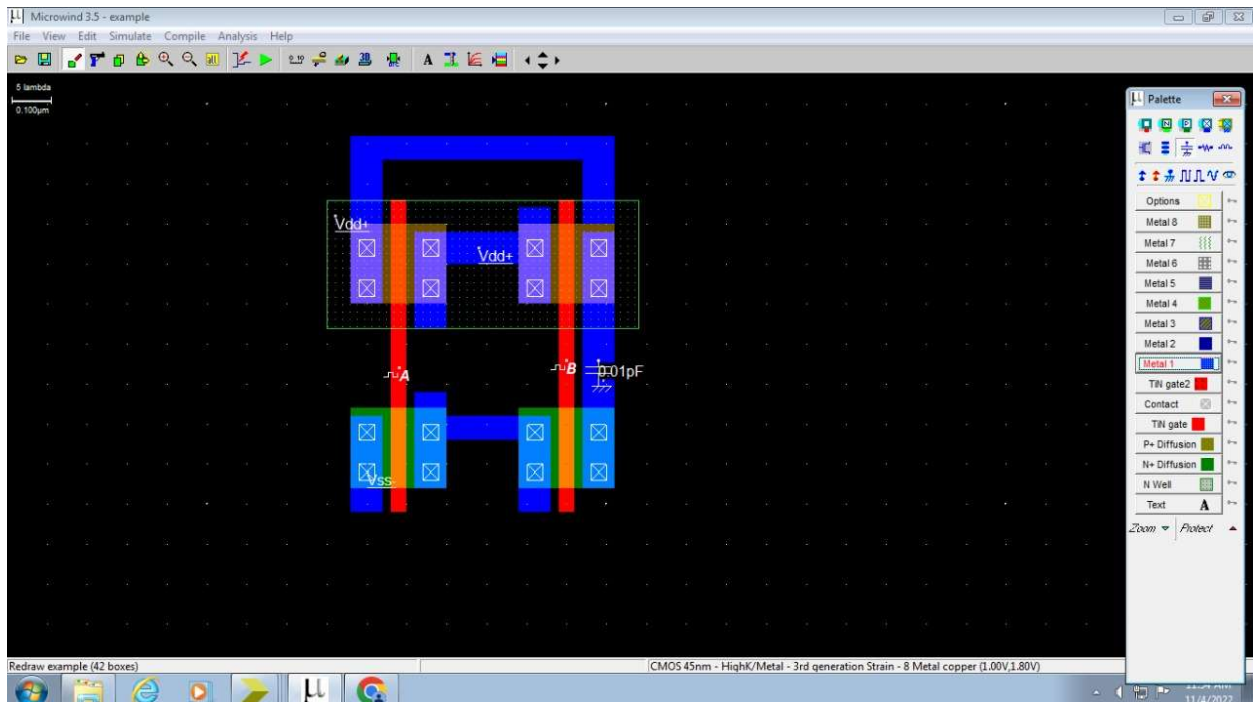


NAND Gate:-

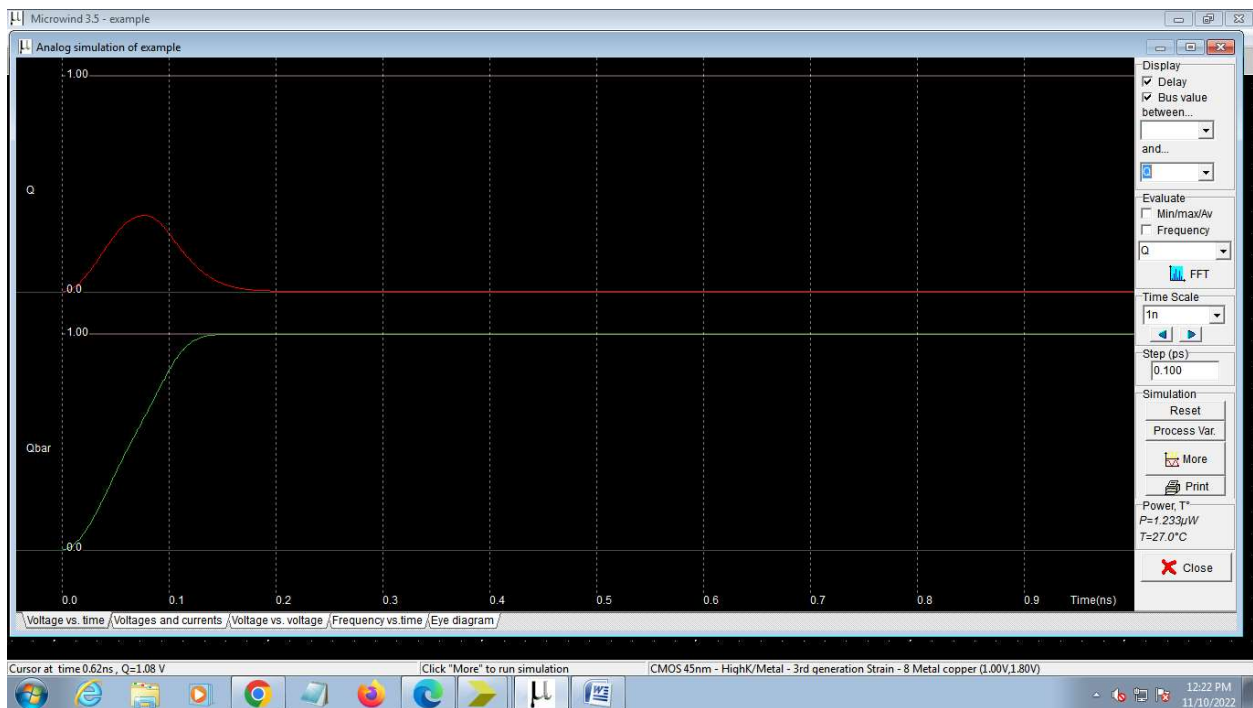
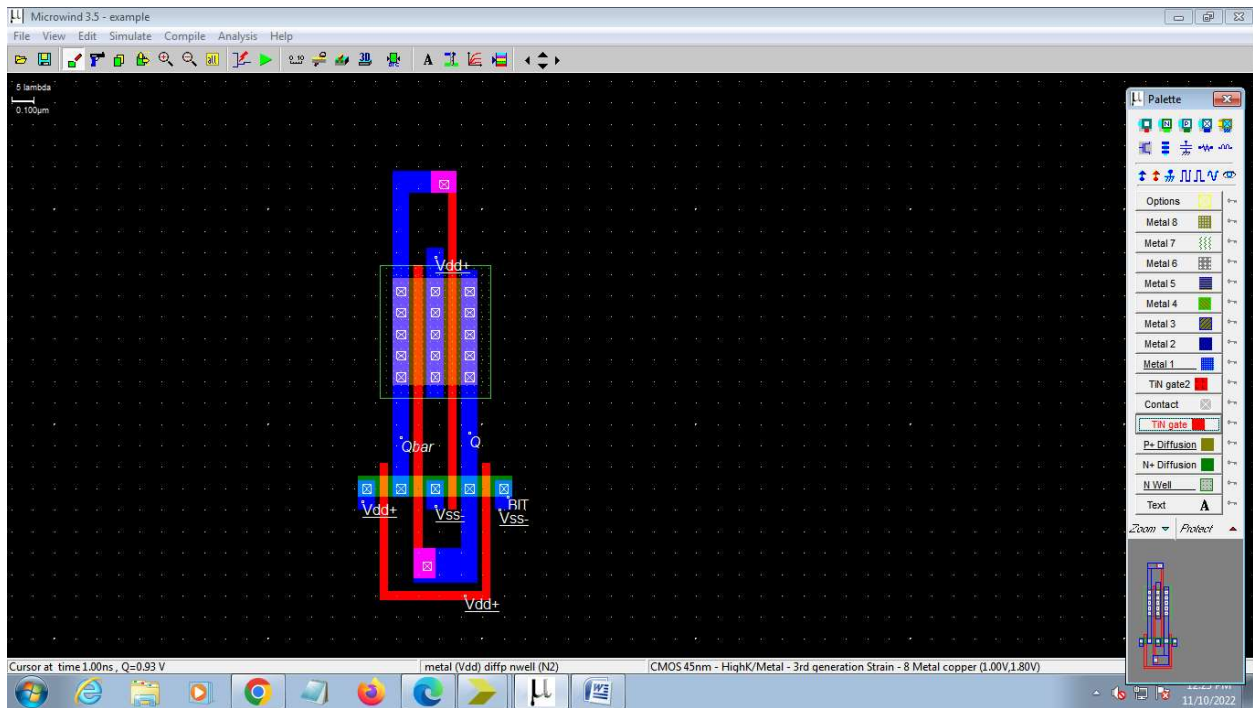
Without Capacitor

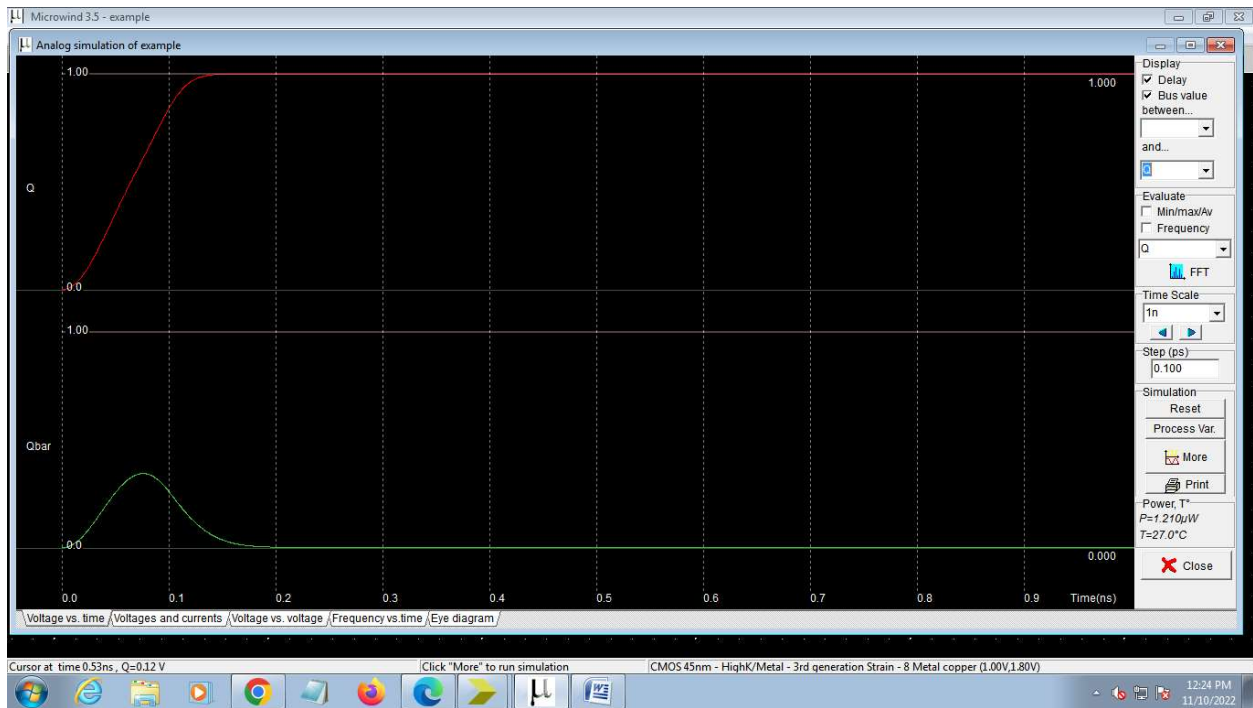
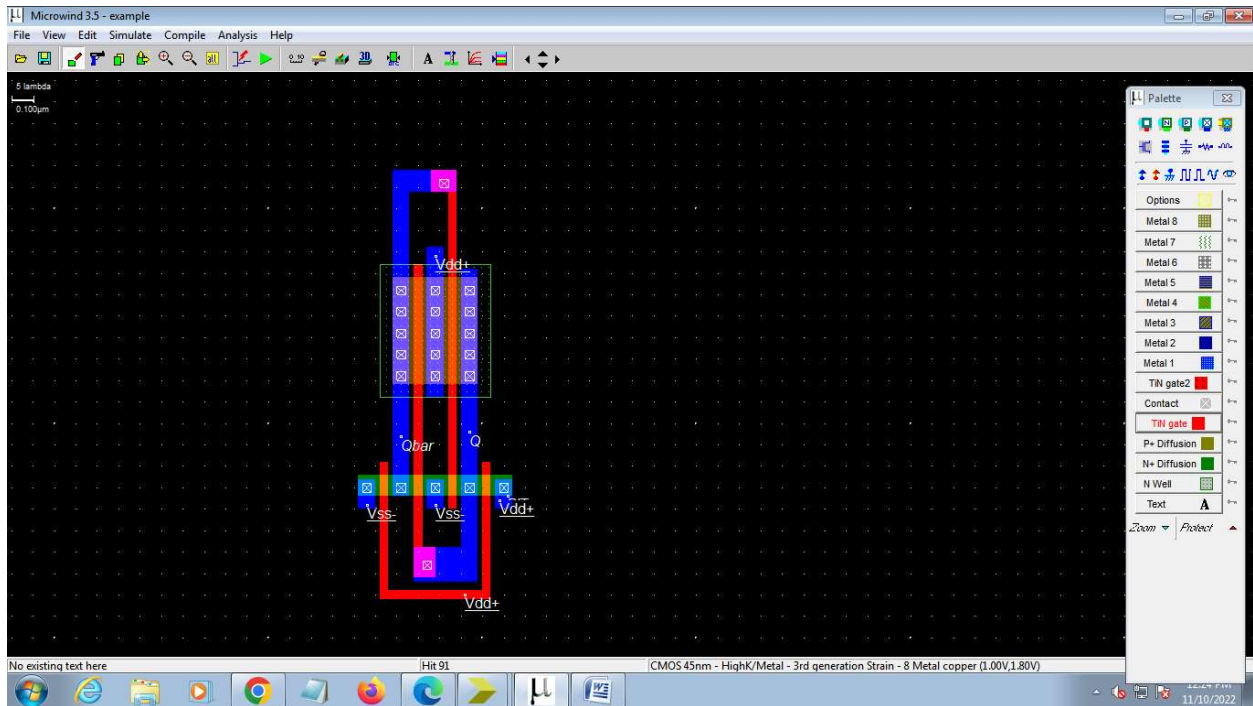


With Capacitor



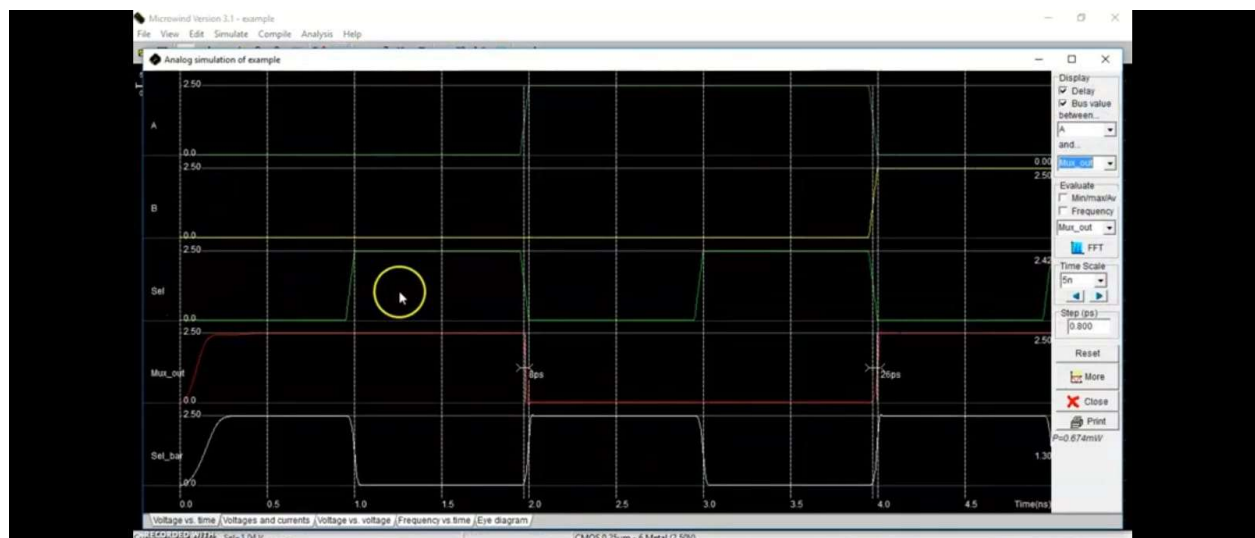
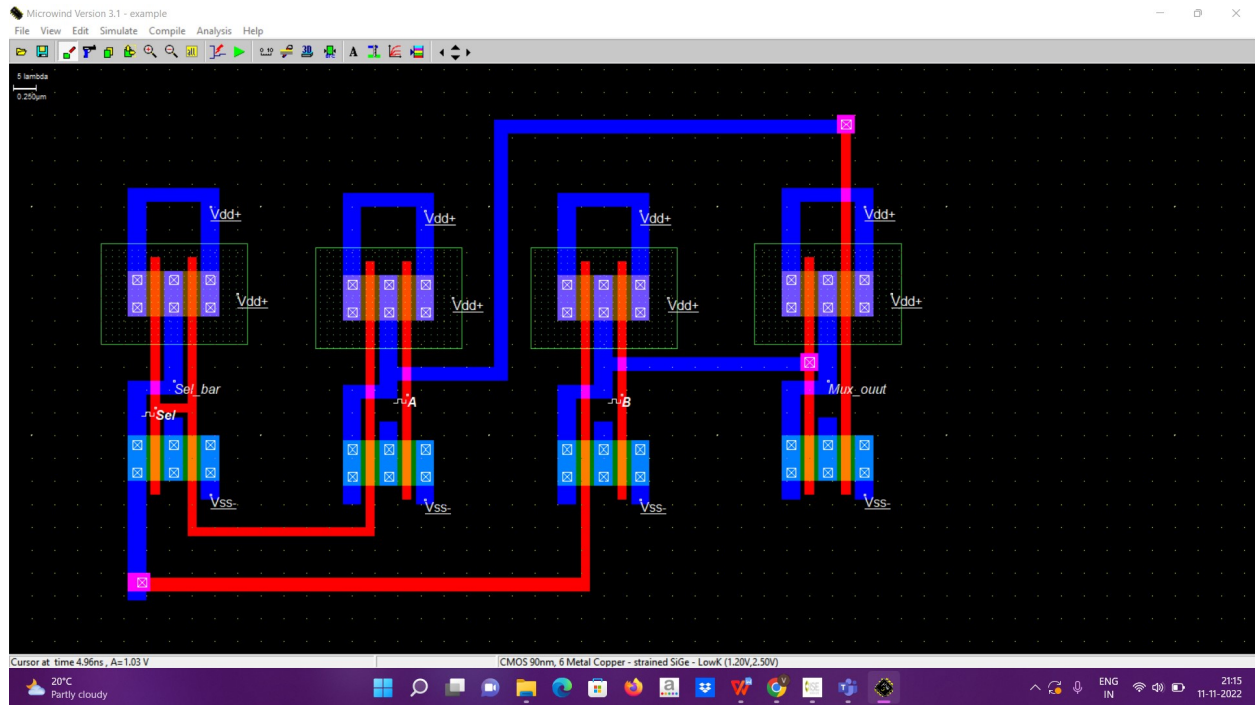
SRAM :-





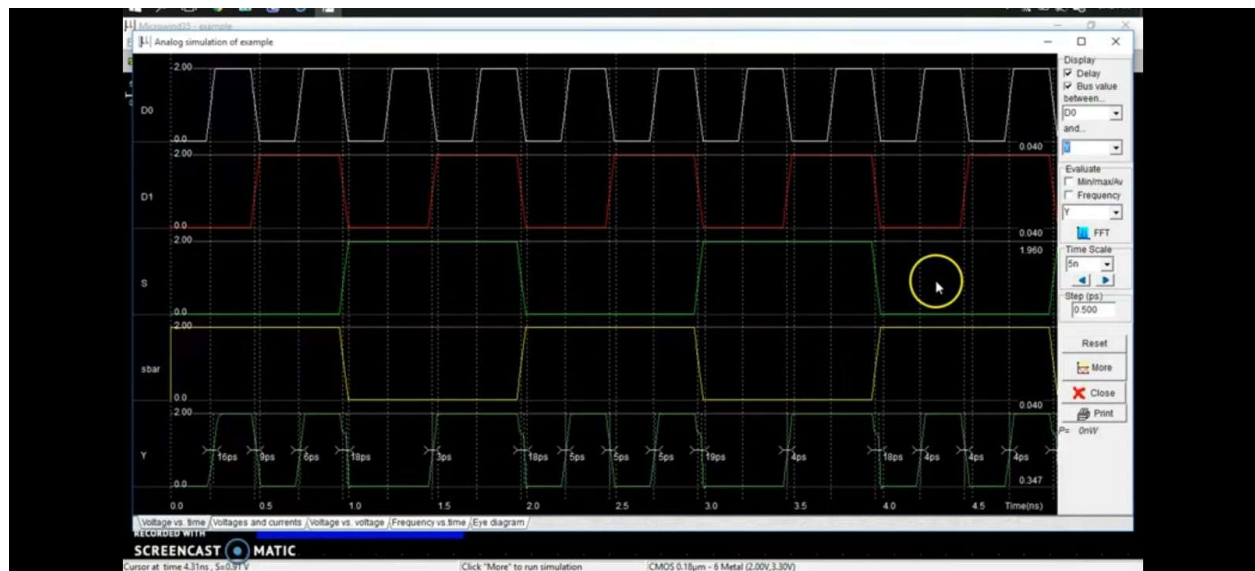
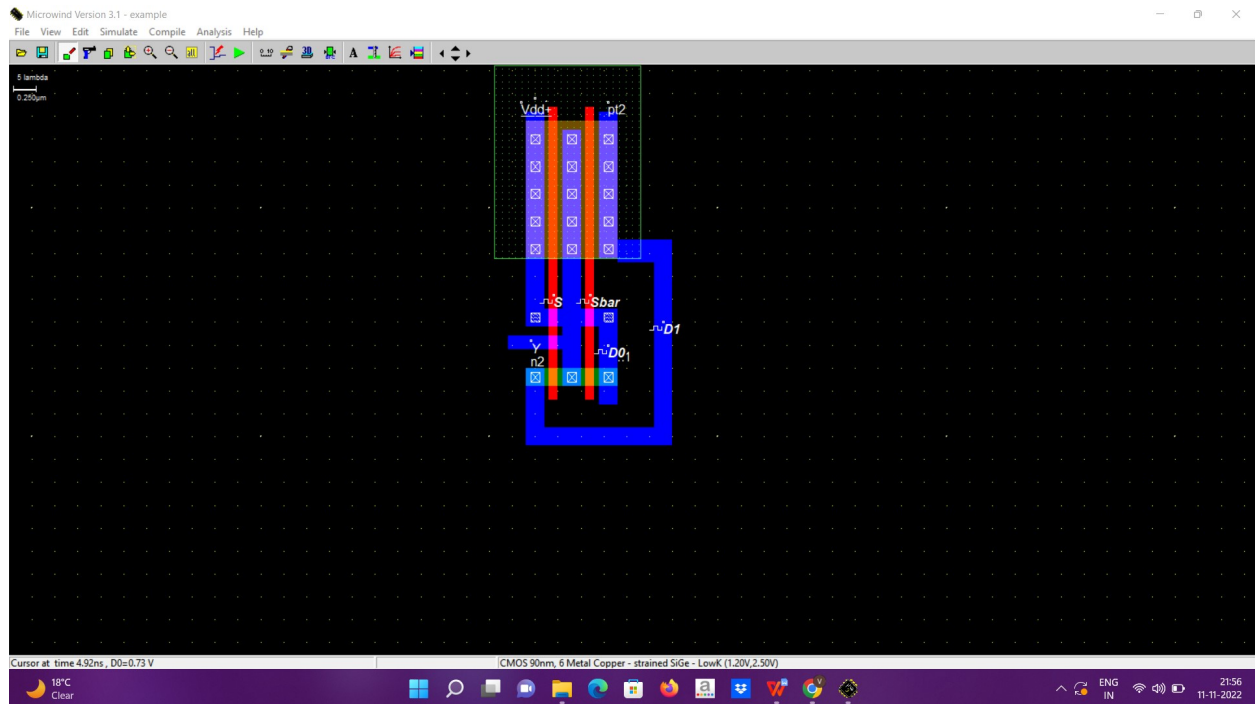
2:1 Mux

Using NAND Gate



Using Transmission Gate

Without capacitor



With capacitor

