



UNIT TEST I - MICROCONTROLLERS (2021-22)

Day: Thursday

Date:14/10/2021

Time 3PM - 4PM

...

* Required

CO3

Identify various blocks and components of internal architecture of PIC 18F4550

25. PIC 18 is having ----- bit PC *

(1 Point)

☐ 16☐ 8☒ 21☐ 12

26. PIC18F4550 is having----- of RAM space

*

(1 Point)

☒ 4KB☐ 8KB☐ 2KB☐ 16KB

27. Which port in PIC18F4550 has interrupt related functionality *

(1 Point)

☐ Port E☒ Port B

☐ Port A

☐ Port C

28. With 4096 byte ,the file register has an addresses of ----- *

(1 Point)

☐ 00H --- FFH

☒ 000H --- FFFH

☐ 00H --- 7FH

☐ 000000 --- 1FFFFFF

29. In PIC18F4550 , SP is of size ----- *

(1 Point)

☐ 21 BIT

☐ 5 BIT

☒ 8 BIT

☐ 16 BIT

30. How many level of stack exist in PIC18F4550? *

(1 Point)

☐ 21

☐ 31

☒ 16

☐ 8

31. Address range of program memory in PIC18F4550 is *

(1 Point)

- ☒ 000000 -- 1FFFFFFH
- ☐ 000000 --- FFFFFFFH
- ☐ 000000 --- 007FFFH
- ☐ 000000 ---- 00FFFFH

32. Which is the default data bank in PIC18F4550? *

(1 Point)

- ☐ Bank 1
- ☒ Access BANK
- ☐ BSR
- ☐ Bank 0

33. Power up TIMER(PWRT) in PIC18F4550 is ---- bit counter *

(1 Point)

- ☐ 10
- ☐ 11
- ☐ 21
- ☒ 16

34. Internal RC (INTRC) oscillator works on ---- *

(1 Point)

- ☐ 4MHZ
- ☐ 48MHZ
- ☐ 31KHZ
- ☒ 32KHZ

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