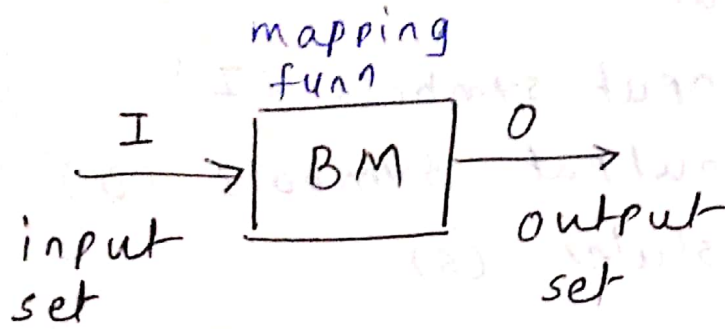


Basic machine



- ① most primitive machine
- ② concerned with I and O only
- ③ behaves like a function that maps input set I to output set O
- ④ It has only machine function (MAF)
no state function (STF) \times

e.g. Logic Gate OR

MAF $I \times S \rightarrow O$

$I \backslash O$	output
$(0,0)$	0
$(0,1)$	1
$(1,0)$	1
$(1,1)$	1

$$M = (I, O)$$

$$I = \{(0,0), (0,1), (1,0), (1,1)\}$$

ie set of ordered pair of 0 or 1.

$$O = \{1, 0\}$$

FSM

Design FSM for testing divisibility by 3 for decimal numbers.

step 1: Defⁿ of fsm

FSM is a machine which consists of finite set of states (S) that alters on receiving the input set of symbols (I) to produce output set of symbols (O).

Fsm defines two functions.

- ① state function (STF) : $S \times I \rightarrow S$
- ② machine function (MAF) : $S \times I \rightarrow O$

step 2: Logic

- ① input is a decimal number. so we define $I = \{0, 1, 2, 3, 4, 5, 6, 7, 8, 9\}$
 - ② The machine checks for divisibility by 3. $O = \{Y, N\}$
 - ③ We define one initial/start state $q_s = \text{initial/start state.}$
 - ④ Whenever the number is divided by 3 there are 3 remainder possibilities.
 $q_0 - 0 \text{ remainder}$
 $q_1 - 1 \text{ remainder}$
 $q_2 - 2 \text{ remainder}$
- $S = \{q_s, q_0, q_1, q_2\}$
 initial state \rightarrow final with state 01PY

Step 3 : Implementation - Transition Table

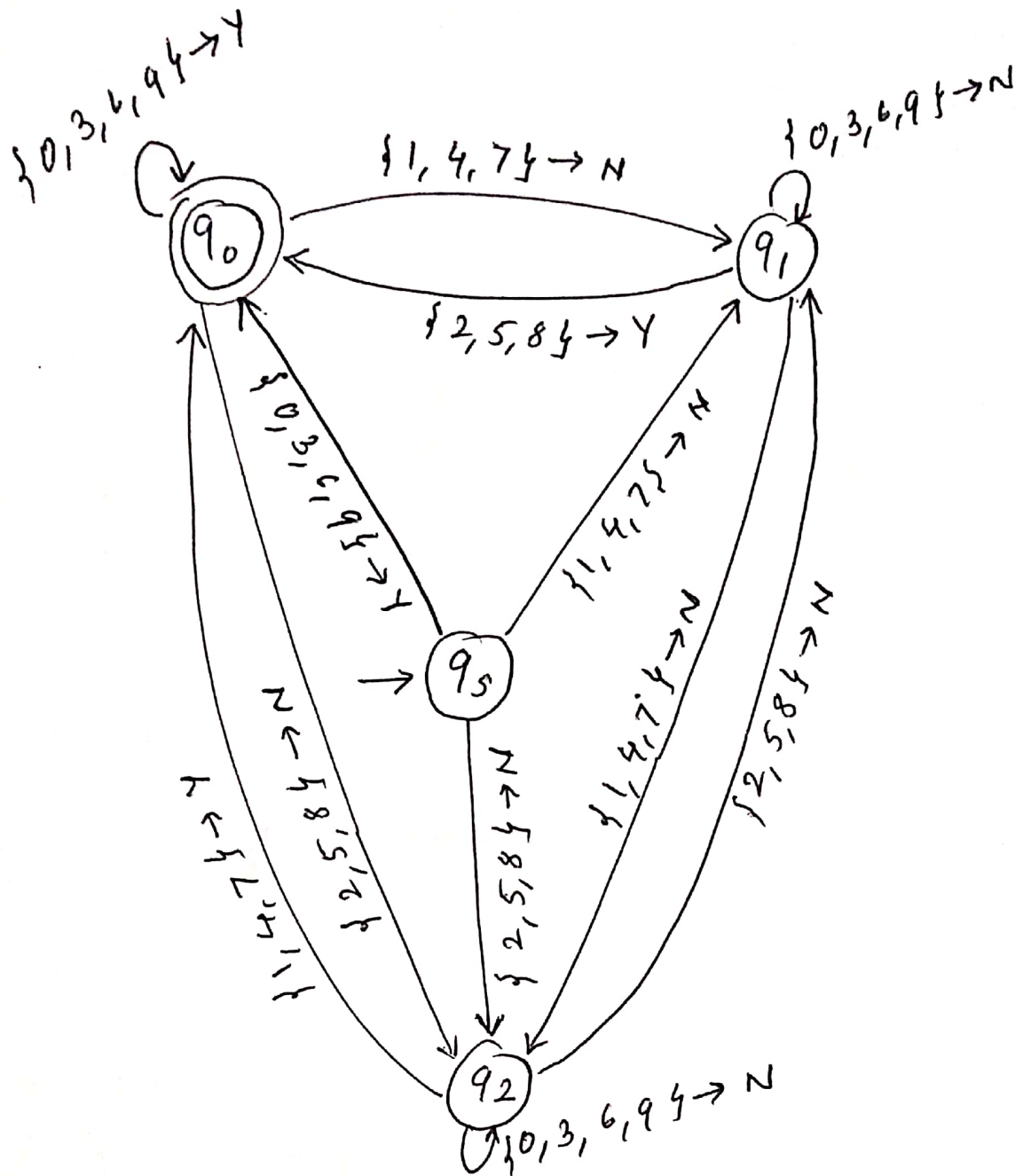
MAF: $S \times I \rightarrow O$

S \ I			
	{0,3,6,9}	{1,4,7}	{2,5,8}
q ₅	Y	N	N
q ₀	Y	N	N
q ₁	N	N	Y
q ₂	N	Y	N

STF: $S \times I \rightarrow S$

S \ I			
	{0,3,6,9}	{1,4,7}	{2,5,8}
q ₅	q ₀	q ₁	q ₂
q ₀	q ₀	q ₁	q ₂
q ₁	q ₁	q ₂	q ₀
q ₂	q ₂	q ₀	q ₁

transition diagram



step 5 : - FSM = $\{I, O, S\}$

where

$I = \{0, 1, 2, 3, 4, 5, 6, 7, 8, 9\}$

$O = \{Y, N\}$

$S = \{q_0, q_1, q_2, q_5\}$

δ : as per transition table/diagram

Finite state machine (FSM)

FSM consists of

set of input symbols (I)

set of output symbols (O)

set of states (S).

It defines two functions

$$MAF = I \times S \rightarrow O$$

$$STF = I \times S \rightarrow S$$

* Design FSM for binary numbers addition

$$\Sigma \text{ or } I = \{0, 1\}$$

step 1 = FSM defn

step 2 = logic

$$\textcircled{1} I \text{ or } \Sigma = \{0, 1\}$$

$$\textcircled{2} Q = \{s_1, s_2\}$$

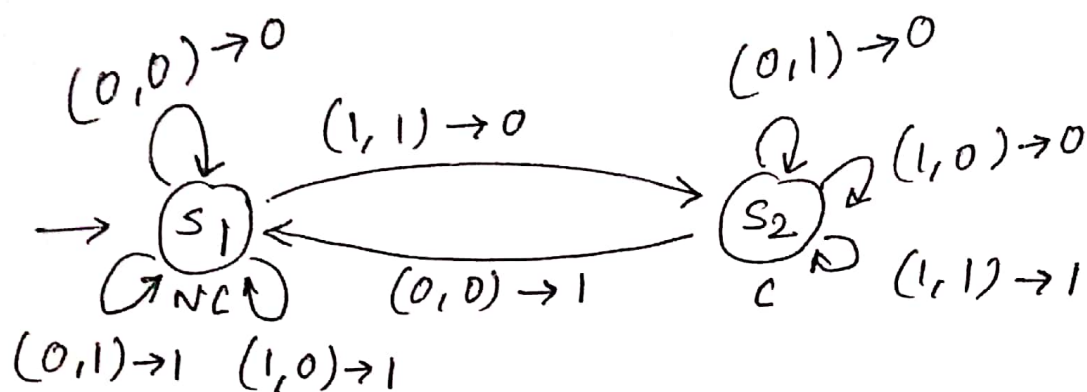
$$\textcircled{3} O = \{0, 1\}$$

$$\textcircled{4} \text{ initial state} \\ = s_1 \text{ (no carry)}$$

	s_1 NC sum	s_2 C carry
$0 + 0 =$	0	0
$0 + 1 =$	1	0
$1 + 0 =$	1	0
$1 + 1 =$	0	1

Step 3 - Design

Transition diagram



MAF

I \ O		
	NC S ₁	C S ₂
(0,0)	0	1
(0,1)	1	0
(1,0)	1	0
(1,1)	0	1

$$I \times S \rightarrow O$$

$$\Sigma \times Q \rightarrow \Sigma$$

STF

I		
	NC S ₁	C S ₂
(0,0)	S ₁	S ₁
(0,1)	S ₁	S ₂
(1,0)	S ₁	S ₂
(1,1)	S ₂	S ₂

$$I \times S \rightarrow S$$

$$\Sigma \times Q \rightarrow Q$$