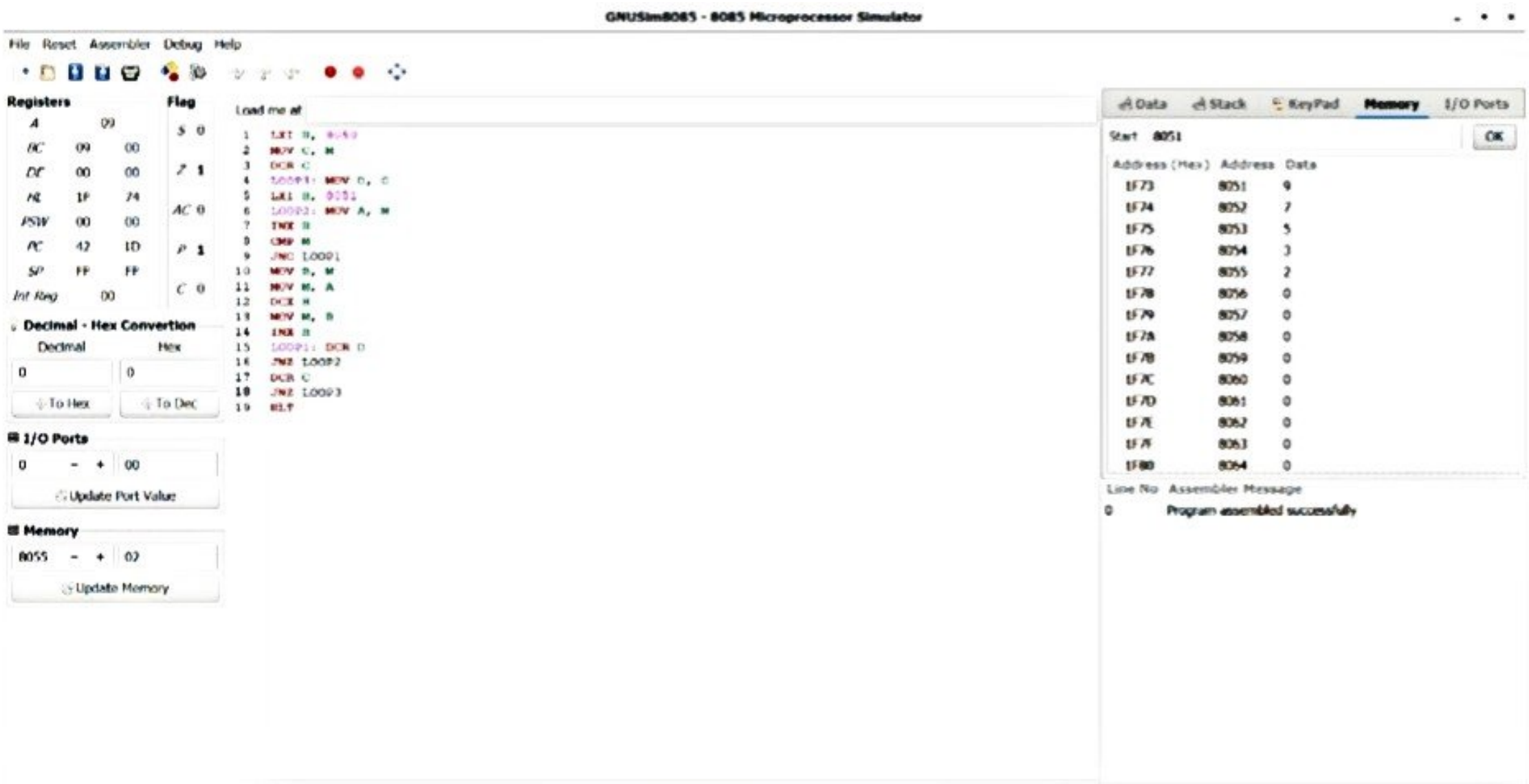
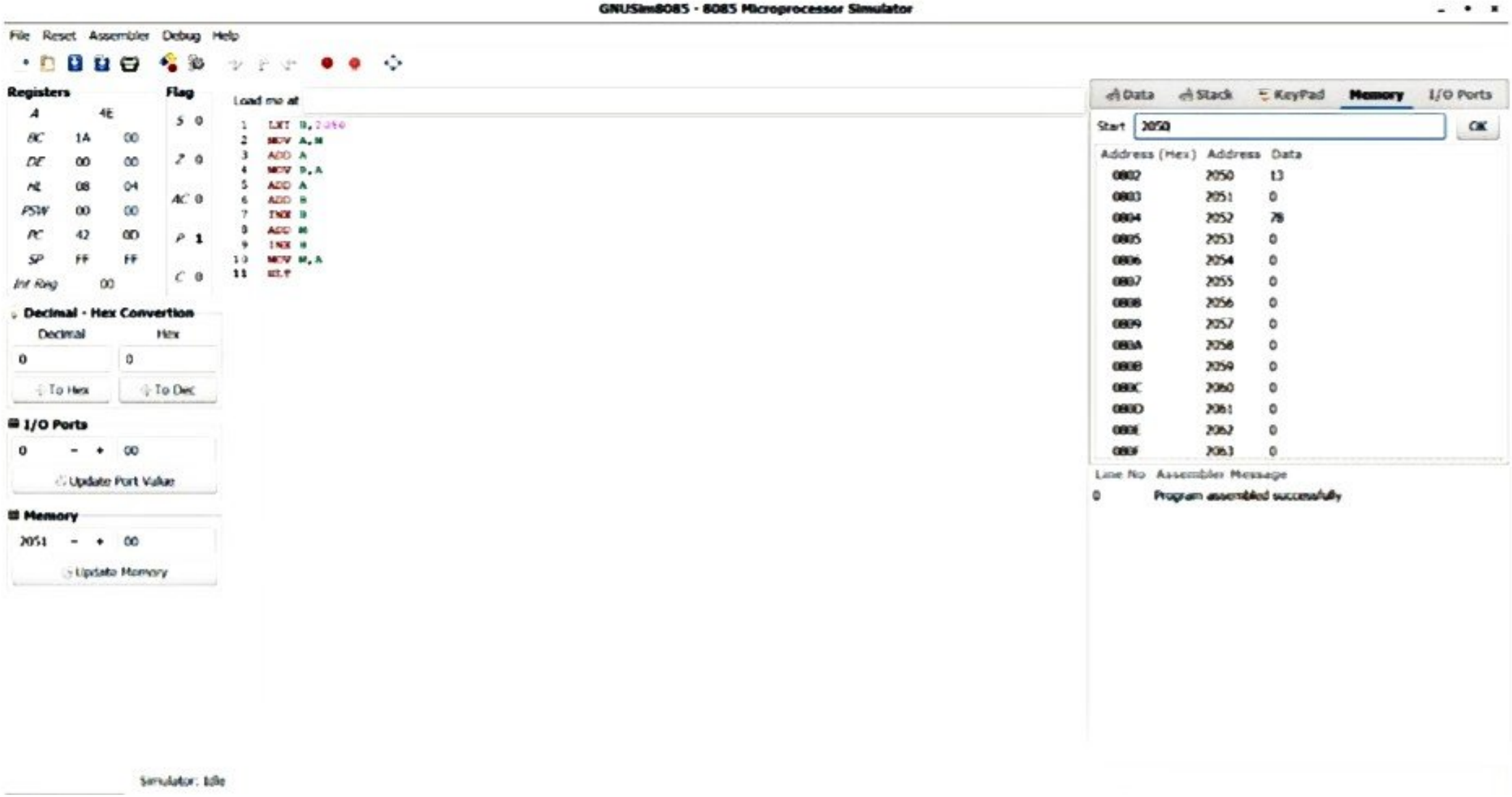
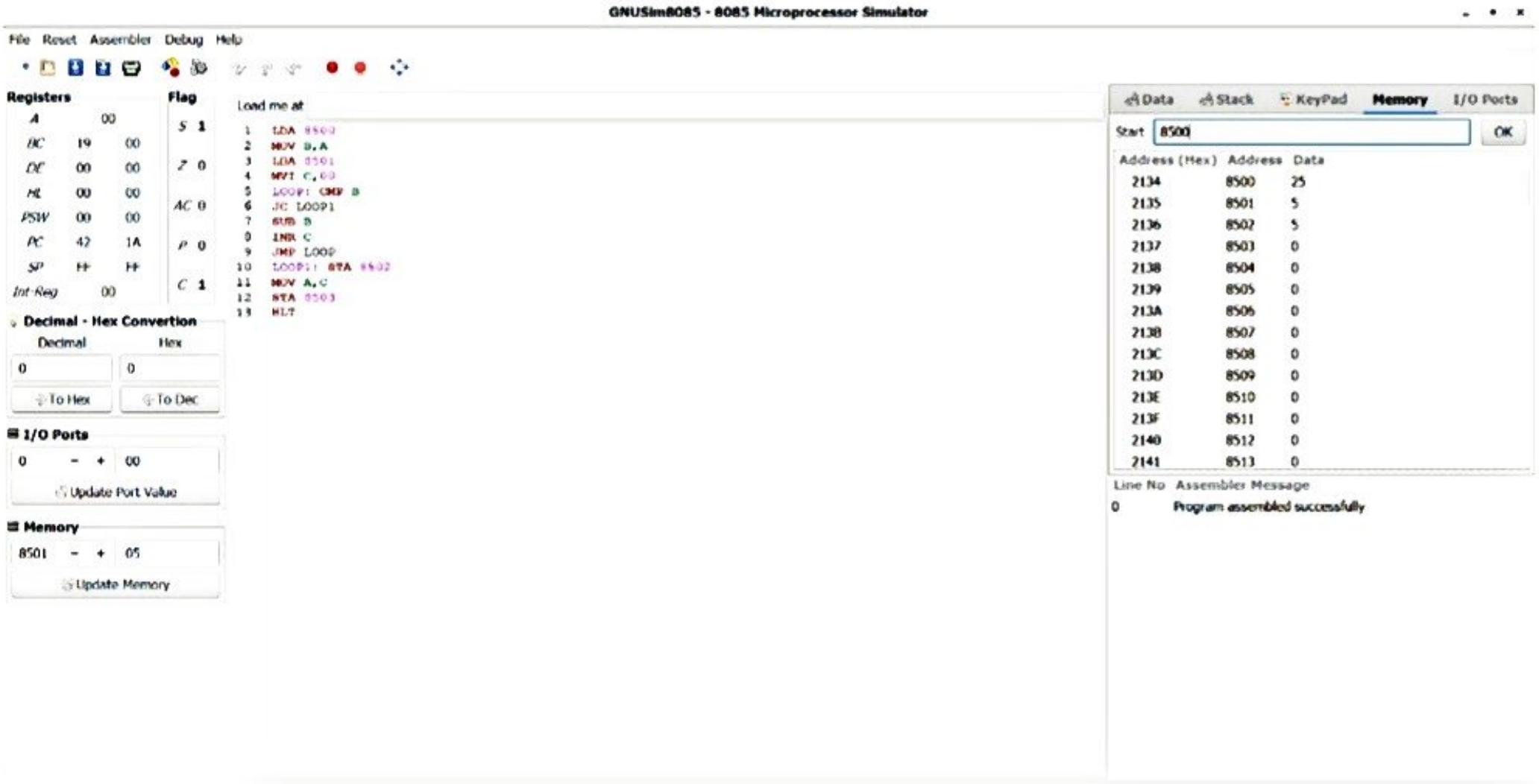
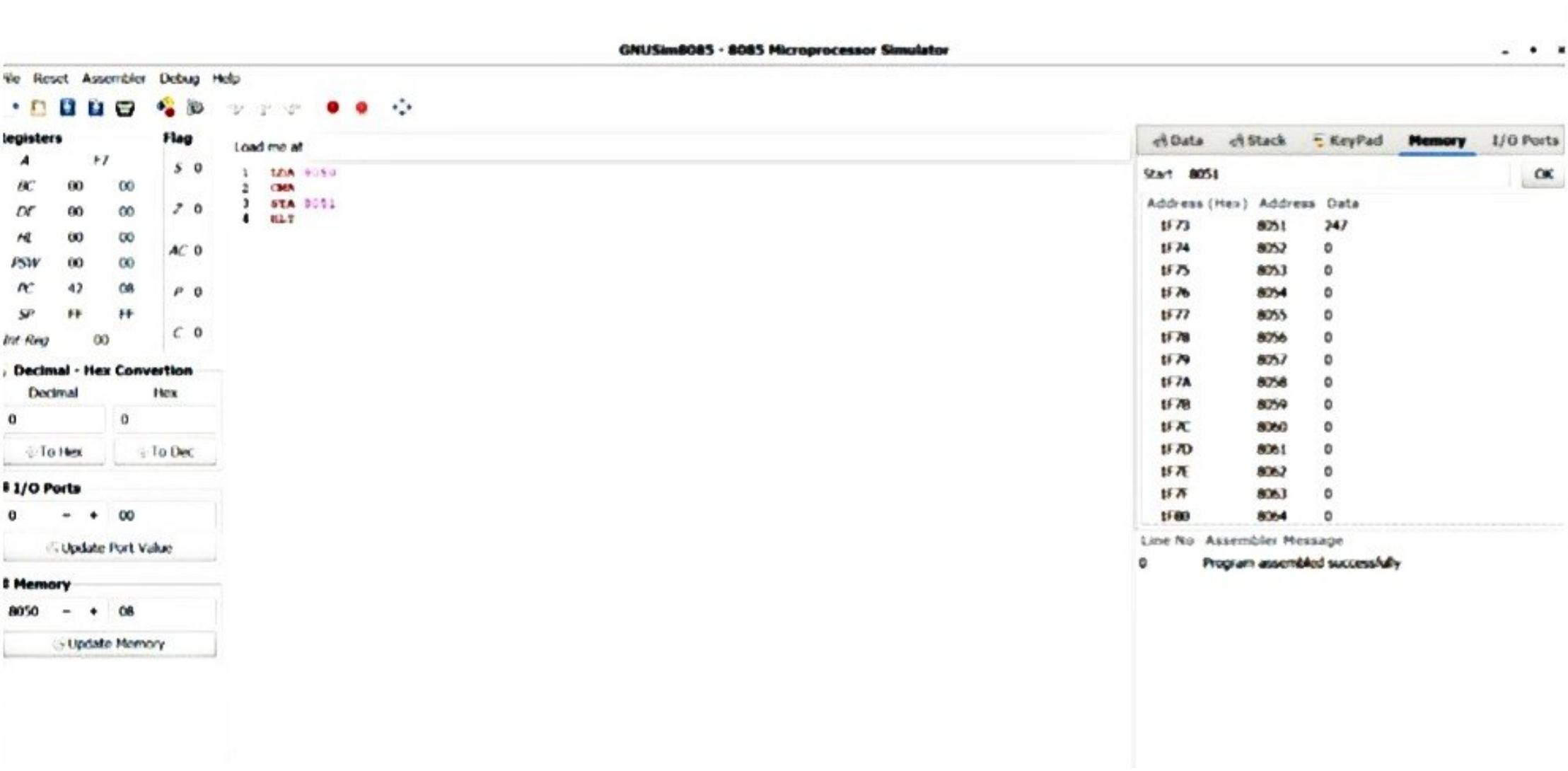
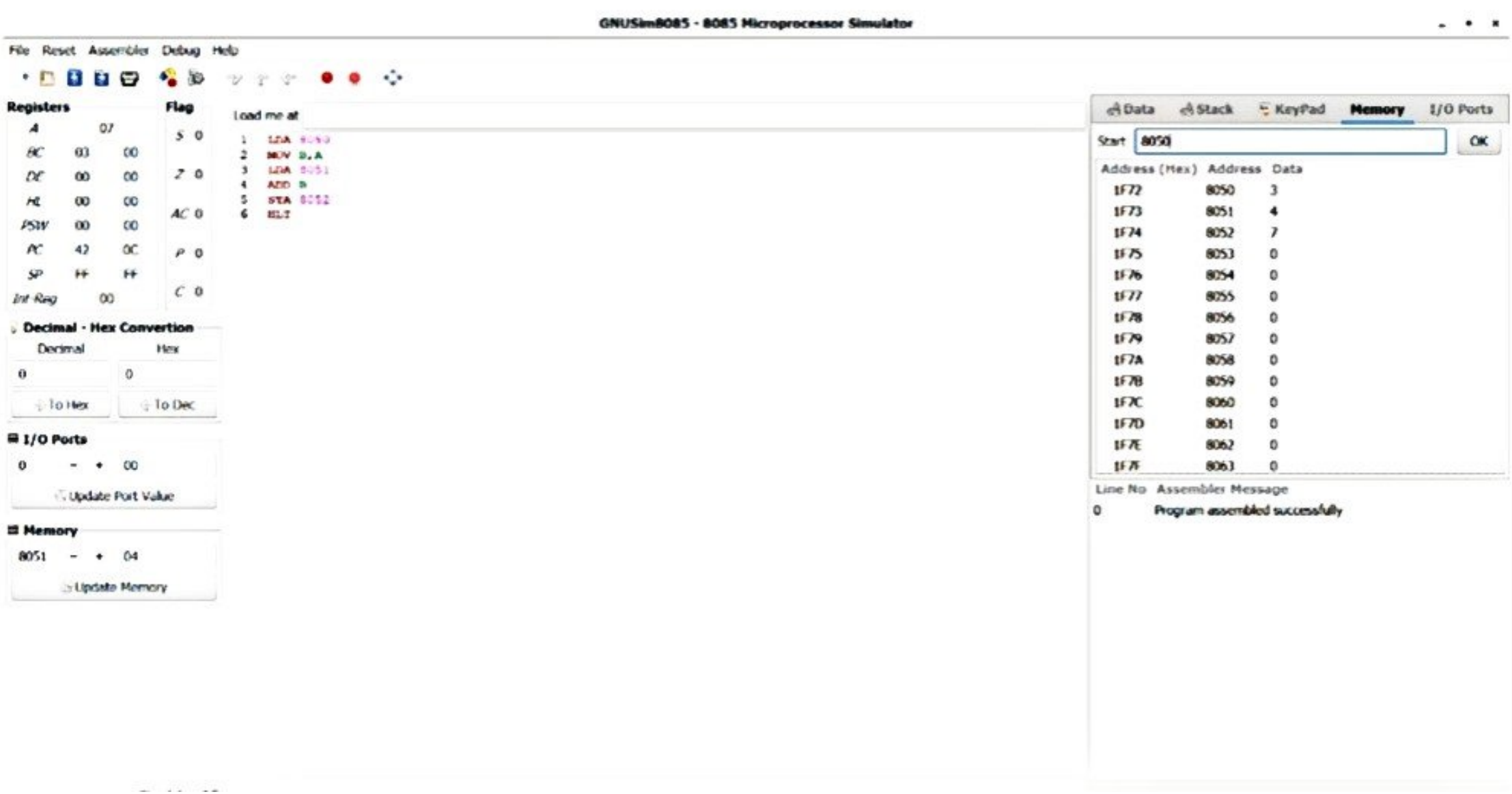
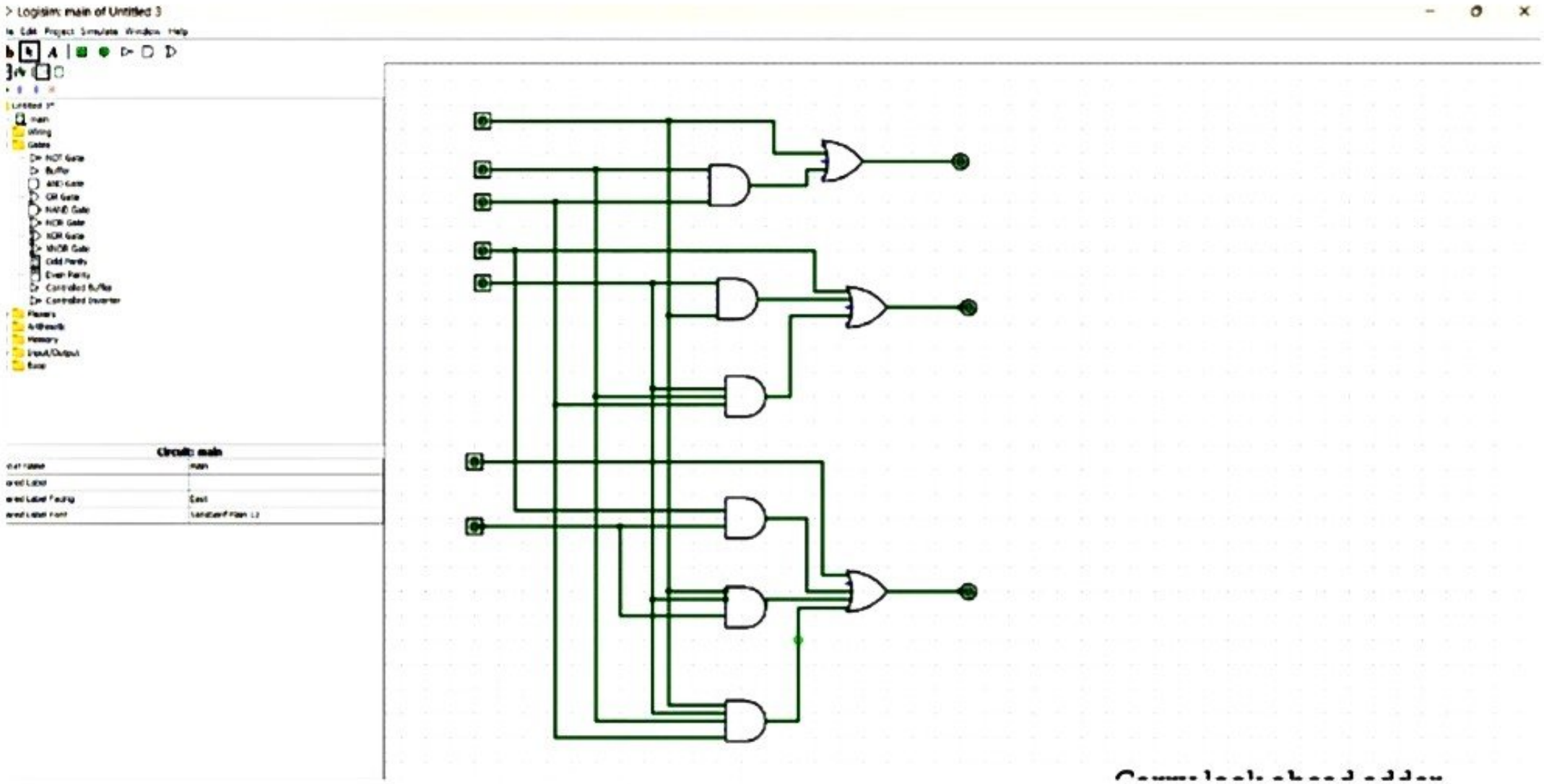

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
Load me at
Data Stack KeyPad Memory I/O Ports
Start 8051
OK
Address (Hex) Address Data
Line No Assembler Message
0 Program assembled successfully
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
Update Memory

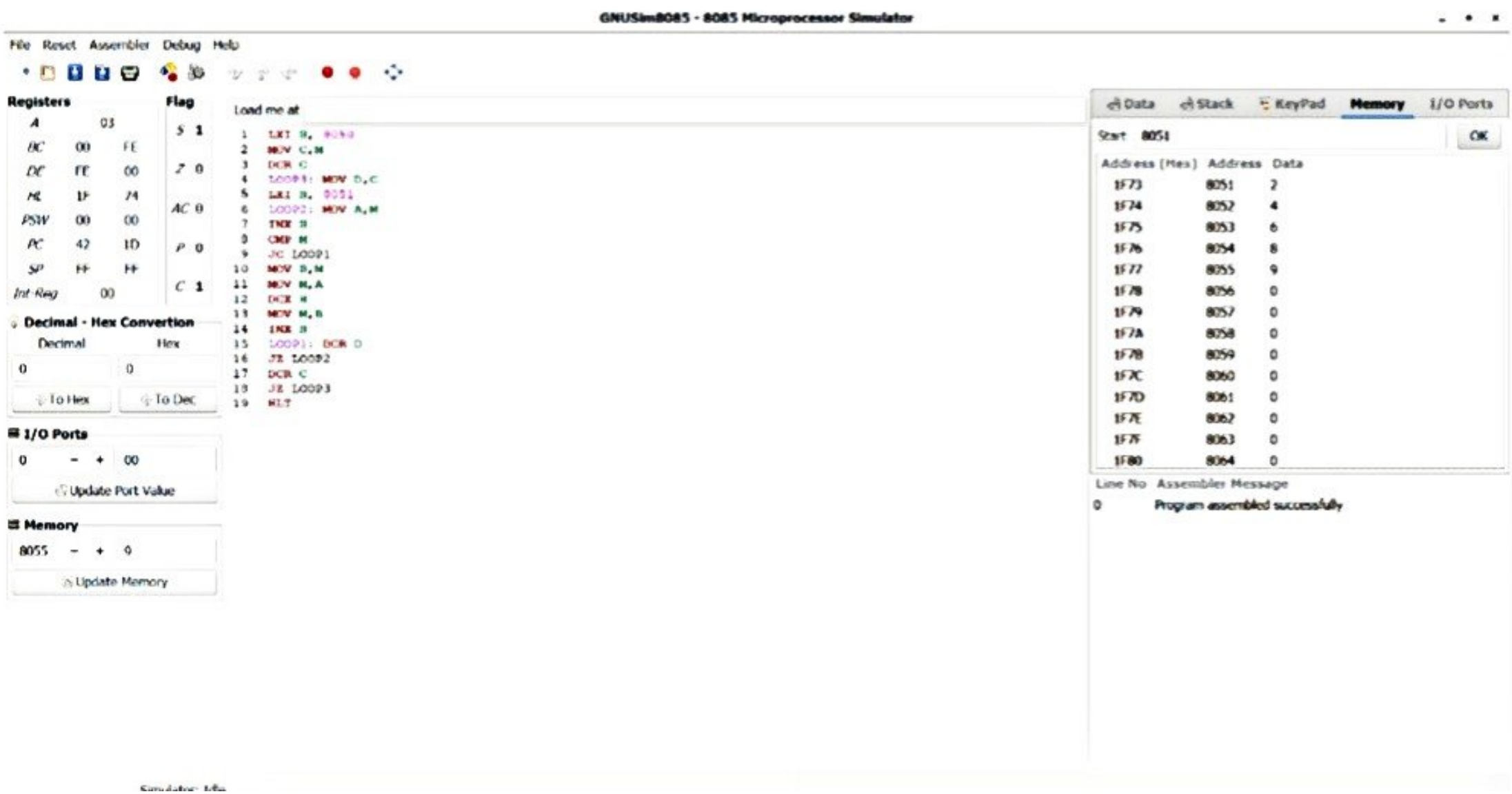

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 4E
BC 1A 00
DE 00 00
HL 08 04
PSW 00 00
PC 42 0D
SP FF FF
Int Reg 00
Flag
S 0
Z 0
AC 0
P 1
C 0
Load me at
1 LXI B,2050
2 MOV A,M
3 ADD A
4 MOV D,A
5 ADD A
6 ADD B
7 INX B
8 ADD M
9 INX H
10 MOV M,A
11 HLT
Data Stack KeyPad Memory I/O Ports
Start 2050 OK
Address (Hex) Address Data
0802 2050 13
0803 2051 0
0804 2052 78
0805 2053 0
0806 2054 0
0807 2055 0
0808 2056 0
0809 2057 0
080A 2058 0
080B 2059 0
080C 2060 0
080D 2061 0
080E 2062 0
080F 2063 0
Line No Assembler Message
0 Program assembled successfully
Decimal - Hex Convertion
Decimal Hex
0 0
To Hex To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
2051 - + 00
Update Memory
Simulator: Idle

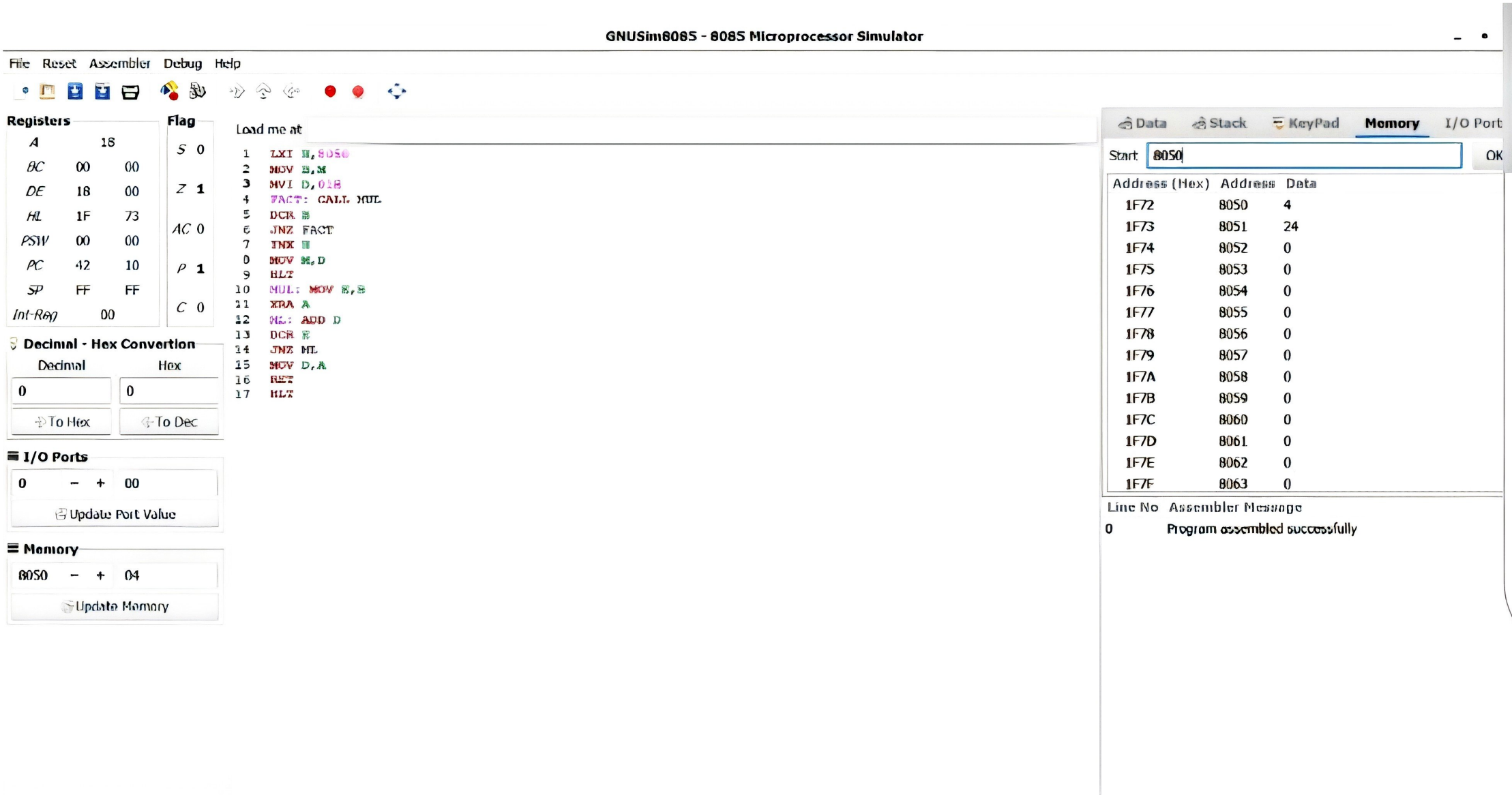

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 00
BC 19 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 1A
SP FF FF
Int Reg 00
Flag
S 1
Z 0
AC 0
P 0
C 1
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8501 - + 05
Update Memory
Load me at
1 LDA 8500
2 MOV B,A
3 LDA 8501
4 MVI C,00
5 LOOP1: CMP B
6 JC LOOP1
7 SUB B
8 INR C
9 JMP LOOP
10 LOOP1: STA 8502
11 MOV A,C
12 STA 8503
13 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8500
OK
Address (Hex) Address Data
2134 8500 25
2135 8501 5
2136 8502 5
2137 8503 0
2138 8504 0
2139 8505 0
213A 8506 0
213B 8507 0
213C 8508 0
213D 8509 0
213E 8510 0
213F 8511 0
2140 8512 0
2141 8513 0
Line No Assembler Message
0 Program assembled successfully

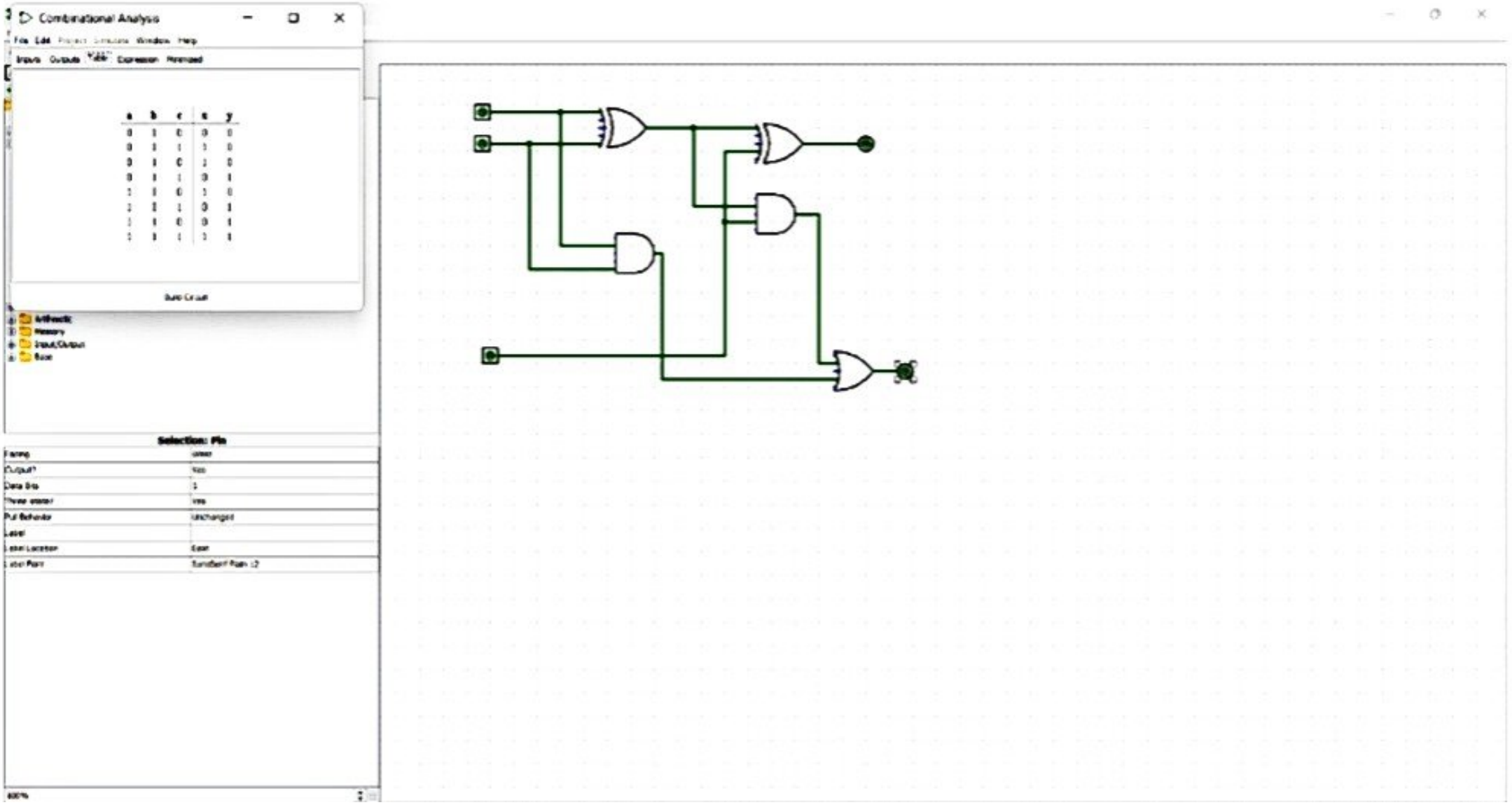

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
A FF
BC 00 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 08
SP FF FF
Int Reg 00
S 0
Z 0
AC 0
P 0
C 0
Load me at
1 LDA 8050
2 CMA
3 STA 8051
4 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8051
OK
Address (Hex) Address Data
1F73 8051 247
1F74 8052 0
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
1F80 8064 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8050 - + 08
Update Memory
Line No Assembler Message
0 Program assembled successfully

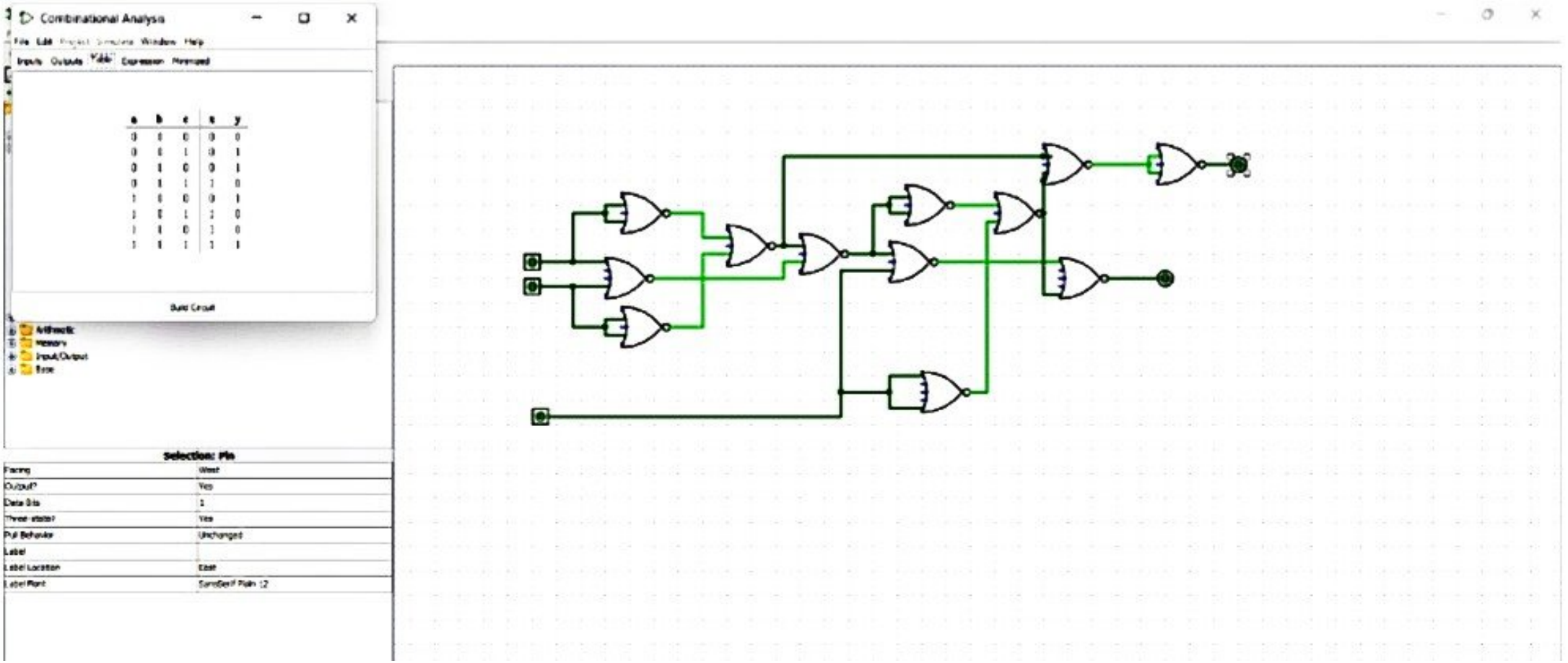

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
Load me at
Data Stack KeyPad Memory I/O Ports
Start 8050
OK
Address (Hex) Address Data
1F72 8050 3
1F73 8051 4
1F74 8052 7
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Line No Assembler Message
0 Program assembled successfully
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
8051
04
Update Memory

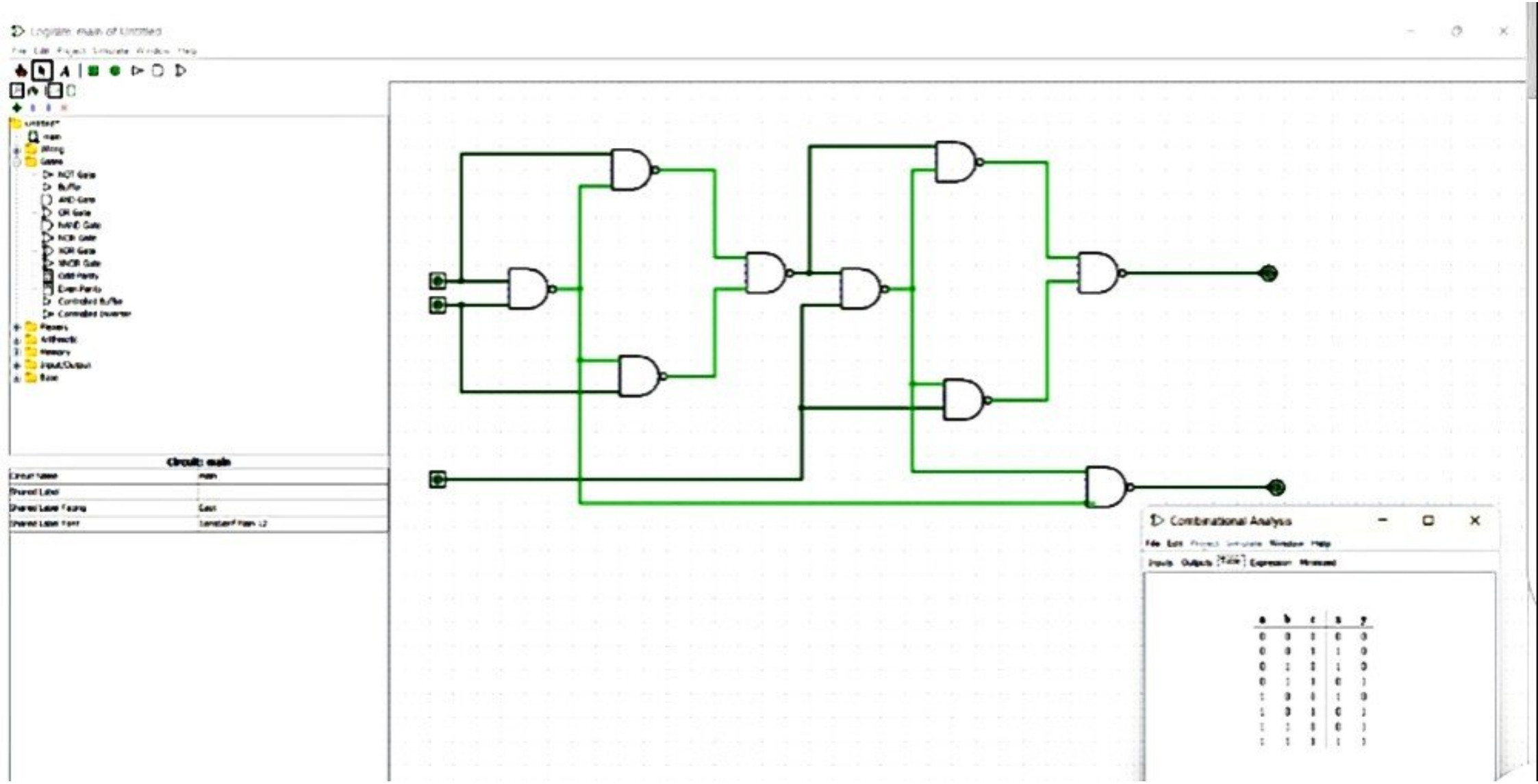

Logisim: main of Untitled 3
Circuit: main

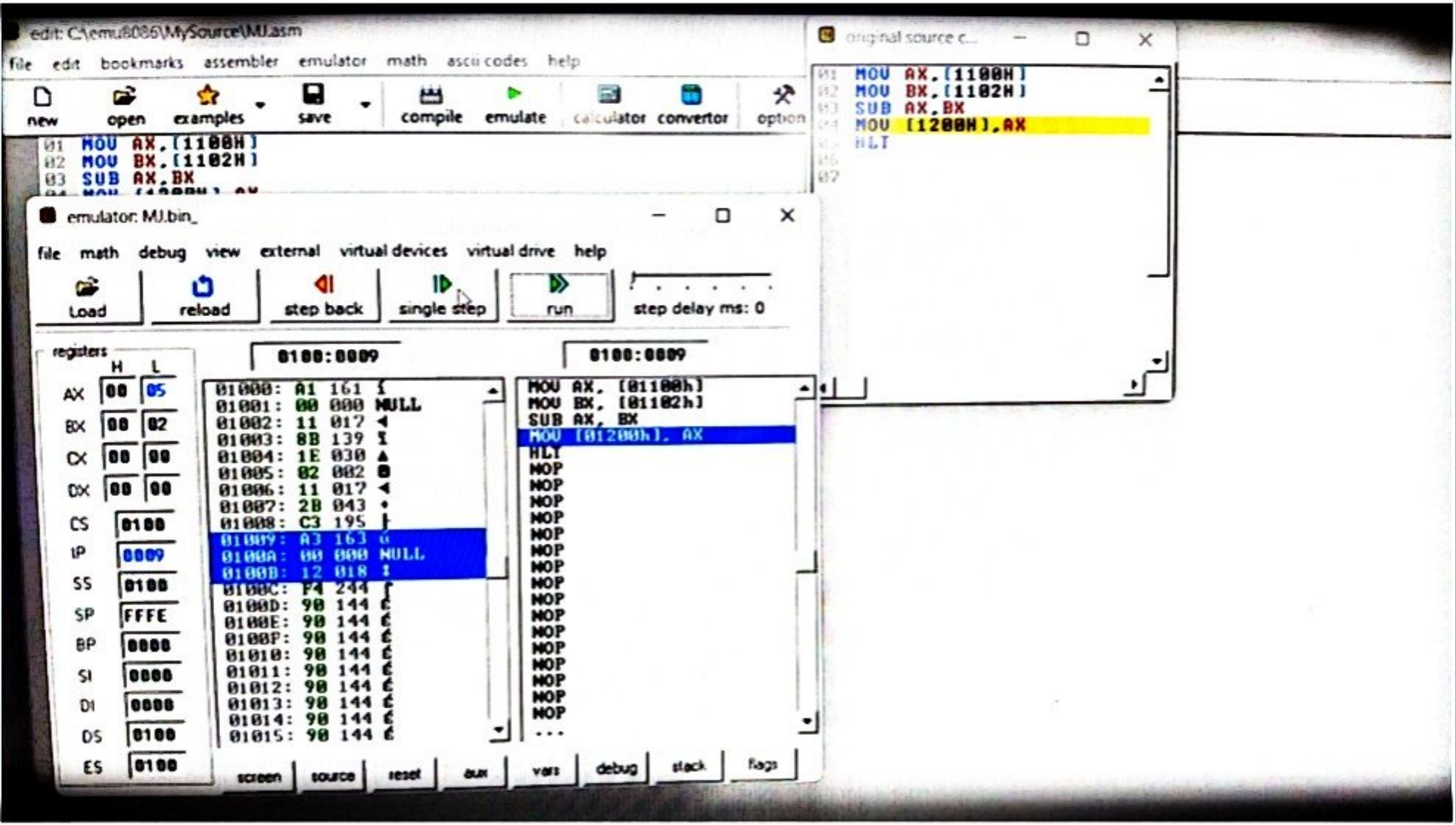

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 03
BC 00 FE
DE FE 00
HL 1F 74
PSW 00 00
PC 42 1D
SP FF FF
Int-Reg 00
Flag
S 1
Z 0
AC 0
P 0
C 1
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8055 - + 0
Update Memory
Load me at
1 LXI B, 8050
2 MOV C,M
3 DCR C
4 LOOP1: MOV D,C
5 LXI B, 8051
6 LOOP2: MOV A,M
7 INX B
8 CMP M
9 JC LOOP1
10 MOV B,M
11 MOV M,A
12 DCX H
13 MOV M,B
14 INX B
15 LOOP1: DCR D
16 JZ LOOP2
17 DCR C
18 JZ LOOP3
19 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8051
OK
Address (Hex) Address Data
1F73 8051 2
1F74 8052 4
1F75 8053 6
1F76 8054 8
1F77 8055 9
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
1F80 8064 0
Line No Assembler Message
0 Program assembled successfully

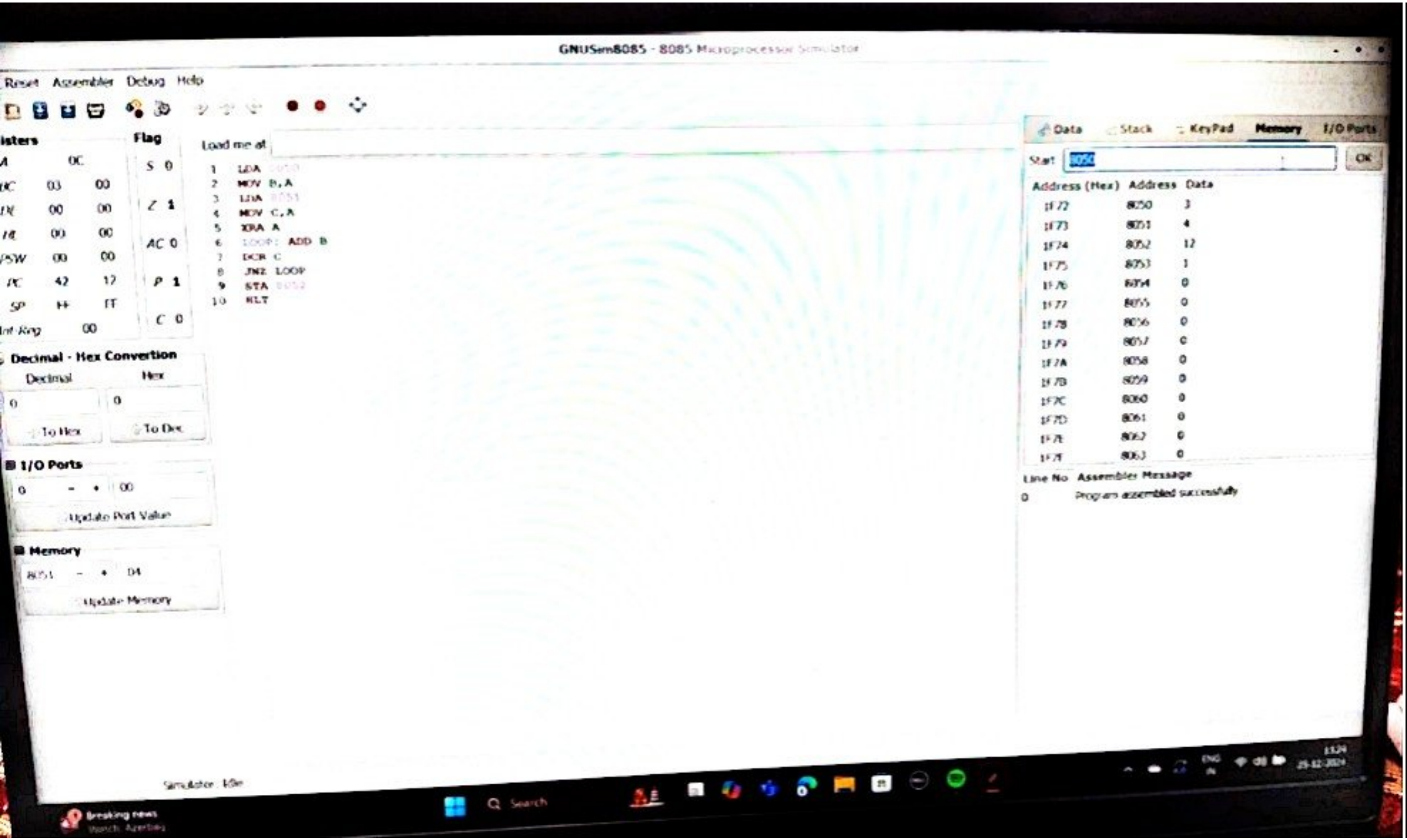

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 18
BC 00 00
DE 18 00
HL 1F 73
PSW 00 00
PC 42 10
SP FF FF
Int-Req 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8050 - + 04
Update Memory
Load me at
1 LXI H,8050
2 MOV B,M
3 MVI D,01H
4 FACT: CALL MUL
5 DCR B
6 JNZ FACT
7 INX H
8 MOV M,D
9 HLT
10 MUL: MOV B,B
11 XRA A
12 ML: ADD D
13 DCR B
14 JNZ ML
15 MOV D,A
16 RET
17 HLT
Data
Stack
KeyPad
Memory
I/O Port
Start: 8050
OK
Address (Hex) Address Data
1F72 8050 4
1F73 8051 24
1F74 8052 0
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Line No Assembler Message
0 Program assembled successfully

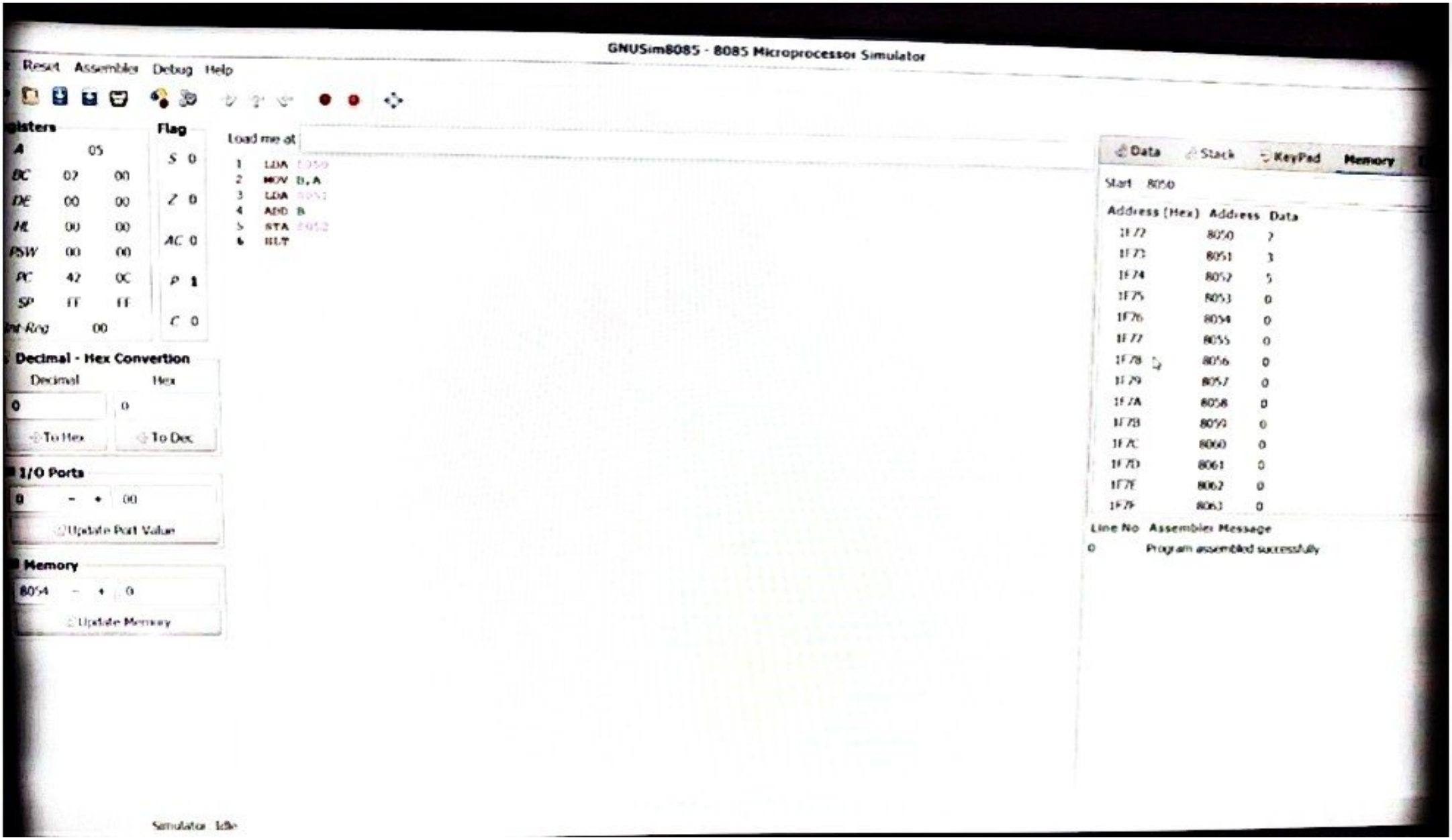

Combinational Analysis
Inputs Outputs Table Expression Minimized
a b c x y
Build Circuit
Selection: Pin

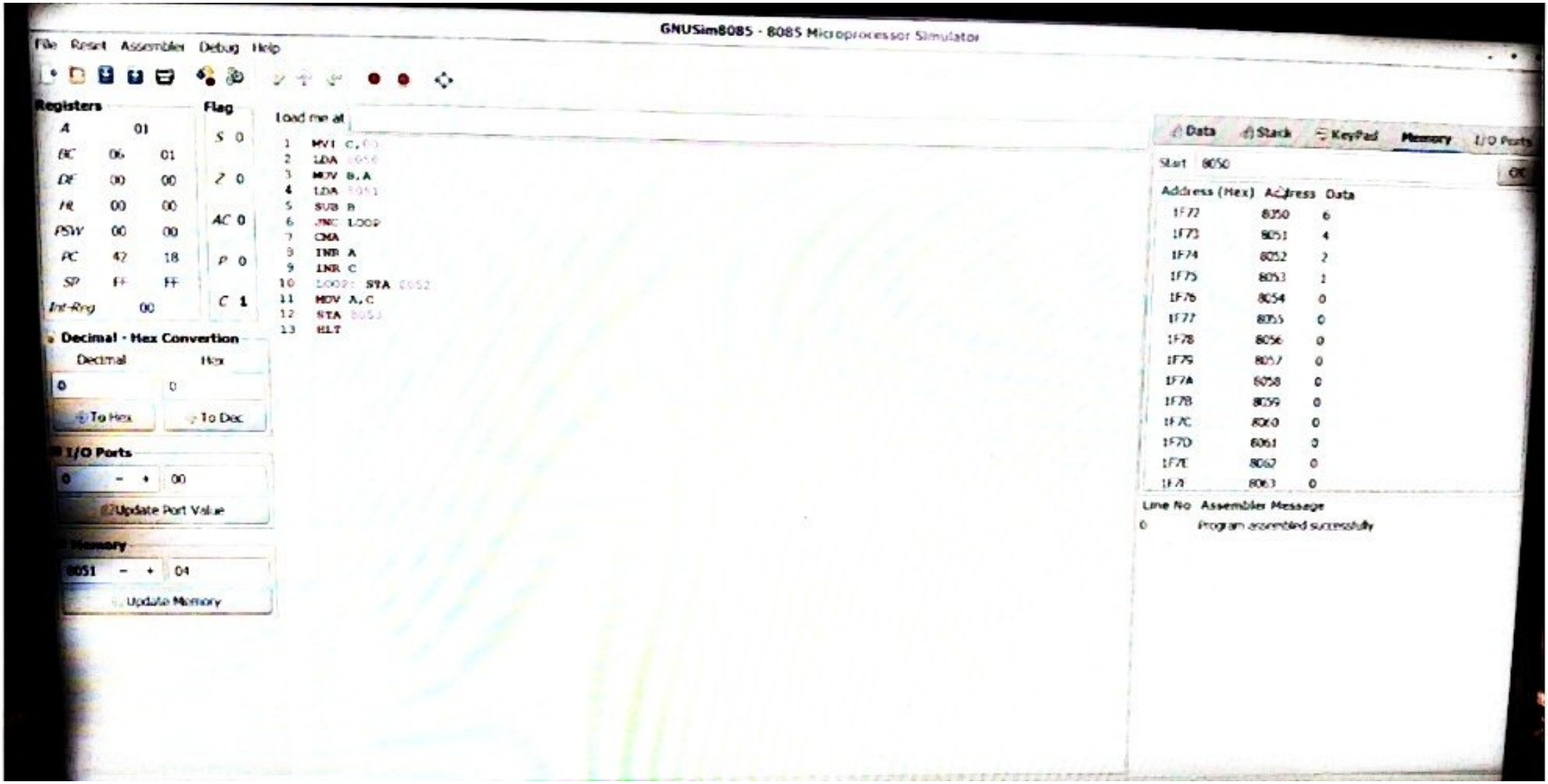

Combinational Analysis
File Edit Project Simulate Window Help
Inputs Outputs Table Expression Minimized
a b c s y
0 0 0 0 0
0 0 1 0 1
0 1 0 0 1
0 1 1 1 0
1 0 0 0 1
1 0 1 1 0
1 1 0 1 0
1 1 1 1 1
Build Circuit
Arithmetic
Memory
Input/Output
Base
Selection: Pin
Facing West
Output? Yes
Data Bits 1
Three-state? Yes
Pull Behavior Unchanged
Label
Label Location East
Label Font SansSerif Plain 12

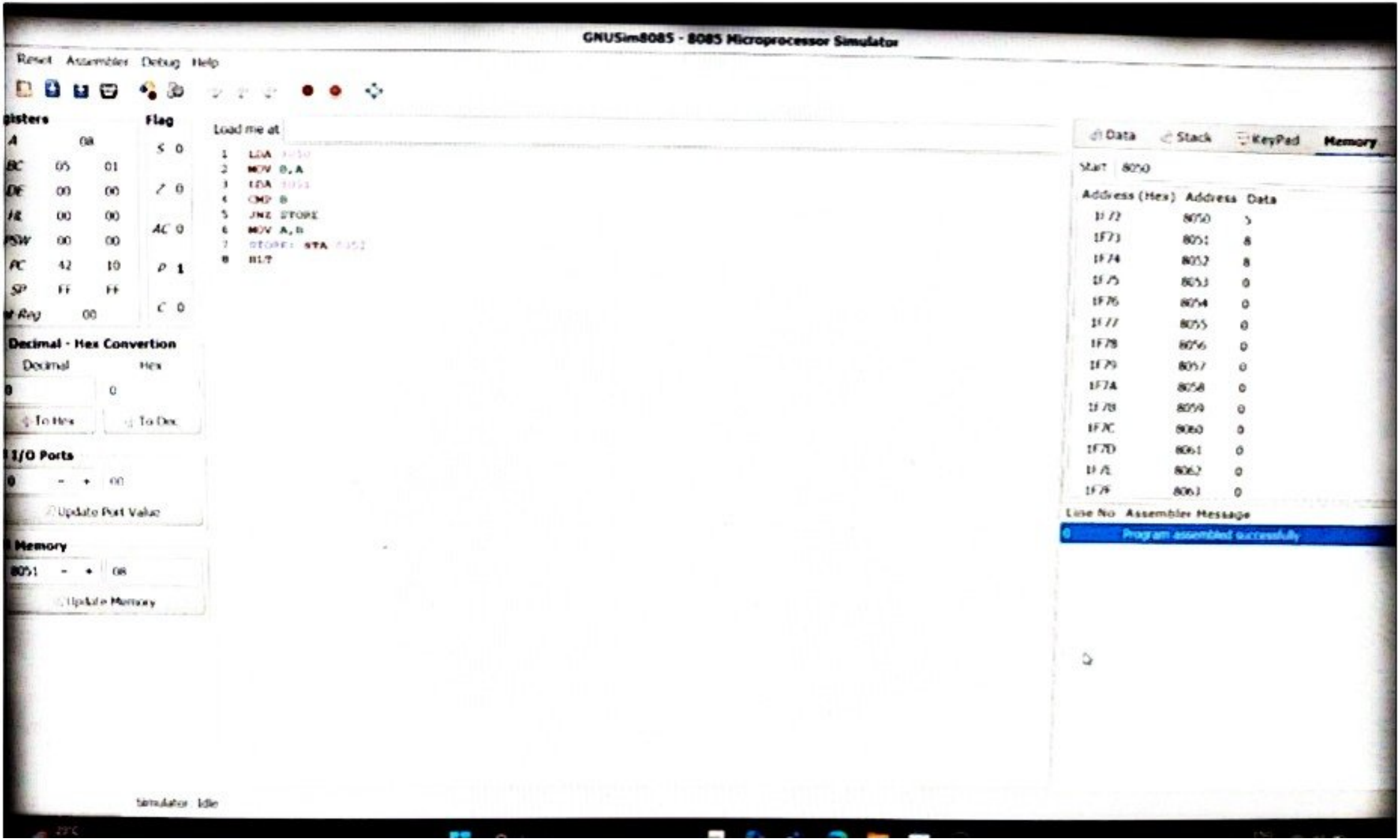

Combinational Analysis
a b c s y

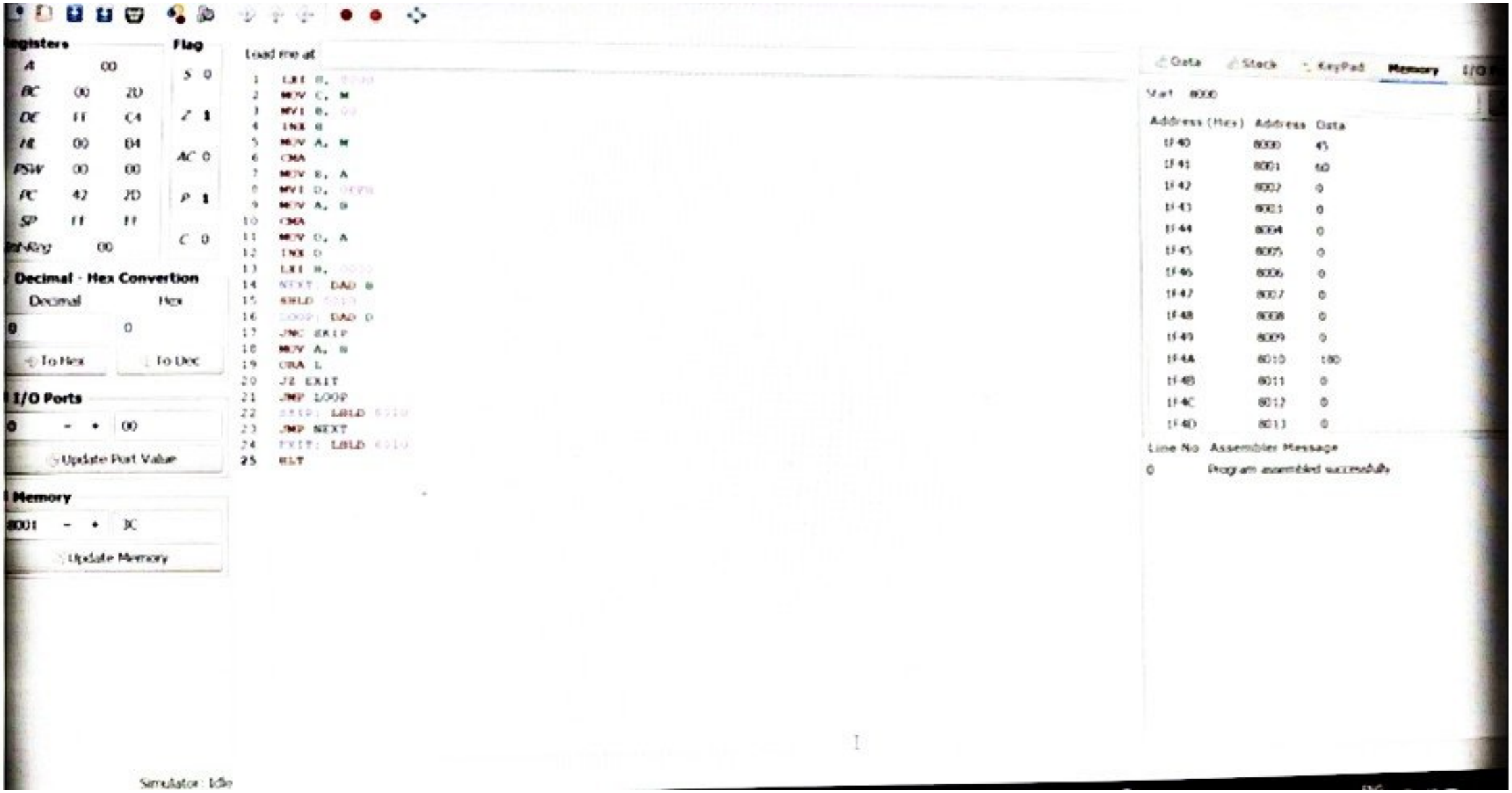

edit: C:\emu8086\MySource\MU.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor option
01 MOU AX,[1100H]
02 MOU BX,[1102H]
03 SUB AX,BX
original source c...
01 MOU AX,[1100H]
02 MOU BX,[1102H]
03 SUB AX,BX
04 MOU [1200H],AX
05 HLT
emulator: MU.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 00 05
BX 00 02
CX 00 00
DX 00 00
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
0100:0009
01000: A1 161 ¡
01001: 00 000 NULL
01002: 11 017 ◄
01003: 8B 139 ï
01004: 1E 030 ▲
01005: 02 002 ☻
01006: 11 017 ◄
01007: 2B 043 +
01008: C3 195 ├
01009: A3 163 ú
0100A: 00 000 NULL
0100B: 12 018 ↕
0100C: F4 244 ⌠
0100D: 90 144 É
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
MOU AX, [01100h]
MOU BX, [01102h]
SUB AX, BX
MOU [01200h], AX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags

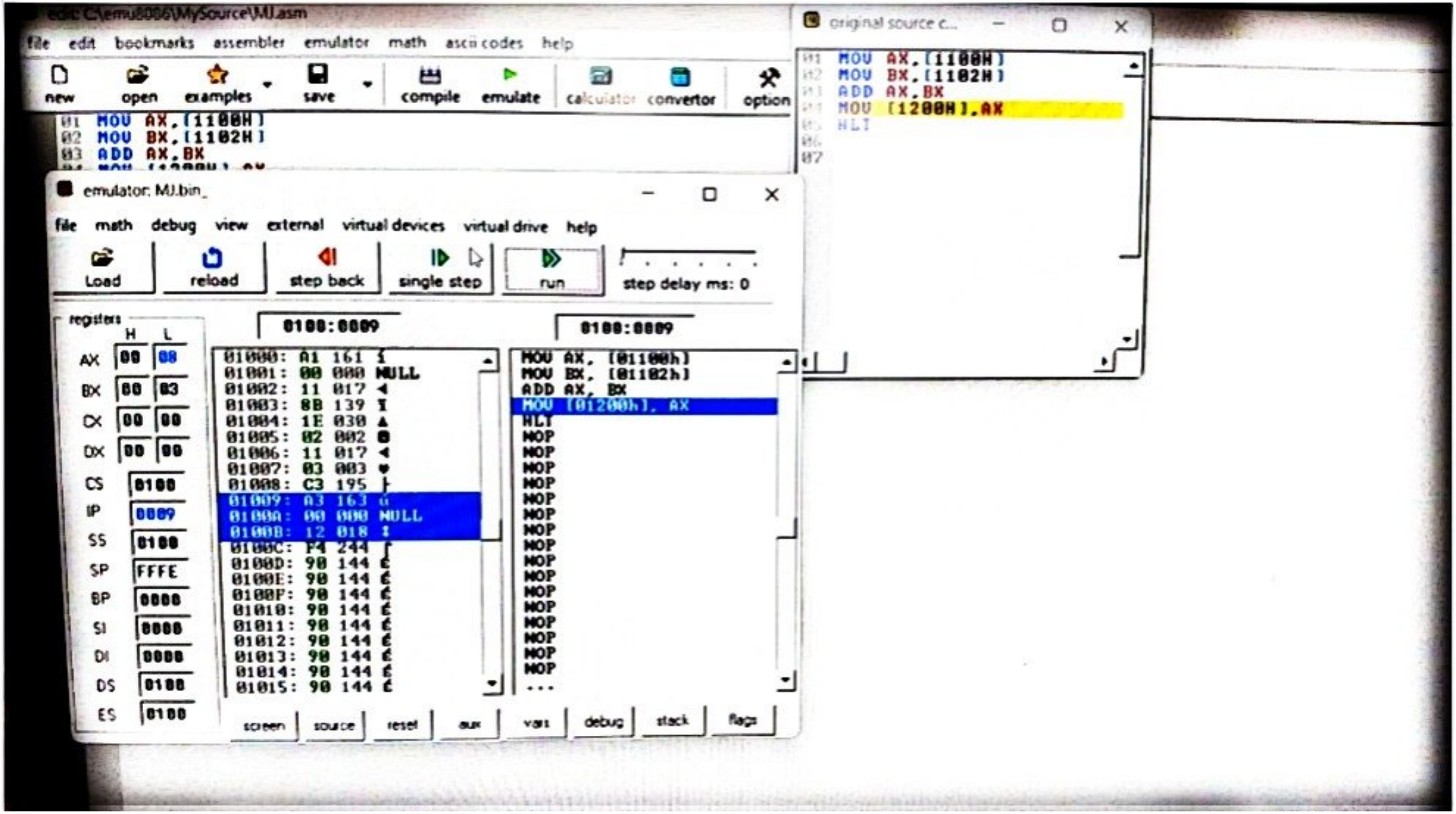

GNUSim8085 - 8085 Microprocessor Simulator
Reset Assembler Debug Help
Flag
S 0
Z 1
AC 0
P 1
C 0
Load me at
1 LDA
2 MOV B,A
3 LDA
4 MOV C,A
5 XRA A
6 LOOP: ADD B
7 DCR C
8 JNZ LOOP
9 STA
10 HLT
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
Update Memory
Data
Stack
KeyPad
Memory
I/O Ports
Start
OK
Address (Hex) Address Data
1F72 8050 3
1F73 8051 4
1F74 8052 12
1F75 8053 1
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Line No Assembler Message
0 Program assembled successfully
Simulator Idle
Search

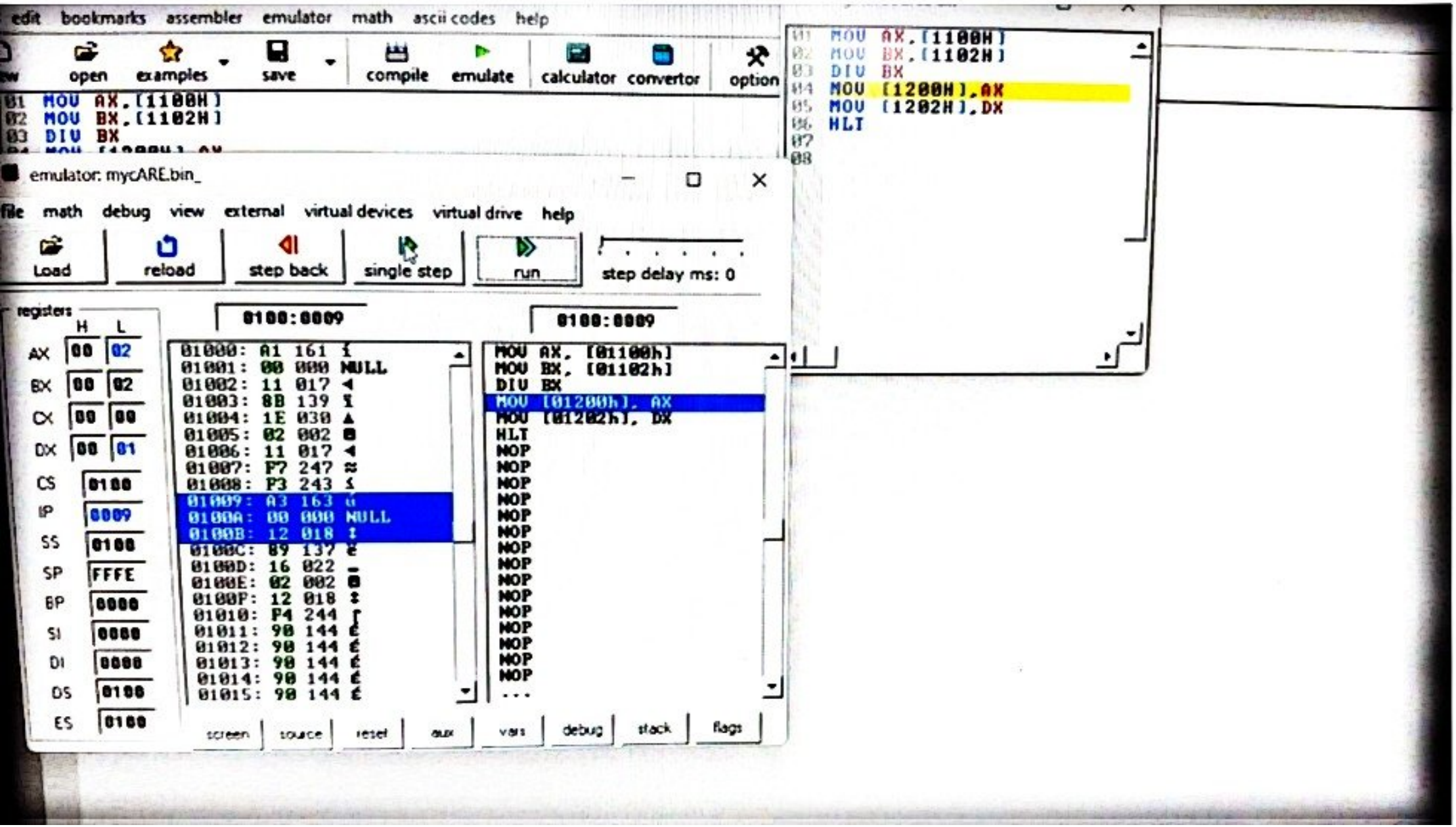

GNUSim8085 - 8085 Microprocessor Simulator
Reset Assemble Debug Help
Flag
S 0
Z 0
AC 0
P 1
C 0
Load me at
1 LDA
2 MOV B,A
3 LDA
4 AND B
5 STA
6 HLT
Data Stack KeyPad Memory
Start 8050
Address (Hex) Address Data
1F72 8050 7
1F73 8051 3
1F74 8052 5
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
Update Memory
Line No Assembler Message
0 Program assembled successfully
Simulator Idle

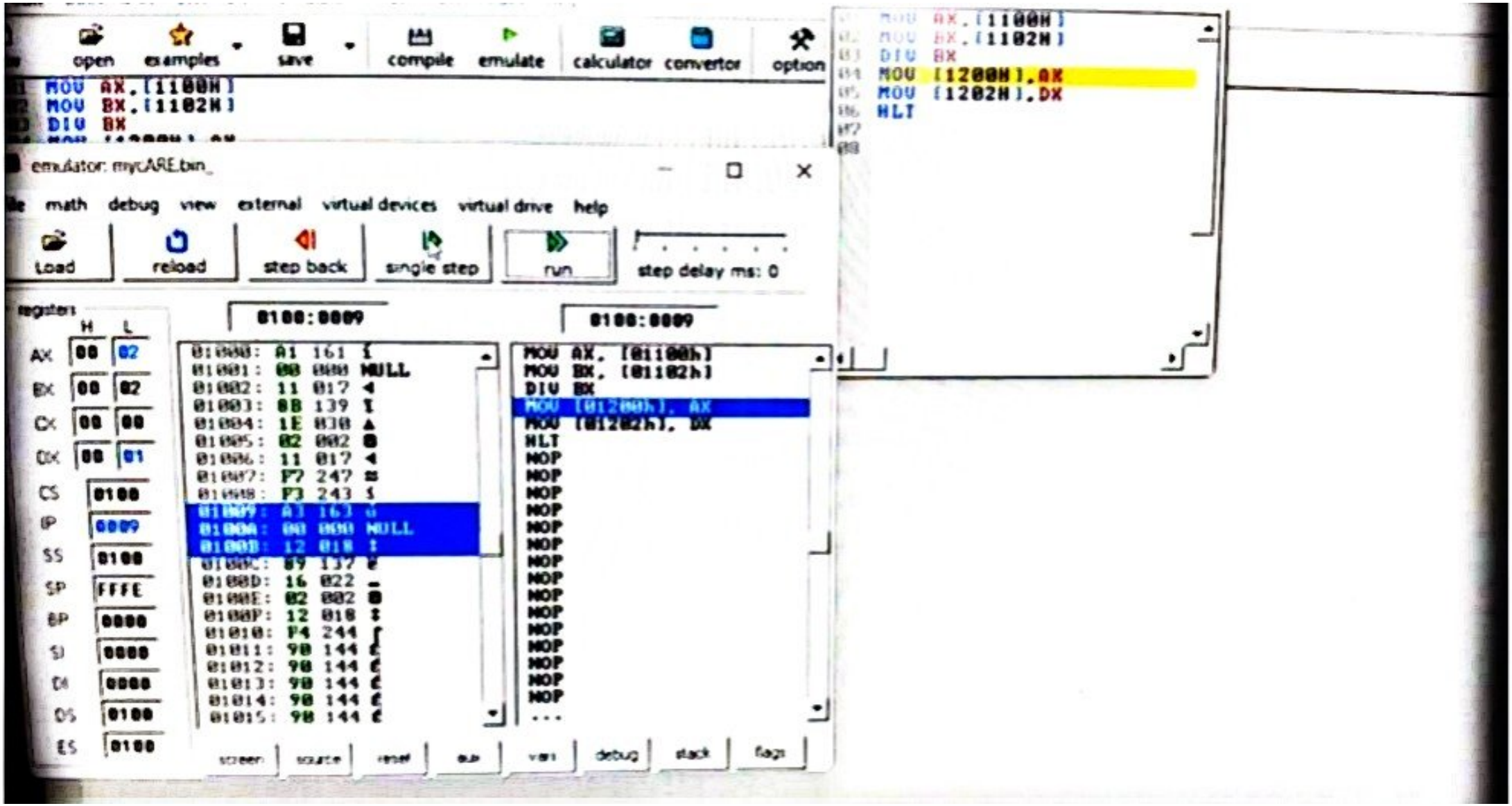

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 01
BC 06 01
DE 00 00
HL 00 00
PSW 00 00
PC 42 18
SP FF FF
Int-Reg 00
Flag
S 0
Z 0
AC 0
P 0
C 1
Load me at
1 MVI C,00
2 LDA 8050
3 MOV B,A
4 LDA 8051
5 SUB B
6 JNC LOOP
7 CMA
8 INR A
9 INR C
10 LOOP: STA 8052
11 MOV A,C
12 STA 8053
13 HLT
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8051 - + 04
Update Memory
Data
Stack
KeyPad
Memory
I/O Ports
Start 8050
OK
Address (Hex) Address Data
1F72 8050 6
1F73 8051 4
1F74 8052 2
1F75 8053 1
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Line No Assembler Message
0 Program assembled successfully

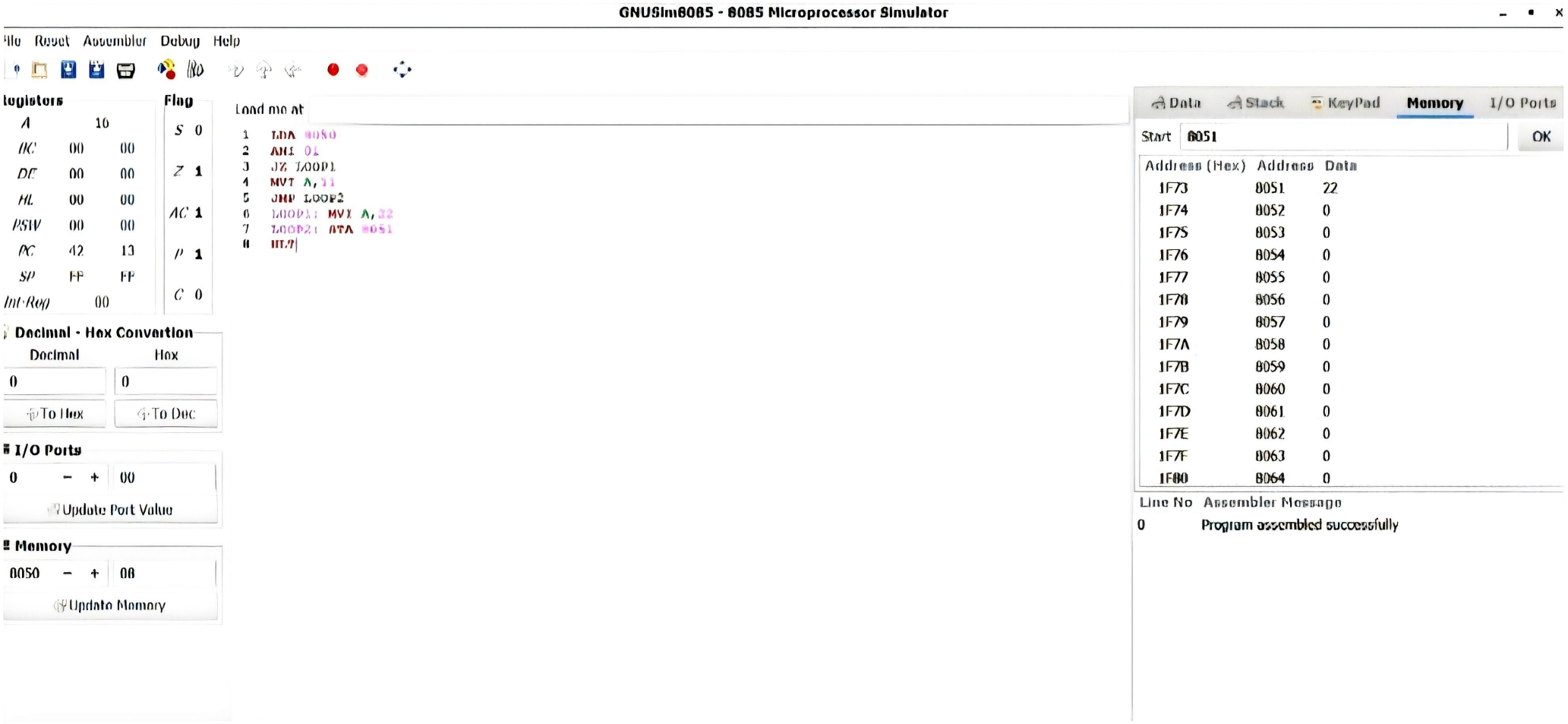

GNUSim8085 - 8085 Microprocessor Simulator
Reset Assembler Debug Help
Flag
Load me at
1 LDA
2 MOV B,A
3 LDA
4 CMP B
5 JNZ STORE
6 MOV A,B
7 STORE: STA
8 HLT
Data Stack KeyPad Memory
Start 8050
Address (Hex) Address Data
Decimal - Hex Convertion
Decimal Hex
To Hex To Dec
I/O Ports
Update Port Value
Memory
Update Memory
Line No Assembler Message
Program assembled successfully
Simulator Idle

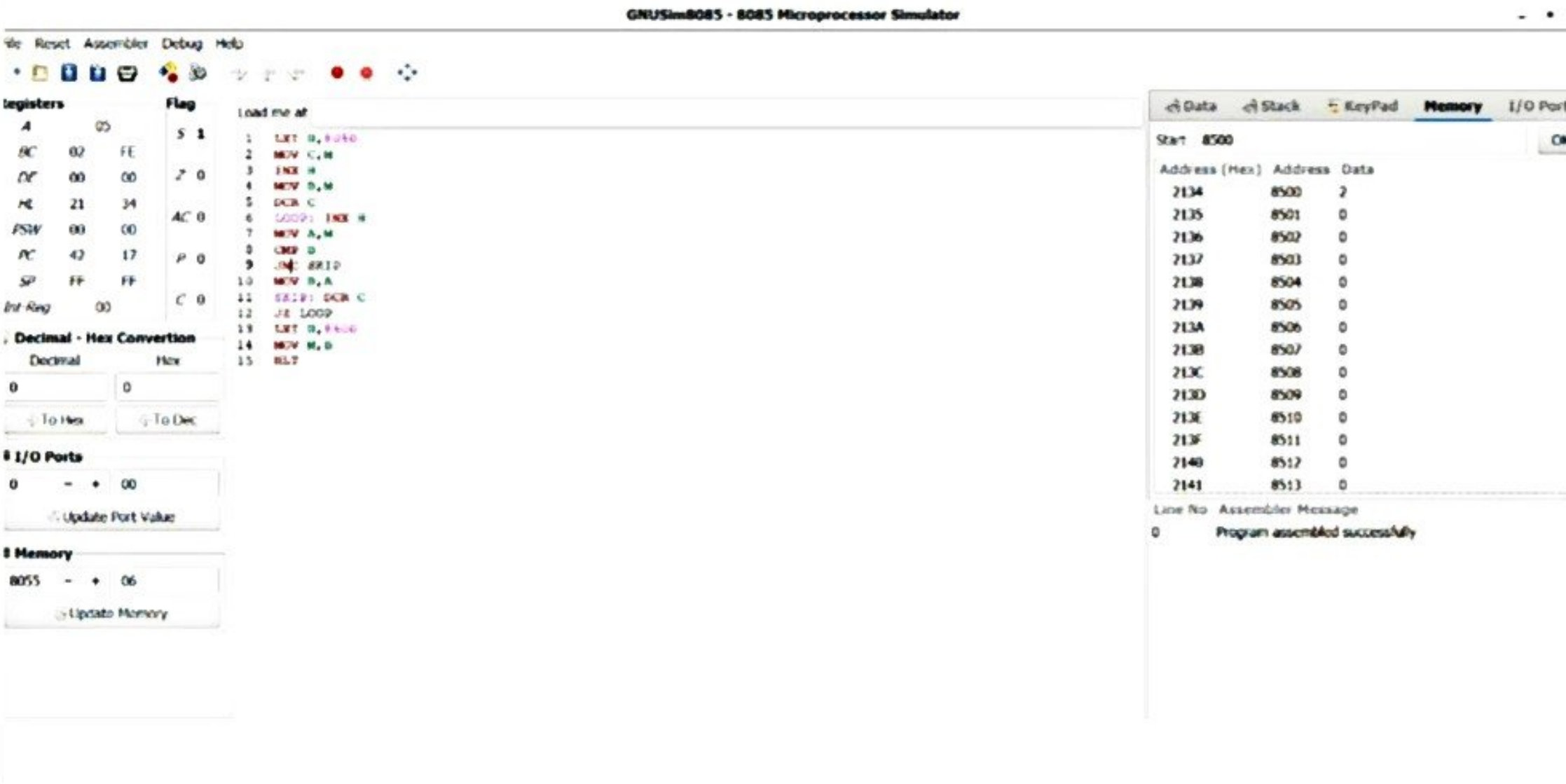

Registers
A 00
BC 00 2D
DE FF C4
HL 00 B4
PSW 00 00
PC 42 2D
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8001 - + 3C
Update Memory
Load me at
1 LXI B, 8500
2 MOV C, M
3 MVI B, 00
4 INX H
5 MOV A, M
6 CMA
7 MOV E, A
8 MVI D, FFFF
9 MOV A, B
10 CMA
11 MOV D, A
12 INX D
13 LXI B, 0000
14 NEXT: DAD B
15 SBLD 8510
16 LOOP: DAD D
17 JNC SKIP
18 MOV A, B
19 ORA L
20 JZ EXIT
21 JMP LOOP
22 SKIP: LBLD 8510
23 JMP NEXT
24 EXIT: LBLD 8510
25 HLT
Data
Stack
KeyPad
Memory
Start 8500
Address (Hex) Address Data
1F40 8500 45
1F41 8501 60
1F42 8502 0
1F43 8503 0
1F44 8504 0
1F45 8505 0
1F46 8506 0
1F47 8507 0
1F48 8508 0
1F49 8509 0
1F4A 850A 180
1F4B 850B 0
1F4C 850C 0
1F4D 850D 0
Line No Assembler Message
0 Program assembled successfully
Simulator : Idle

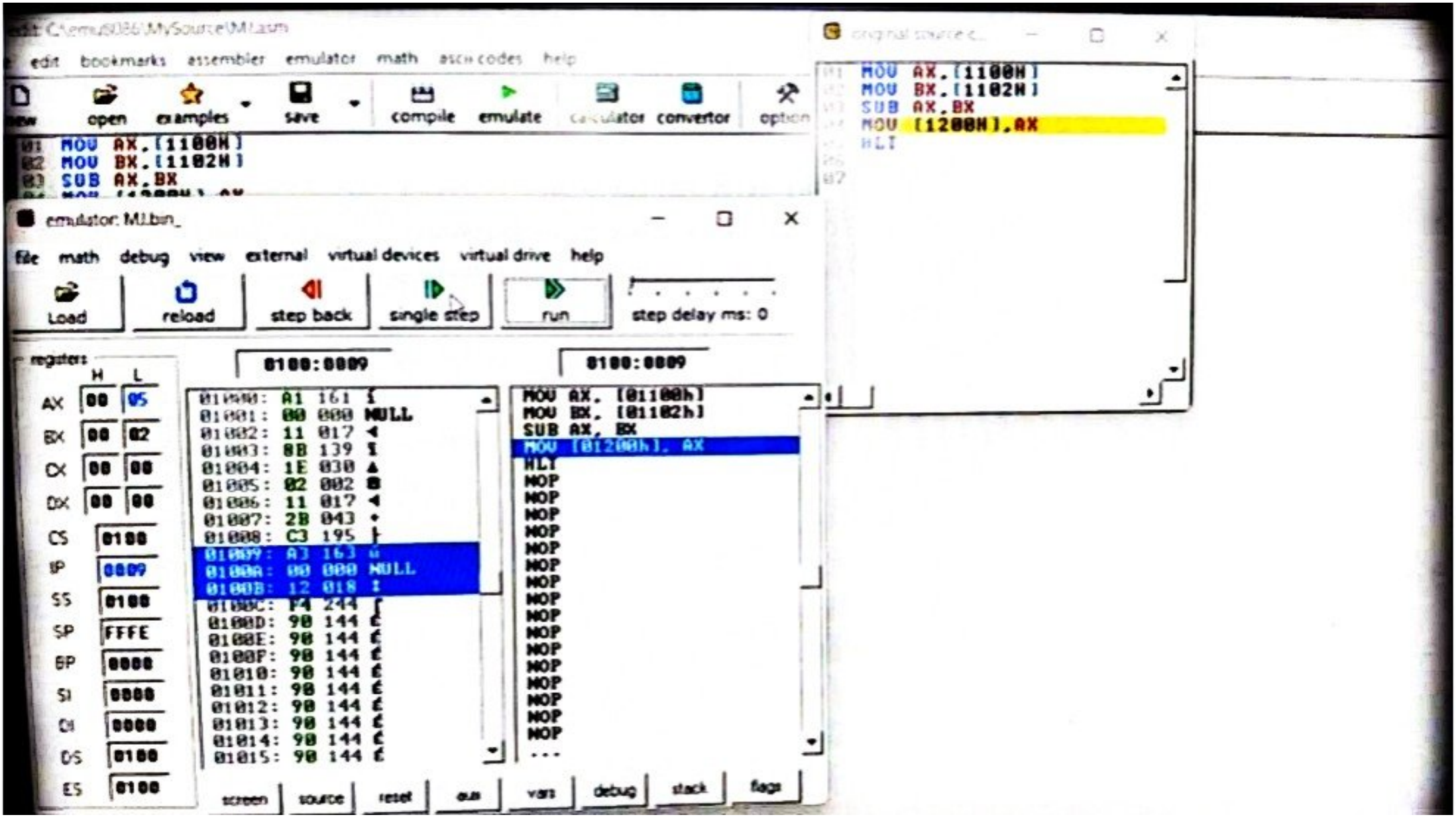

edit: C:\emu8086\MySource\MJ.asm
file edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor option
01 MOV AX,[1100H]
02 MOV BX,[1102H]
03 ADD AX,BX
original source c...
01 MOV AX,[1100H]
02 MOV BX,[1102H]
03 ADD AX,BX
04 MOV [1200H],AX
05 HLT
06
07
emulator: MJ.bin
file math debug view external virtual devices virtual drive help
Load reload step back single step run
step delay ms: 0
registers
H L
AX 00 08
BX 00 03
CX 00 00
DX 00 00
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
01000: A1 161 í
01001: 00 000 NULL
01002: 11 017 ◄
01003: 8B 139 ï
01004: 1E 030 ▲
01005: 02 002 ☻
01006: 11 017 ◄
01007: 03 003 ♥
01008: C3 195 ├
01009: A3 163 ú
0100A: 00 000 NULL
0100B: 12 018 ↕
0100C: F4 244 ⌠
0100D: 90 144 É
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
0100:0009
MOV AX, [01100h]
MOV BX, [01102h]
ADD AX, BX
MOV [01200h], AX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags

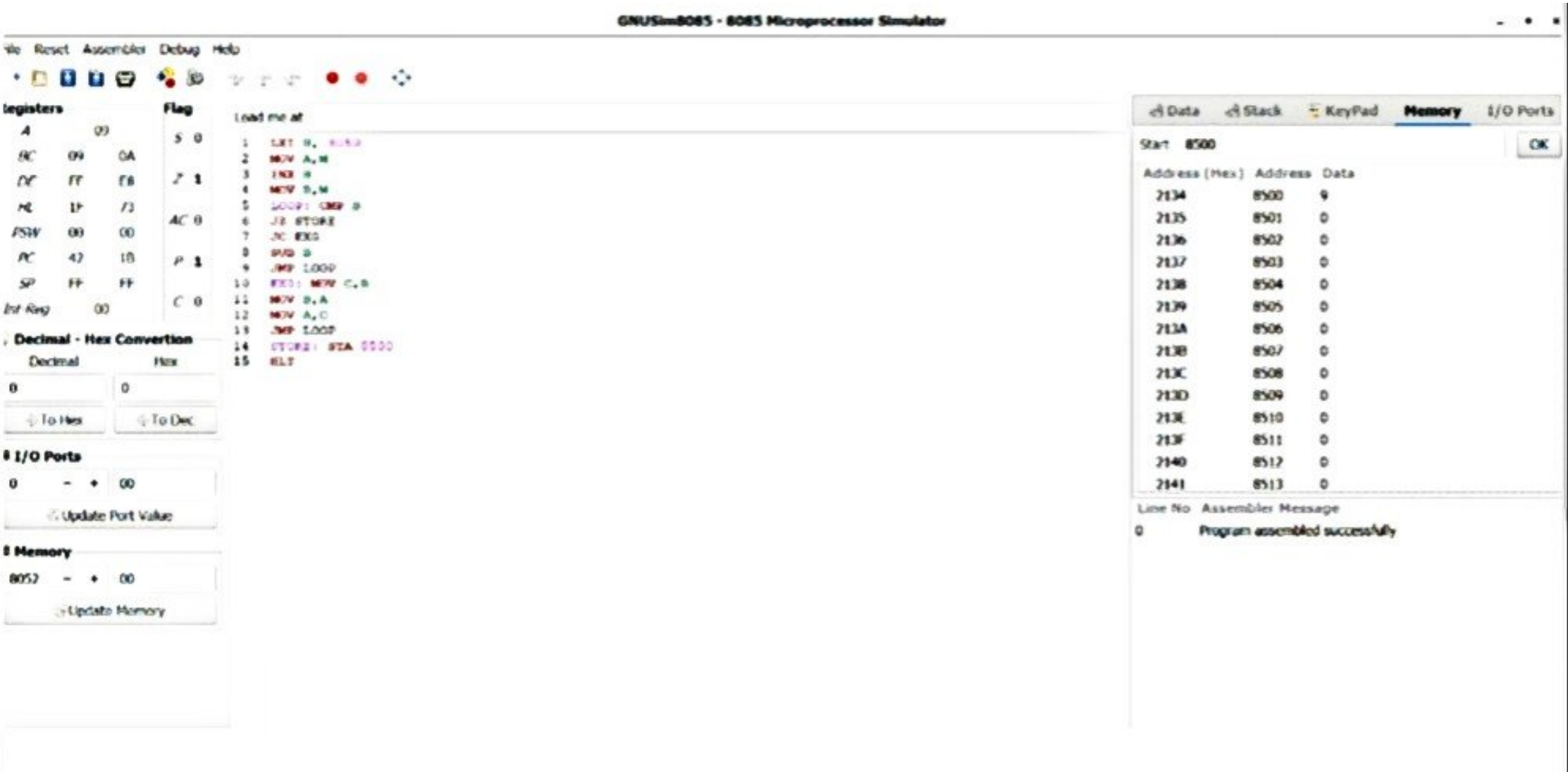

edit bookmarks assembler emulator math ascii codes help
open examples save compile emulate calculator convertor option
01 MOU AX,[1100H]
02 MOU BX,[1102H]
03 DIU BX
04 MOU [1200H],AX
05 MOU [1202H],DX
06 HLT
07
08
emulator: mycARE.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 00 02
BX 00 02
CX 00 00
DX 00 01
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
0100:0009
01000: A1 161 í
01001: 00 000 NULL
01002: 11 017 ◄
01003: 8B 139 ï
01004: 1E 030 ▲
01005: 02 002 ☻
01006: 11 017 ◄
01007: F7 247 ≈
01008: F3 243 ≤
01009: A3 163 ú
0100A: 00 000 NULL
0100B: 12 018 ↕
0100C: 89 137 ë
0100D: 16 022 ▬
0100E: 02 002 ☻
0100F: 12 018 ↕
01010: F4 244 ⌠
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
MOU AX, [01100h]
MOU BX, [01102h]
DIU BX
MOU [01200h], AX
MOU [01202h], DX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags


open
examples
save
compile
emulate
calculator
convertor
option
MOU AX,[1100H]
MOU BX,[1102H]
DIV BX
emulator: myCARE.bin
file
math
debug
view
external
virtual devices
virtual drive
help
Load
reload
step back
single step
run
step delay ms: 0
registers
H
L
AX 00 02
BX 00 02
CX 00 00
DX 00 01
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
0100:0009
MOU AX, [01100h]
MOU BX, [01102h]
DIV BX
MOU [01200h], AX
MOU [01202h], DX
HLT
NOP
screen
source
reset
exit
vars
debug
stack
flags

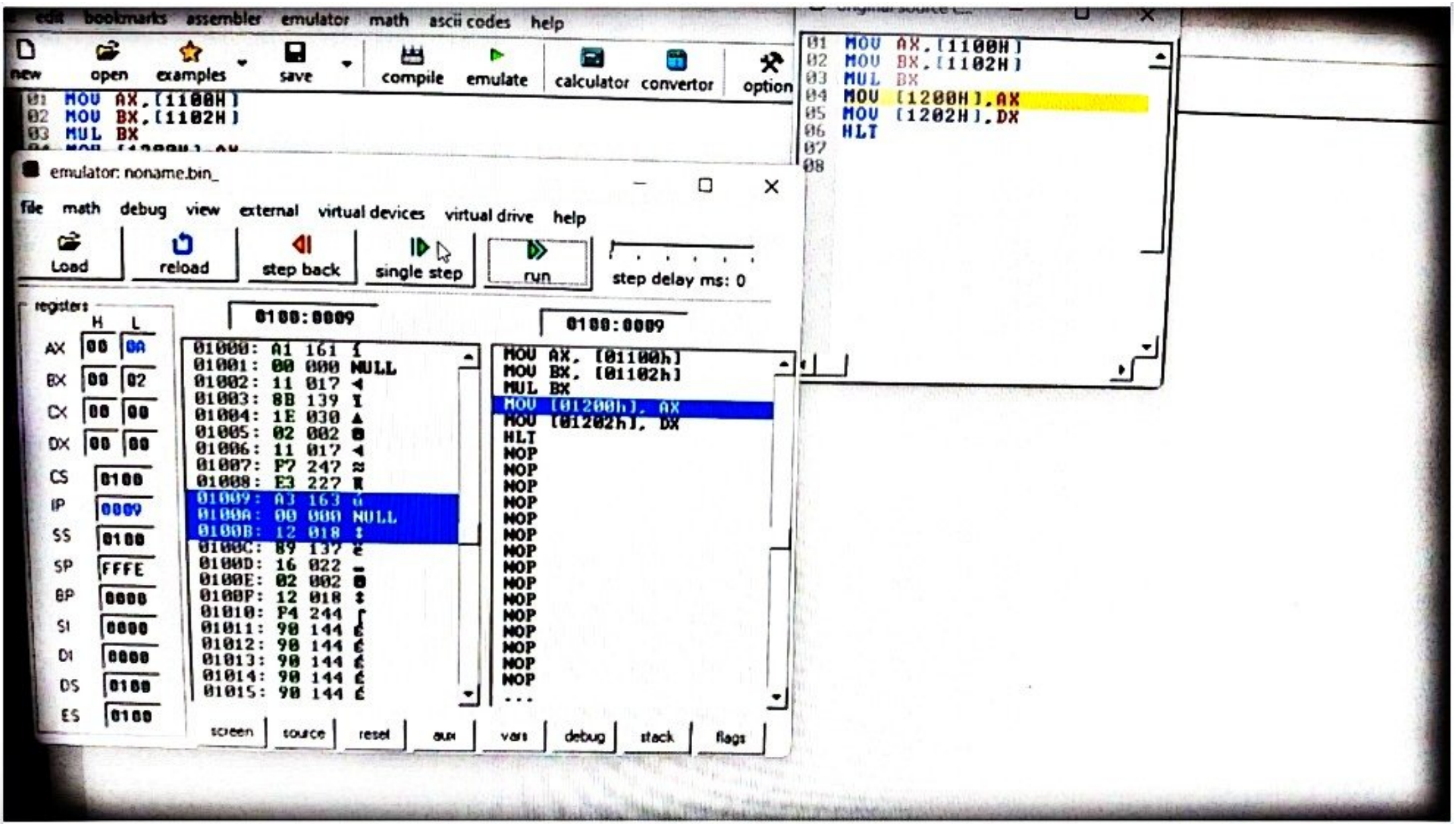

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 10
BC 00 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 13
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 1
P 1
C 0
Load me at
1 LDA 8050
2 ANI 01
3 JZ LOOP1
4 MVI A,11
5 JMP LOOP2
6 LOOP1: MVI A,22
7 LOOP2: STA 8051
8 HLT
Data Stack KeyPad Memory I/O Ports
Start 8051 OK
Address (Hex) Address Data
1F73 8051 22
1F74 8052 0
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
1F80 8064 0
Decimal - Hex Convertion
Decimal Hex
0 0
To Hex To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8050 - + 00
Update Memory
Line No Assembler Message
0 Program assembled successfully


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
A 05
BC 02 FE
DE 00 00
HL 21 34
PSW 00 00
PC 42 17
SP FF FF
Int Reg 00
S 1
Z 0
AC 0
P 0
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8055 - + 06
Update Memory
Load file at
1 LXI B,8540
2 MOV C,M
3 INX H
4 MOV D,M
5 DCR C
6 LOOP: INX H
7 MOV A,M
8 CMP D
9 JNC SKIP
10 MOV D,A
11 SKIP: DCR C
12 JZ LOOP
13 LXI B,8600
14 MOV M,D
15 HLT
Data
Stack
KeyPad
Memory
I/O Port
Start 8500
Address (Hex) Address Data
2134 8500 2
2135 8501 0
2136 8502 0
2137 8503 0
2138 8504 0
2139 8505 0
213A 8506 0
213B 8507 0
213C 8508 0
213D 8509 0
213E 8510 0
213F 8511 0
2140 8512 0
2141 8513 0
Line No Assembler Message
0 Program assembled successfully


edit bookmarks assembler emulator math asc-ii codes help
new open examples save compile emulate convertor option
01 MOU AX,[1100H]
02 MOU BX,[1102H]
03 SUB AX,BX
emulator: M1.bin
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 00 05
BX 00 02
CX 00 00
DX 00 00
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
0100:0009
01000: A1 161 í
01001: 00 000 NULL
01002: 11 017 ◄
01003: 8B 139 ï
01004: 1E 030 ▲
01005: 02 002 ☻
01006: 11 017 ◄
01007: 2B 043 +
01008: C3 195 ├
01009: A3 163 ú
0100A: 00 000 NULL
0100B: 12 018 ↕
0100C: F4 244 ⌠
0100D: 90 144 É
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
MOU AX, [01100h]
MOU BX, [01102h]
SUB AX, BX
MOU [01200h], AX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset out vars debug stack flags
MOU AX,[1100H]
MOU BX,[1102H]
SUB AX,BX
MOU [1200H],AX
HLT

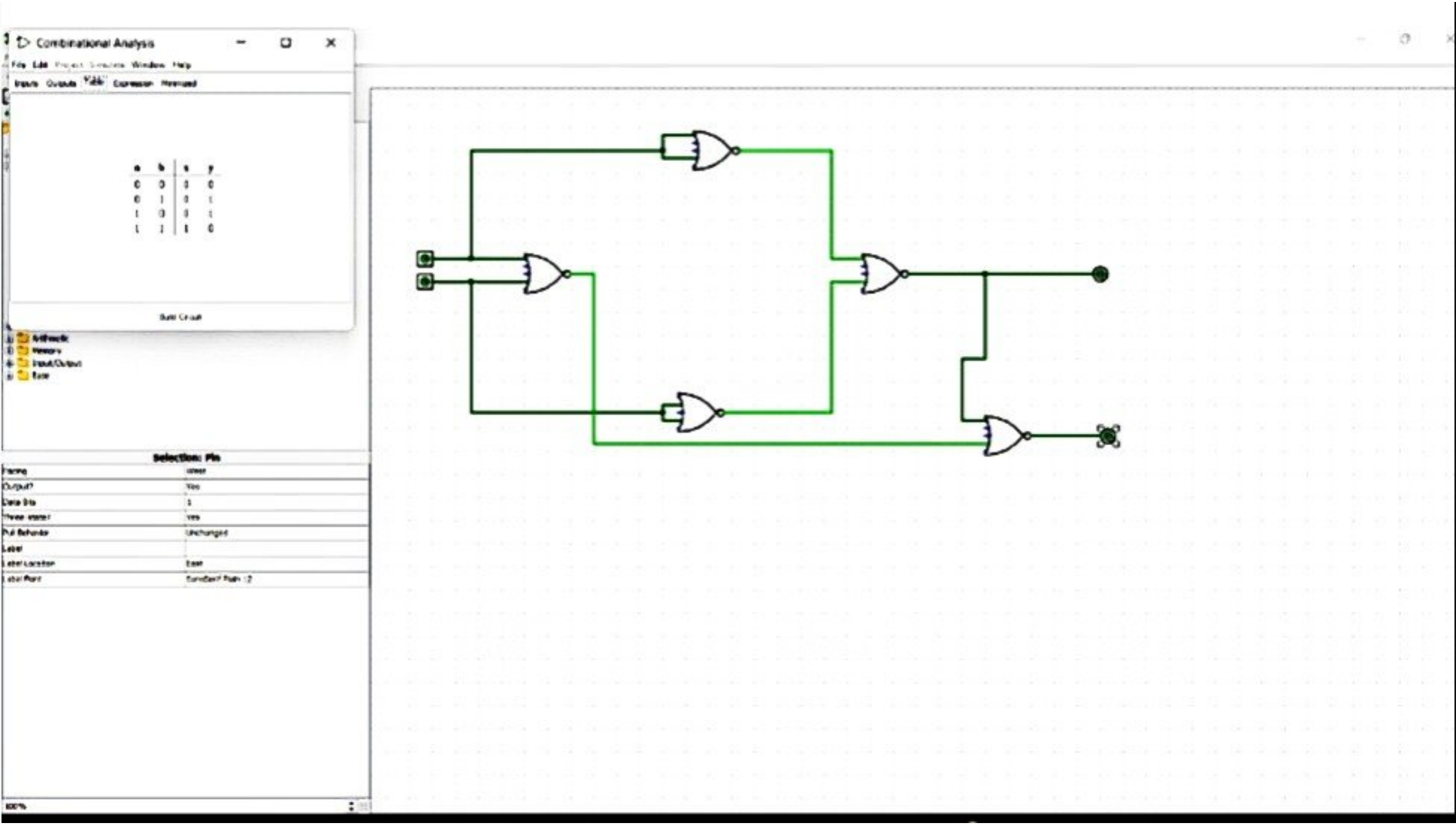

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assemble Debug Help
Registers
Flag
Load me at
1 LXI H, 8500
2 MOV A,M
3 INX H
4 MOV B,M
5 LOOP: CMP B
6 JZ STORE
7 JC DEC
8 MVI B
9 JMP LOOP
10 DEC: MOV C,B
11 MOV B,A
12 MOV A,C
13 JMP LOOP
14 STORE: STA 8500
15 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8500
OK
Address (Hex) Address Data
2134 8500 9
2135 8501 0
2136 8502 0
2137 8503 0
2138 8504 0
2139 8505 0
213A 8506 0
213B 8507 0
213C 8508 0
213D 8509 0
213E 8510 0
213F 8511 0
2140 8512 0
2141 8513 0
Line No Assembler Message
0 Program assembled successfully
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
Update Memory


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
A 04
BC 04 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 0F
SP FF FF
Int Reg 00
S 0
Z 0
AC 0
P 0
C 0
Load me at
1 LDA 8050
2 MOV B,A
3 LDA 8051
4 STA 8060
5 MOV A,B
6 STA 8051
7 HLT
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8051 - + 04
Update Memory
Data
Stack
KeyPad
Memory
I/O Ports
Start 8050
OK
Address (Hex) Address Data
1F72 8050 8
1F73 8051 4
1F74 8052 0
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Line No Assembler Message
0 Program assembled successfully

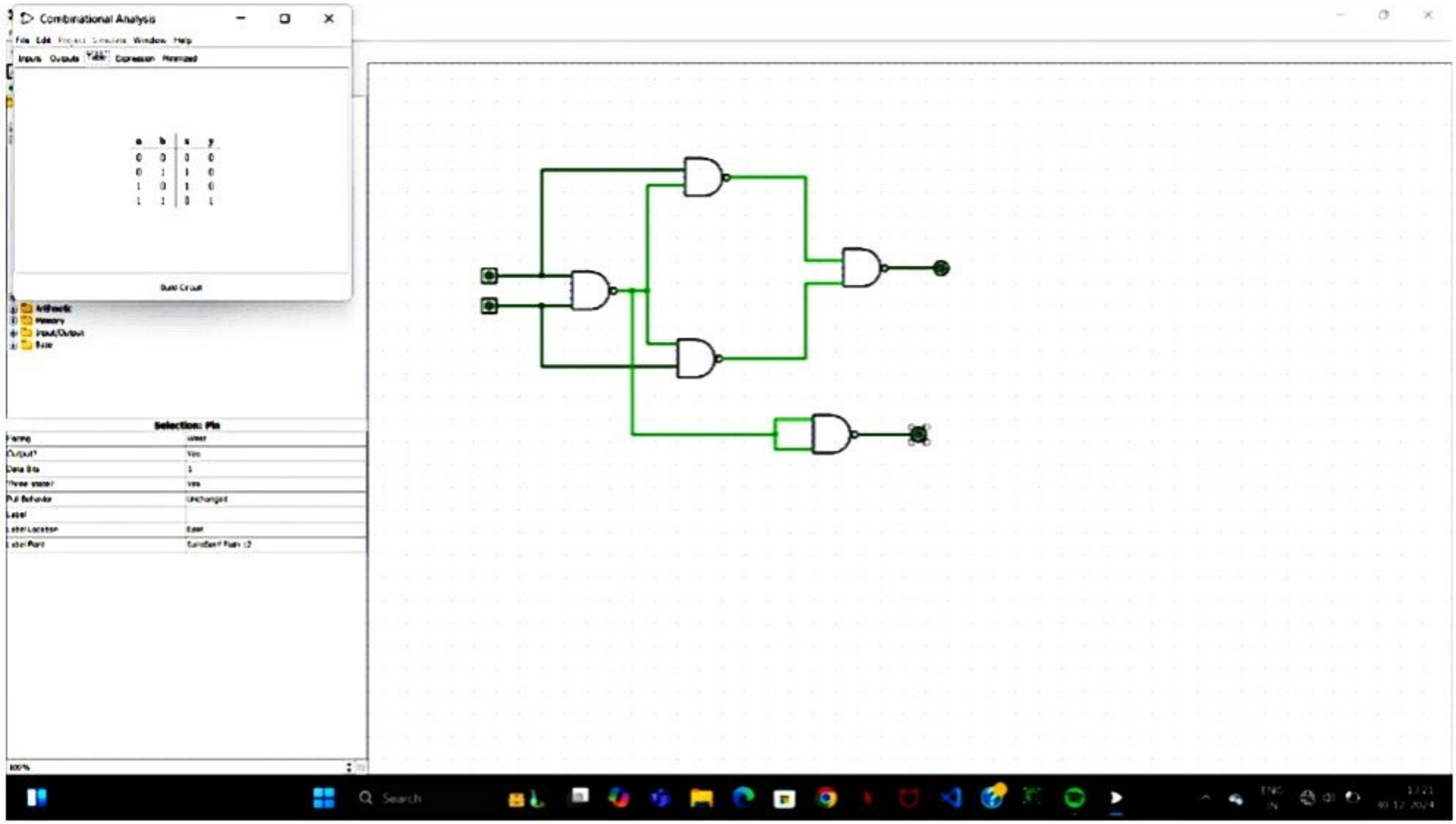

edit bookmarks assembler emulator math ascii codes help
new open examples save compile emulate calculator convertor option
01 MOV AX,[1100H]
02 MOV BX,[1102H]
03 MUL BX
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run
step delay ms: 0
registers
H L
AX 00 0A
BX 00 02
CX 00 00
DX 00 00
CS 0100
IP 0009
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0009
0100:0009
01000: A1 161 í
01001: 00 000 NULL
01002: 11 017 ◄
01003: 8B 139 ï
01004: 1E 030 ▲
01005: 02 002 ☻
01006: 11 017 ◄
01007: F7 247 ≈
01008: E3 227 π
01009: A3 163 ú
0100A: 00 000 NULL
0100B: 12 018 ↕
0100C: 89 137 ë
0100D: 16 022 ▬
0100E: 02 002 ☻
0100F: 12 018 ↕
01010: F4 244 ⌠
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
MOV AX, [01100h]
MOV BX, [01102h]
MUL BX
MOV [01200h], AX
MOV [01202h], DX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags
01 MOV AX,[1100H]
02 MOV BX,[1102H]
03 MUL BX
04 MOV [1200H],AX
05 MOV [1202H],DX
06 HLT
07
08


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
S 0
Z 0
AC 0
P 0
C 0
Load me at
1 LDA 8050
2 MOV B,A
3 LDA 8051
4 CMP B
5 JNZ STORE
6 STORE: MOV A,B
7 STA 8052
8 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8050
OK
Address (Hex)
Address
Data
8050 7
8051 9
8052 7
8053 0
8054 0
8055 0
8056 0
8057 0
8058 0
8059 0
8060 0
8061 0
8062 0
8063 0
Decimal - Hex Convertion
Decimal
Hex
To Hex
To Dec
I/O Ports
Update Port Value
Memory
8051
09
Update Memory
Line No
Assembler Message
Program assembled successfully


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
Flag
A 00
BC 0C 00
DE 00 00
HL 21 34
PSW 00 00
PC 42 17
SP FF FF
Int Reg 00
S 0
Z 1
AC 0
P 1
C 1
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8054 - + 10
Update Memory
Load me at
1 LXI H, 8500
2 MOV C,M
3 INX H
4 MOV B,M
5 DCR C
6 LOOP: INX H
7 MOV A,M
8 CMP B
9 JC SKIP
10 MOV B,A
11 SKIP: DCR C
12 JNZ LOOP
13 LXI H, 9500
14 MOV M,B
15 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8500
OK
Address (Hex) Address Data
2134 8500 12
2135 8501 0
2136 8502 0
2137 8503 0
2138 8504 0
2139 8505 0
213A 8506 0
213B 8507 0
213C 8508 0
213D 8509 0
213E 8510 0
213F 8511 0
2140 8512 0
2141 8513 0
Line No Assembler Message
0 Program assembled successfully


Combinational Analysis
Inputs Outputs Table Expression Minimized
a b s y
0 0 0 0
0 1 0 1
1 0 0 1
1 1 1 0
Build Circuit
Selection: Pin
Output?
Yes
Label
Label Location
East


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assemble Debug Help
Registers
A 16
BC 00 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 13
SP FF FF
Int Reg 00
Flag
S 0
Z 0
AC 1
P 0
C 0
Load me at
1 LDA 8050
2 ANI 01
3 JZ LOOP1
4 MVI A,22
5 JMP LOOP2
6 LOOP1: MVI A,11
7 LOOP2: STA 8061
8 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 8050
OK
Address (Hex) Address Data
1F72 8050 5
1F73 8051 22
1F74 8052 0
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
1F7E 8062 0
1F7F 8063 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
8050 - + 05
Update Memory
Line No Assembler Message
0 Program assembled successfully


Combinational Analysis
File Edit Project Circuits Window Help
Inputs Outputs Table Expression Minimized
a b | s y
0 0 | 0 0
0 1 | 1 0
1 0 | 1 0
1 1 | 0 1
Build Circuit
Arithmetic
Memory
Input/Output
Base
Selection: Pin
Name
Output?
Data Bits
Three-state?
Pull Behavior
Unchanged
Label
Label Location
East
Label Font
100%
Search