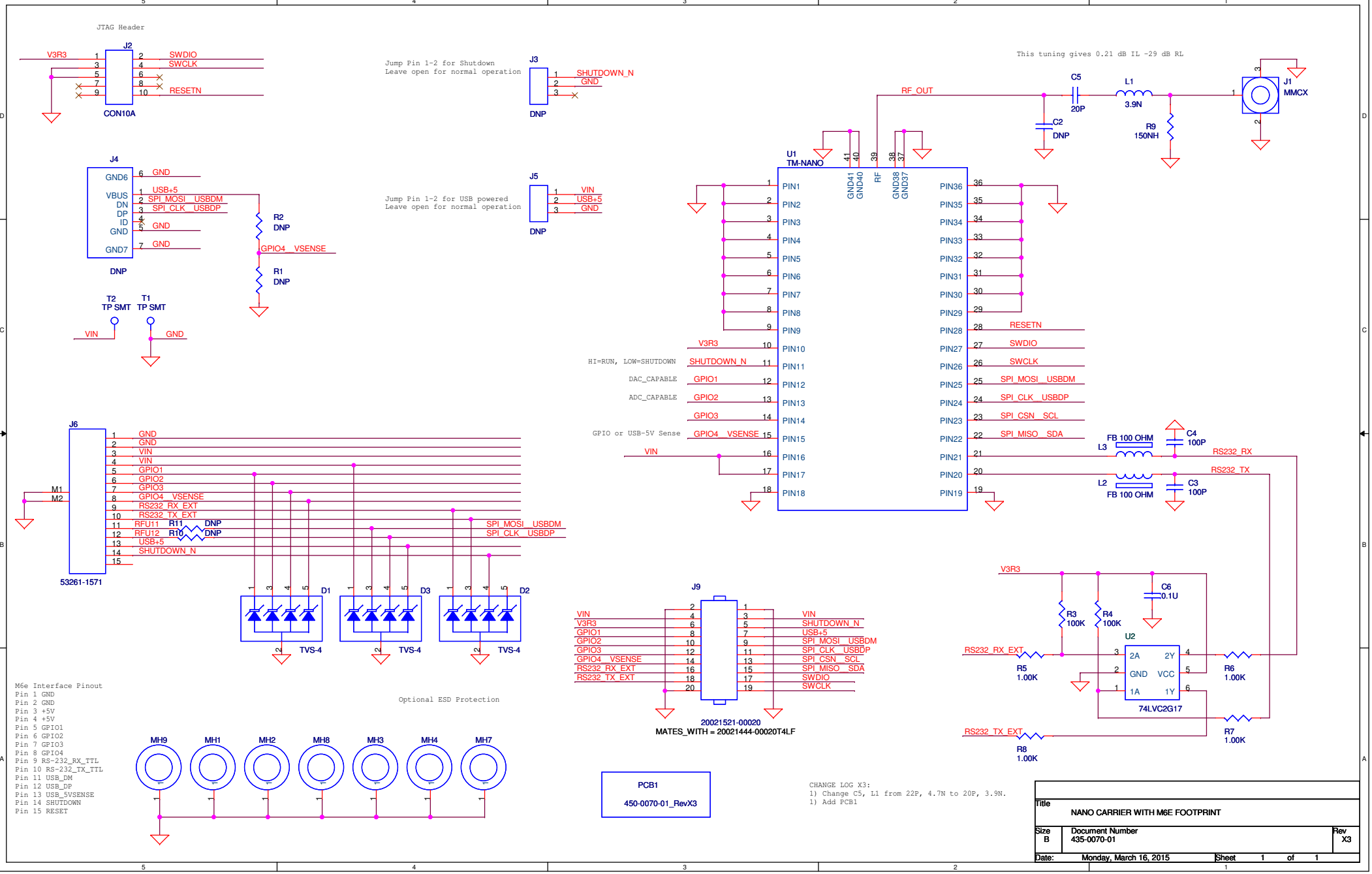
JTAG Header
J2
CON10A
V3R3
SWDIO
SWCLK
RESETN
Jump Pin 1-2 for Shutdown
Leave open for normal operation
J3
DNP
SHUTDOWN_N
GND
J4
DNP
GND6
VBUS
DN
DP
ID
GND
GND7
USB+5
SPI_MOSI__USBDM
SPI_CLK__USBDP
R2
DNP
R1
DNP
GPIO4__VSENSE
Jump Pin 1-2 for USB powered
Leave open for normal operation
J5
DNP
VIN
USB+5
T2
TP SMT
T1
TP SMT
This tuning gives 0.21 dB IL -29 dB RL
RF_OUT
C5
20P
C2
DNP
L1
3.9N
R9
150NH
J1
MMCX
U1
TM-NANO
HI=RUN, LOW=SHUTDOWN
DAC_CAPABLE
ADC_CAPABLE
GPIO or USB-5V Sense
SPI_CSN__SCL
SPI_MISO__SDA
FB 100 OHM
L3
L2
C4
100P
C3
100P
RS232_RX
RS232_TX
J6
53261-1571
M1
M2
RFU11
RFU12
R11
R10
DNP
RS232_RX_EXT
RS232_TX_EXT
D1
D3
D2
TVS-4
J9
20021521-00020
MATES_WITH = 20021444-00020T4LF
V3R3
C6
0.1U
R3
100K
R4
100K
U2
74LVC2G17
R5
1.00K
R6
1.00K
R7
1.00K
R8
1.00K
M6e Interface Pinout
Pin 1 GND
Pin 2 GND
Pin 3 +5V
Pin 4 +5V
Pin 5 GPIO1
Pin 6 GPIO2
Pin 7 GPIO3
Pin 8 GPIO4
Pin 9 RS-232_RX_TTL
Pin 10 RS-232_TX_TTL
Pin 11 USB_DM
Pin 12 USB_DP
Pin 13 USB_5VSENSE
Pin 14 SHUTDOWN
Pin 15 RESET
Optional ESD Protection
MH9
MH1
MH2
MH8
MH3
MH4
MH7
PCB1
450-0070-01_RevX3
CHANGE LOG X3:
1) Change C5, L1 from 22P, 4.7N to 20P, 3.9N.
1) Add PCB1
Title
NANO CARRIER WITH M6E FOOTPRINT
Size
B
Document Number
435-0070-01
Rev
X3
Date:
Monday, March 16, 2015
Sheet
1
of
1