

STATE Q1 Q0		N		MOORE OUTPUTS		
		0	1	boot	wr_square	muxes
S0	00	01, 11	01, 11	1	1	x
S1	01	11, 11	01, 00	0	0	1
S2	11	01, 10	01, 10	0	1	0
		q1* q2*, ready wr_root				

STATE Q1 Q0		N2 N1				MOORE OUTPUTS		
		00	01	10	11	boot	wr_square	muxes
S0	00	01, 11x	01, 11x	01, 11x	01, 11x	1	1	x
S1	01	11, 11x	01, 000	01,001	01, 000	0	0	1
S2	11	01, 10x	01, 10x	01, 10x	01, 10x	0	1	0
		q1* q2*, ready wr_root root						