

(Attending online)

## ELECTRONIC WORKSHOP – 1

### LAB 2 – MEASUREMENT OF RC, RL AND RLC CIRCUITS

2020102043

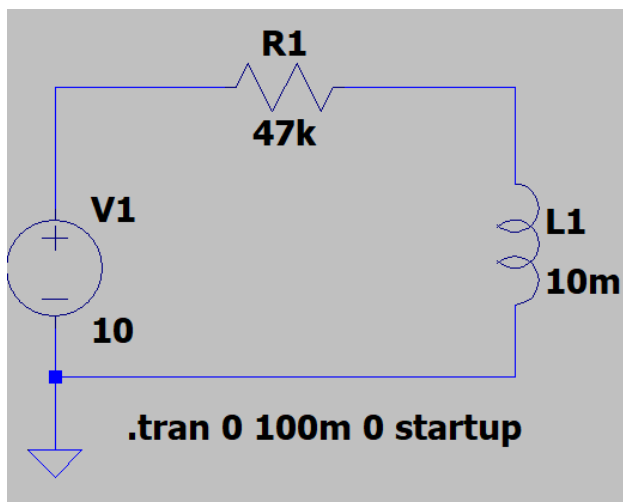
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#### 1) Introduction to RL and RC circuits

##### RL circuit

1) Time constant for the given LR circuit is  $T = L/R = 10 \text{ mH} / 47 \text{ Kohms} = 2.13 \times 10^{-7}$

Expected steady state inductor voltage = 0 (here and for the rest of this doc, exp is in powers of '10' not 'e').

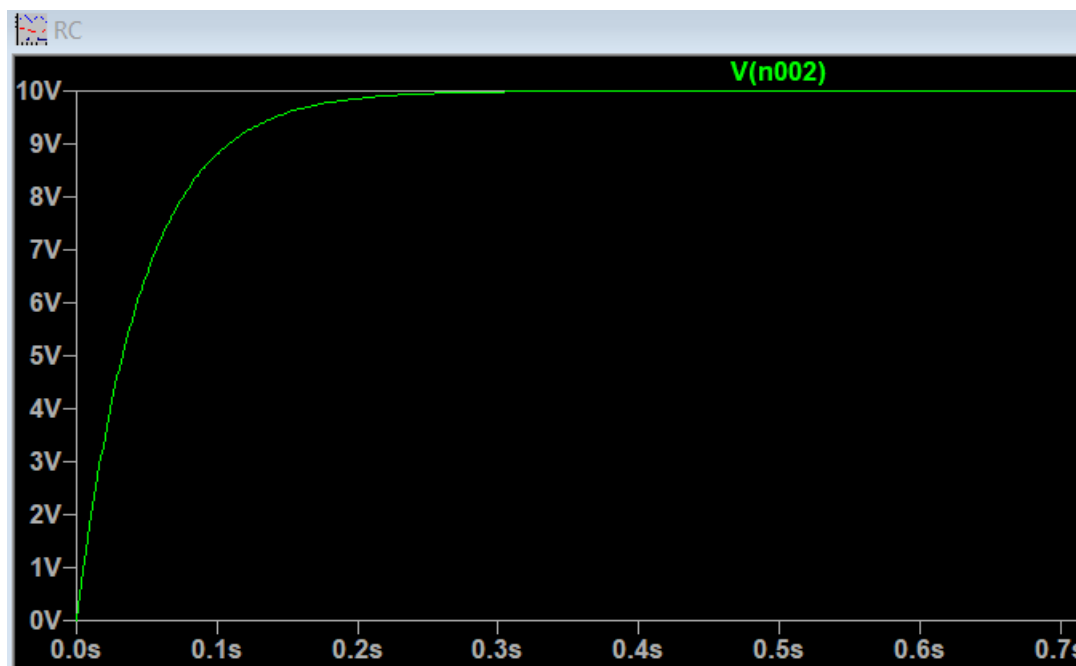
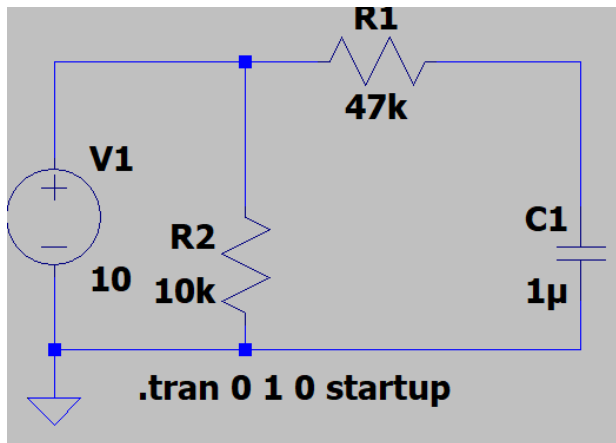


Voltage in steady state = 0.

## RC circuit

1) Time constant of circuit =  $CR$ . From Thevenin's theorem,  $R = 47k$  ohms and  $C = 1\mu$ .

Thus, time constant =  $47k \times 1\mu = 4.7 \times 10^{-5}$ . Expected steady state voltage of capacitor =  $E = 10V$ .



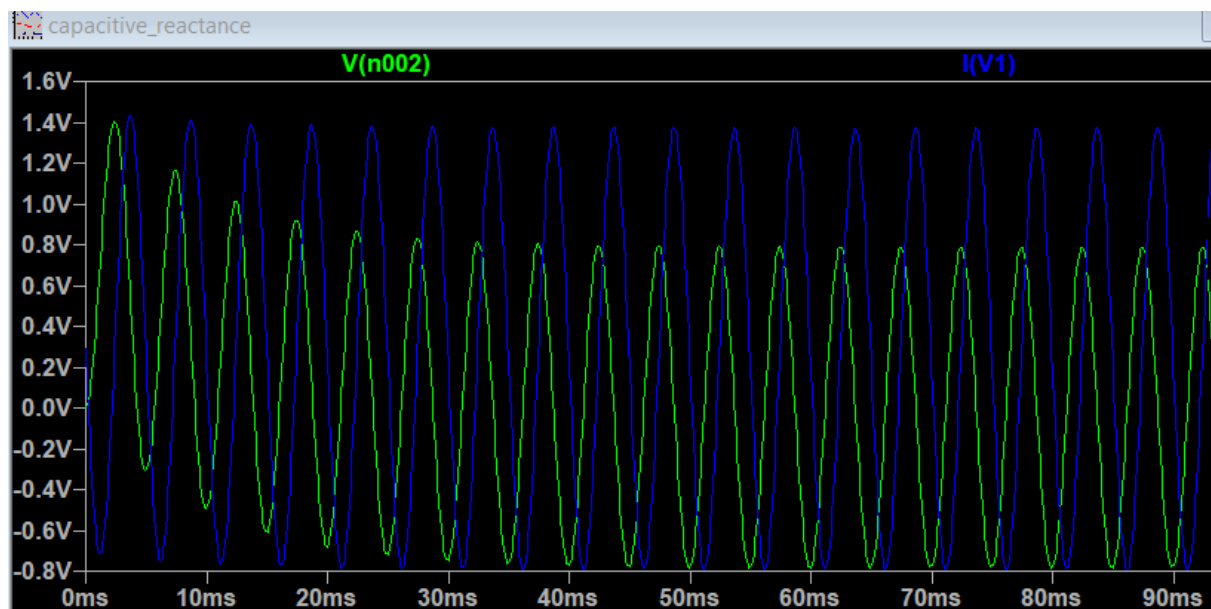
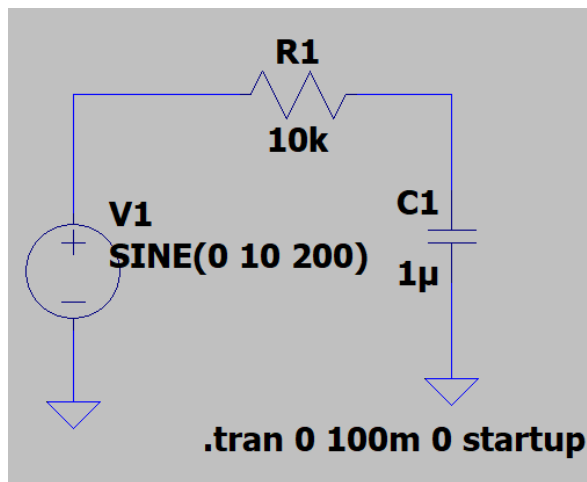
Voltage in steady state = 10.

## Questions:

- 1) Approximation of inductor at DC steady state would be a wire ie. it conducts perfectly.
- 2) Approximation of capacitor at DC steady state would be capacitor would be an open switch ie it does not conduct at all. Does not allow current to pass through.
- 3) After 5 time constants, RC circuits usually reach steady state.
- 4) They are usually in the form of a parabola or an inverse parabola and saturate at one point.

## 2) Capacitive Reactance

- 1) Circulating current is:  $V_{in} / R = 10 \text{ V p-p} / 10 \text{ k ohms} = 1 \text{ X exp}(-3) \text{ A p-p}$
- 2) Theoretical value of  $X_c = -j * (1/2\pi f C) = -j * (1/2 * 3.14 * 200 * \text{exp}(-6)) = 796.2 \text{ ohms}$
- 3) Peak capacitor voltage =  $\text{exp}(-3) \text{ A} \times 796.2 \text{ ohms} = 0.8 \text{ V}$
- 4) For  $2.2 \text{ uF}$ 
  - a. Theoretical value of voltage  $X_c = -j * (1/2\pi f C) = -j * (1/2 * 3.14 * 200 * 2.2 * \text{exp}(-6)) = 362 \text{ ohms}$
  - b. Peak capacitor voltage =  $\text{exp}(-3) \text{ A} \times 362 \text{ ohms} = 0.36 \text{ V}$
- 5) Capacitive reactance should decrease with increase in frequency.



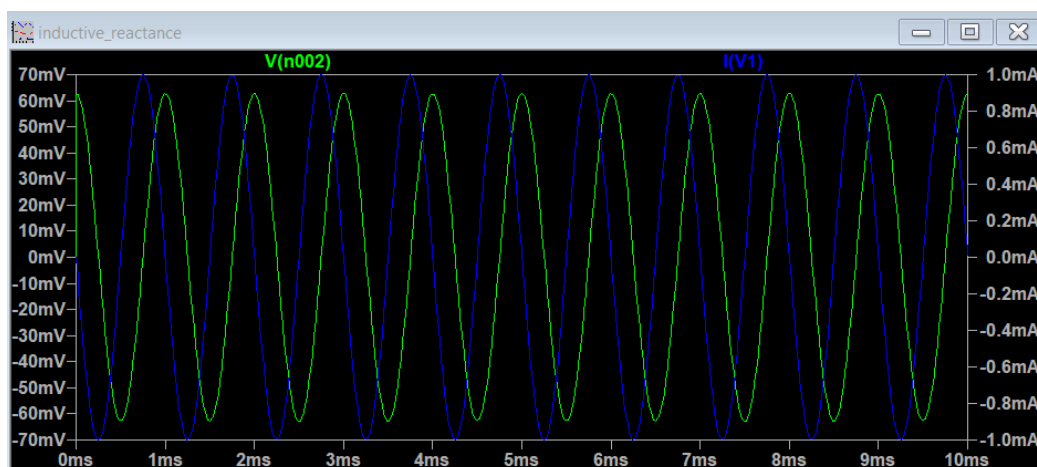
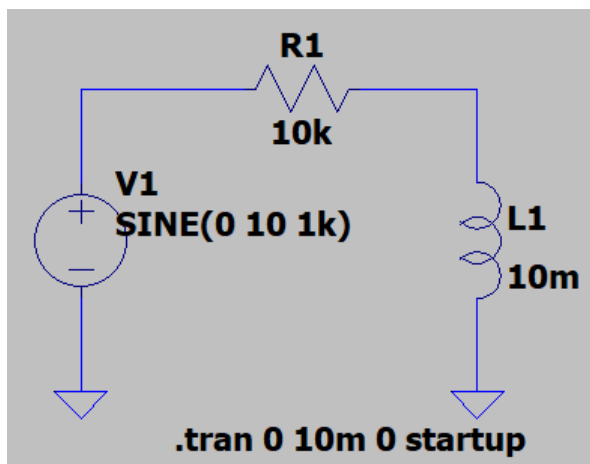
Current leads

## Questions:

- 1) Capacitive reactance and frequency have an inverse relationship
- 2) Capacitive reactance and capacitance have an inverse relationship too.
- 3) The resulting plots would still be of the same shape because its an inverse relationship
- 4) Capacitive reactance would be 10 times higher if the frequencies are 10 times lower. In this case we might not approximate capacitive reactance as 0 while calculating circulating frequency.

## 3) Inductive Reactance

- 1) Circulating current is :  $V / R = 10V / 10k \text{ ohms} = 1 \text{ mA}$ .
- 2) Theoretical value of  $X_L = j2\pi fL = j * 2 * 3.14 * 1000 * \exp(-2) = j * 62.8 \text{ ohms}$ .
- 3) Peak – Peak inductor voltage =  $1\text{mA} * 62.8 \text{ ohms} = 62.8 \text{ mV}$
- 4) For 1mH:
  - a. Circulating current = 1mA
  - b.  $X_L = 6.28 \text{ ohms}$
  - c. Inductor voltage = 6.28 mA p-p



Voltage leads.

## Questions

- 1) Inductive reactance increases with frequency. It is directly proportional.
- 2) Inductive reactance increases with inductance. It is directly proportional.
- 3) The reactance would have increased 10 times.

## 4) Resonance in RLC circuits

- 1) Parallel RLC

Resonance is achieved in RLC circuits if  $f = 1/(2\pi \sqrt{LC})$ ; thus resonance frequency for the given circuit is 968.6 Hz.

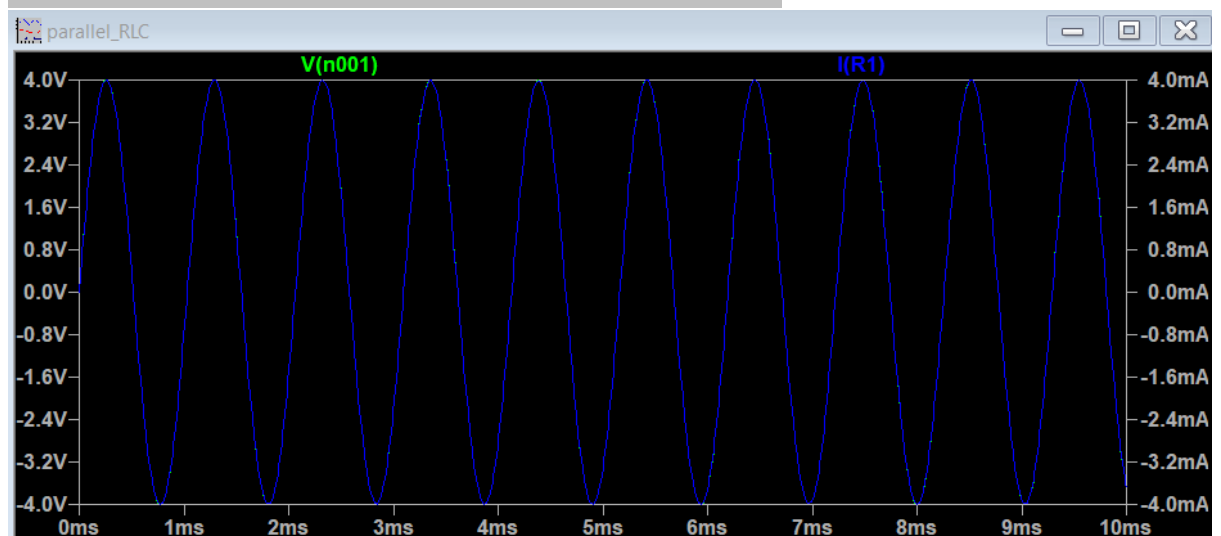
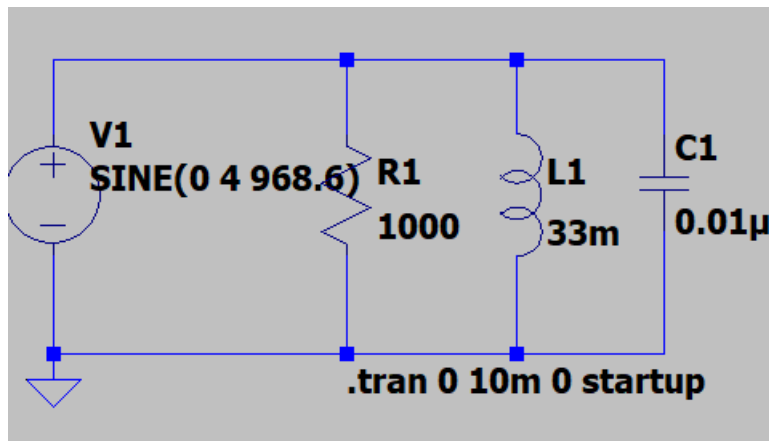
- 2) Series RLC

Resonance in series RLC circuit is also given by the same formula:

$$f = 1/(2\pi \sqrt{LC});$$

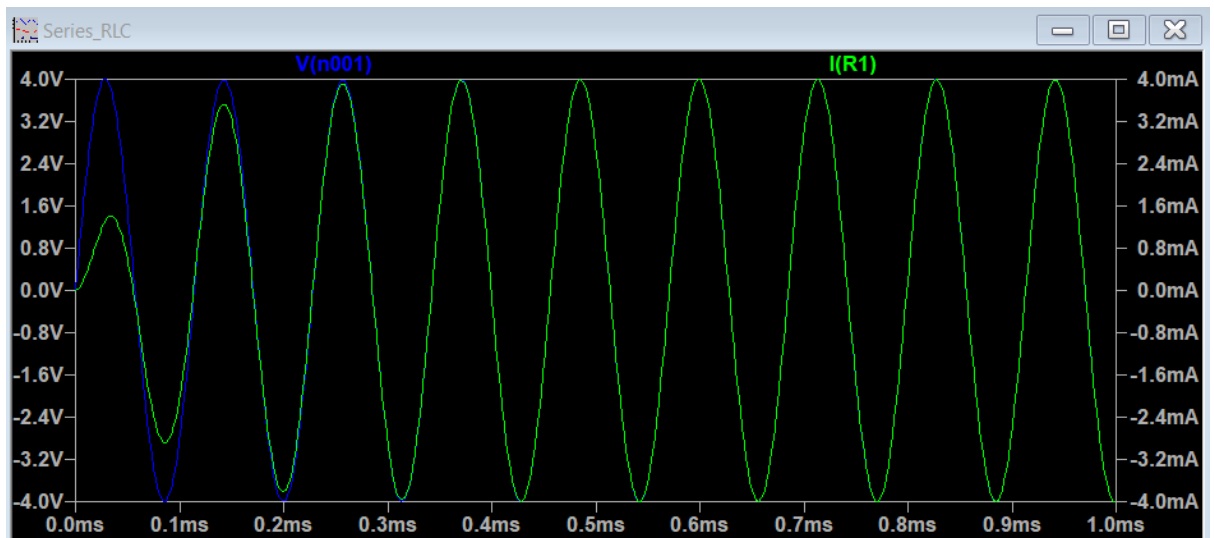
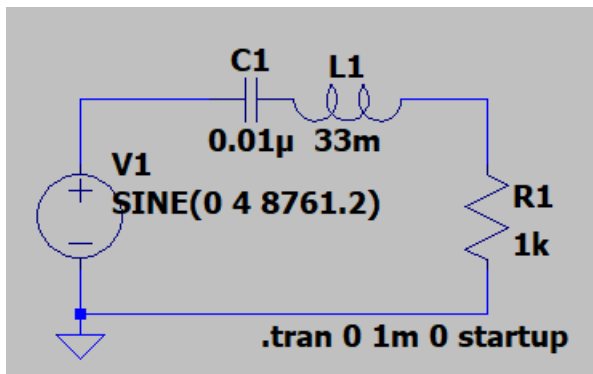
Frequency of the given circuit is: 8761.2 Hz

### PARALLEL RLC



In resonant frequency of 968.6, there is no phase difference between current through R and source voltage.

## SERIES RLC



No phase difference in current through resistance and source voltage in resonant frequency.