

I²C Protocol

208 - 01 - 007

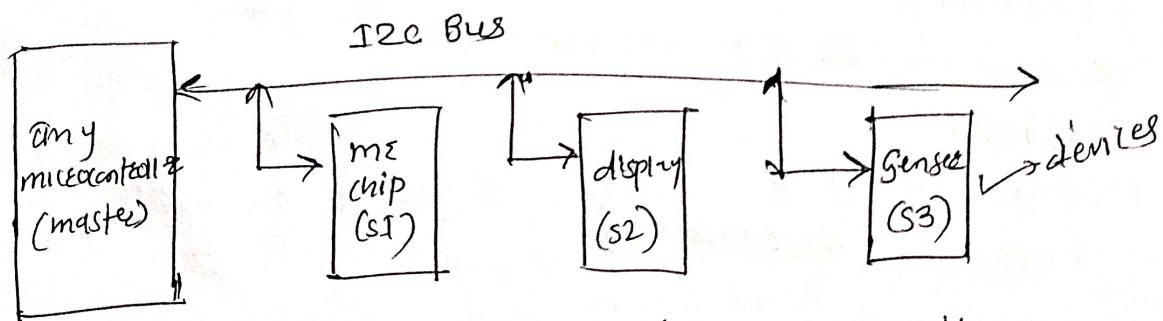
→ Inter Integrated circuit Protocol \rightarrow I²C / I²C / I²I^C

I²C was designed by Philips (NXT) for on-board IC communication (Short distance communication). On-board IC communi. \rightarrow On the same PCB connected communication. But now day company doing off-board communication. I²C used for short distance communication \rightarrow 2 meter distance range.

I²C designed for communication between two IC (Integrated circuit).

USB device :- Ethernet to connect USB,

- * UART device used for pair to pair communication, UART not designed for multiple slaves.
- * ~~But~~ why I²C so popular?
 - We can connect multiple I²C device to I²C Bus.
 - and you communicate with them.



In I²C multiple slave communication is possible.

- * In TV Remotes we used RC5 and ~~RC4~~ RC6 protocols used in remotes.
- * Every device has its own address, now that's why master can communicate with particular devices sensor, display, micro: controller chip has separate own addresses.

* GPS we can not connect here, It's UART compatible

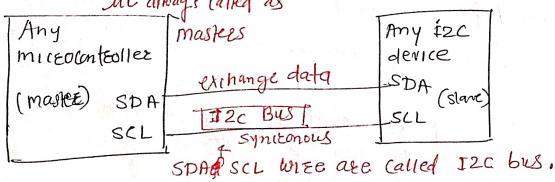
* Features of I₂C Protocols

1. I₂C was designed by Philips, now Philips Renamed as NXP → "Next Experience" also it is on-board IC communication.

2. I₂C is two wire communication Protocol.

- (1) SCL → (Serial Clock)
- (2) SDA → (Serial data)

I₂C always called as



List of I₂C devices

+ AT24C08

(EEPROM)

(RTC) → Remote transmitter Receiver

+ DS1821

(Temp sensor)

+ ADXL345 (Accelerator)

+ OLED display

+ ADS1115

(ADC)

+ etc.

Why SCL used?

To generating clock pulse & clock pulse used for synchronising / synchronization.

* why used SDA?

→ Used to exchange data

* In the master and slave communication Only master can generate the clock pulses,

3. I₂C is half duplex and synchronous serial communication protocol.

4. I₂C is multi slave and multi master communication protocol.

5. Speed of I₂C protocol (max upto 5Mbps)

* Where we should do speed setting?
↳ Only master can do, gives and slave has by default speed setting. (company designed slave that is will gives default speed setting to slave).

* Microcontrollers are much faster than devices.

→ upto 100 kbps : standard speed mode.

upto 400 kbps master set to be Fast mode (Fm)

upto 1 Mbps : Fast mode plus (Fm+)

upto 3.4 Mbps : High speed Mode (Hs)

upto 5Mbps : Ultra high speed mode (Uhs)

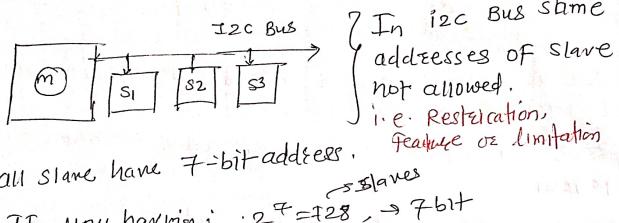
megabit per second

maximum speed of UART upto 4 Mbps.

6. Every I₂C device has its own device address. size of device address can be 7 bits / 10 bits.

* The device address is given by cheap designer and we can not change it.

7. Maximum num of slaves on the bus
 * The maximum no. of slaves that can be connected on the I₂C bus are limited by address space.



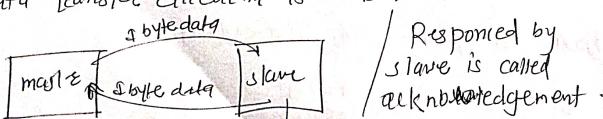
If you having: $2^7 = 128 \rightarrow 7\text{ bit}$
 $2^{10} = 1024 \rightarrow 10\text{ bit}$

If you have combinations of 7 bit & 10 bit addressable slaves,
 $1024 + 128 = 1152 \text{ slaves}$

* Major problem in I₂C is :- If master is not working then slave will become useless.

8. I₂C is an acknowledgement based protocol.
 master will get ACK after every byte written into slave.

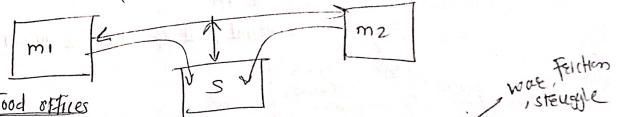
9. Data transfer direction is msB first.



Master & slave both send data MSB to LSB.
 Use of UART LSB first.

Slave will response after one byte received.

10. I₂C supports multi-master arbitration management (avoid data collision & corruption)



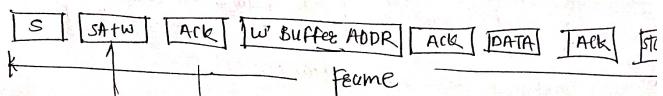
Arbitration :- To avoid transmission conflicts on a n/o the technique which is used is called as arbitration.

* ~~PKA IMP~~ I₂C EEPROM BYTE write algorithm?

This communication is required at least 8 step at most 10 step with this algorithm master can write into random location of the EEPROM memory.

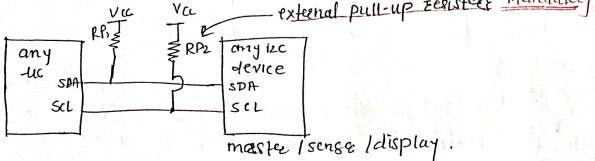
- ① master issue start condition to begin this communication session.
- ② master issue slave address with write option. master take acknowledgement from slave.
- ③ master issue address of buffer of slave where it has to write into.
- ④ master take acknowledgement from slave.
- ⑤ master issue data for that write buffer.
- ⑥ master take acknowledgement from slave.
- ⑦ master issue stop condition to end this communication session.

All steps in one frame



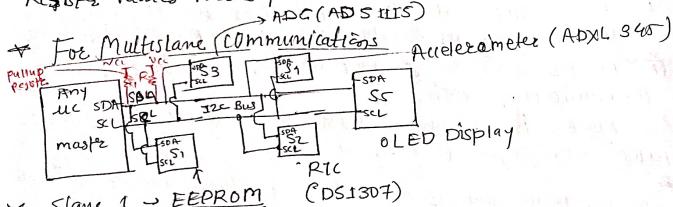
I₂C Hardware Setup of I₂C Protocol.

It is also called as Bus Setup.



External pull-up resistors?
All the I2C devices are open drain devices. To adjust their input voltage level ext pull-up resistors must be connected on the bus.

Registers values from I₂C to ADC.



Slave 1 → EEPROM (AT24C08)
We can use two masters, 1 slave connect to master A, connect master A & slave connect to master B.

Master can communicate to all slaves but one by one.

I₂C Data Frames

- ① I₂C byte write frame
- ② I₂C byte read frame

In UART we have only one data frame, There is no write or no read.

In I₂C protocol there are two frames.

- Method of writing 1 byte into slave.
- ③ I₂C byte of reading 1 byte from slave.

29/09/2021

→ 93.0% complete

Protocol is always explain to master only.

- ① Master is writing → I₂C byte write frame
- ② Master is reading → I₂C byte read frame.

I₂C Byte Write Frame

start condition	write 7 bit slave address + write option	ACK (from slave)	write 8 bits memory address	ACK	write 8 bits ACK data	stop condition
slave address + option read/write	01111111	1	00000000	1	00000000	1

* I₂C protocol they have not given the CRC8 checking mechanism. If master send some data to slave (8bit data) then if master send this data but due to some noise some bit are corrupted but still slave give you acknowledgement, data is zero corrupted. So, I₂C not check CRC8.

* Why I₂C CRC8 checking method not used?
Because I₂C used for send the data for short distance. In short distance protocols data corruption chances very less. that's why.

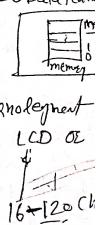
I₂C Hardware Setup / I₂C Bus Setup

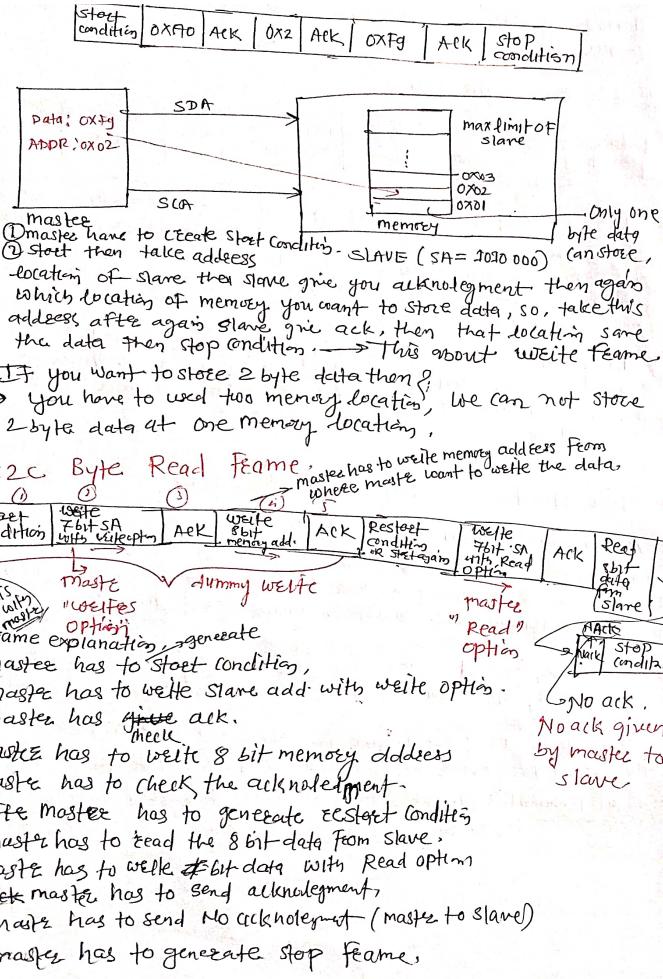
* Acknowledgment given by the slave.

If slave is not available then there is no point of communication. If the slave is available & data transfer but slave not giving acknowledgment the master again send the data frame.

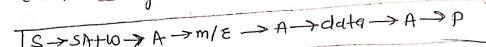
Note: Every I₂C slave has internal memory.
Memory size changes from device to device.

If slave not present you if slave not send acknowledgement then send the error msg, we can send msg through LCD or serial terminal (If error msg is max then).
→ It can display no. of char.



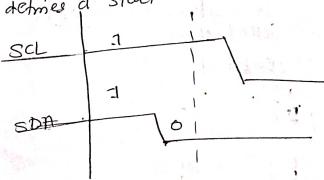


In case of read frame, from start condition till ack is called dummy write. ① to ⑤.



S → SA+R → A → Read
 ↪ If not allowed to write mem. Read then its not allowed
 ① If master write slave address with write condition, then master allow to write memory address.

I₂C Start Condition
 A high to low transition on the SDA line while SCL is high, defines a start condition.

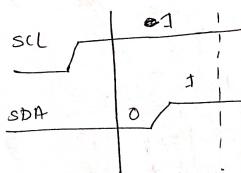


SCL = 1 } optional
 SDA = 1 }

SDA = 1 } Starting condition
 SDA = 0 }

I₂C Stop Condition

A low to high transition (change) on the SDA line while SCL is high, defines a stop condition.



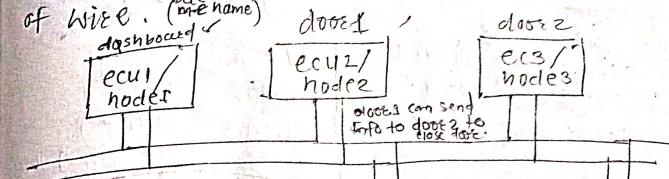
AKM - CAN

CAN Protocol

CAN protocols → Replace Bunch of wires with
two wires, (complex wire with two wires).
(it's used mostly in Automotive but because of its feature it's popular).

ECU → Electronics control unit

No. of microcontroller units connect to only single
of wire. (one name)



This ECU uses connection to each other that's why we can do operation from ecu2 to ecu3. Because internally it's connected to each other.

In side of CAN protocol, if will work work on multimaster slaves.

ECM → Engine control module /
BCM → Break Control Module /

Dashboard → It will take information of seat, battery status information, engine control module, It will print the information on display, switch connected on the dashboard & it can control the things, light, switches.

Suppose:- In car door is close, ECM not send sum any information to dashboard, then dashboard will not show information. (FF closed).

(CAN protocol implemented in 1983 by Robert Bosch, first multivendor implementation 1981, used this CAN

V10 → Advance Control module (this auto
(Directed by: Robert Bosch).
Generally ABS they provide BCM).

In that used BCM (Break control module)
(one of safety feature of vehicle).
There is difference between Break control
module, (locking wheels/unlocking wheels).

Old BCM.
These was drums
Breaking system.
These was Rubber.

New BCM
Disk Brake,
ABS, This will
Advance Breaking
System,
Now we can control like
Speed easily.

* Airbag Control System:
(when vehicle crash into something the electronic control unit (ECU)
for the airbag system react in milliseconds)

* Role of CAN protocol In CAR.

why we used ECU in CAR.

To exchange the data from one ECU to
second ECU. Internally communication to
each other. If we have two wire are
called as CAN BUS.

(In CAN Protocol LRC error detection is three if single bit
error then it will detect and correct it.

* Already we have UART BUS Then why we used CAN BUS?

Because UART BUS cannot cancel noise.
Outside we have wind noise, Because of
this type of noise can be (cancel) the
data, when we will transfer the data (get
noise cancellation option is not UART) (cancel) noise?

As we can used any other material to reduce noise.
So, UART not used multimeter, It is
designed For PL to PL, Error detection
is alternative.

* Why I2C & SPI is not used in
the CAR? (I2C & SPI used for Onboard Communication)
Because I2C & SPI designed for short distance
This is offboard communication.
Suppose, One ECU suppose in front of ECU

ECU detected to send the other ECU, for
that there will be distance more, more noise
of engine, so, these preferable is CAN only
It will transfer long distance data easily
without any noise interrupt.

Whenever device sitting in the form of truck
and it send some Indication it should go from
one ECU to second.

CAN - ISO 11898 → standard Number, which
can represent CAN protocol.

CAN is a serial communication technology used
especially for reliable data exchange between
electronic control units (ECUs) in the
automobile without using any host computer.

* OBD Onboard diagnostics

What is OBD? (On-board diagnostics)
It is vehicle self capability of reporting
errors. (It look like digital multimeter).

* UDS protocol → UDS protocols used inside of
the CAN protocol.

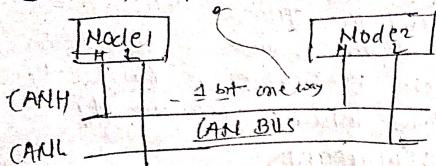
On-board Diagnostics

① CAN is two-wire protocol

— CANH → CANHigh

— CANL → CANLow

② Half duplex



CAN Bus used inside cars buses, trucks, JCB.

③ CAN protocol uses different wiring method.
use for data bit transmission.

* On Hardwires they will twist cable, used
TO: EMI/F → Electronics magnetic interference
avoid noise.



④ It is multimaster slaves used.

⑤ CAN protocol is Broadcast

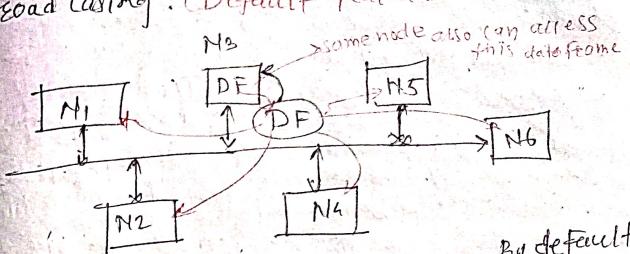
⑥ Asynchronous serial communication protocol.

⑦ Electronics Control Unit.

⑧ Every ECU → master

⑨ If one ECU not work in the bus
then all ecu will work properly.

* IF any ECU sends frame
all ECU can access this, that is called
Broadcasting. (Default feature of CAN).



IF N3 creates data frame, By default it will be broadcasted. But we need to give information only that particular ECU (Node) then there CAN protocol (This is part of protocol setting only) one setting is message acceptance filter setting (This setting is in ECU), with the help of this setting which ECU should accept the data frame msg, & which should ignore.

message acceptance filter → Only available for accepting the msg, of which ECU have to be Ignore.

Interrupt :- is the signal/disturbance which breaks the normal flow of execution of CPU to direct its attention to another flow of execution with higher priority as against the current flow execution.

→ There is 8 interrupt

- ① ISR → Interrupt service Routine :- Refers block of code interrupt to be executed in response to the interrupt misc.
- ② Interrupt Vector :- Refers starting address of where ISR begins from.
- ③ Interrupt vector Table :- collection of one or more interrupt vectors at same predefined location of CPU architectures.
- ④ Hardware Interrupt :- Refers an asynchronous event.
- ⑤ Software Interrupt :-
- ⑥ Maskable Interrupt
- ⑦ Non-maskable Interrupt
- ⑧ Non-spurious Interrupts :- maskable & non-maskable interrupt are called non-spurious interrupts.

In ARM two External Interrupts:

IRQ → Interrupt Request

FIQ → Fast Interrupt Request

Arbitration :- The arbitration mechanism is used to ensure that only one master has access to the bus at any one time. The arbiter performs this function by observing a number of different sequences to use the bus and deciding which is currently the high priority master requesting the bus.

CAN → 2

In-vehicle protocols

In luxuries car more than go ECUs.

All buses connected to the ~~to the~~ single bus. This is the network only and this network in side vehicle is called In-vehicle electronics networks. is nothing but the except can protocol only.

* every ECU have a microcontroller, that's why it a master.

* maximum speed of LAN protocol is 1Mbps bps
50 meters range.

After 50 metres if you want to use them decrease the speed ~~(signaling rate)~~ mbps.

Eg: 500meters at 125 kbps (kilobit per second).

* what is Arbitration?

* MAX Node on CAN Bus :- Upto 30 nodes we can connect, If you want to connect more than 30 nodes then you have connect more bus input impedance. (low-voltage, high current) Current is called high impedance circuit.

For you have to give high current.

* CAN available in two versions \rightarrow both designed

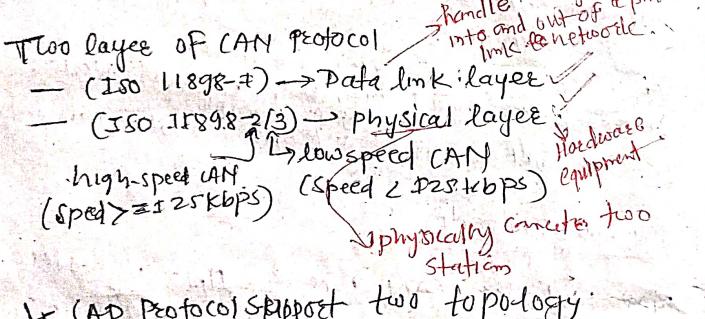
① Standard CAN (version 2.0A) 11 bit identi

② Extended CAN (version 2.0B) 29 bit identi

- Identifiers represent type of msg.
- ① engine control message (ECU-3)
 - ② light control message (FCNT)
 - ③ light control message (EAR)
 - ④ crash detection message (ECU-4)
 - ⑤ ABS message (ECU-5)
- Arb by ECU
- Priority ①, ②, ③, ④, ⑤
- For Standard CAN → support 8 bytes
 $2^{11} \rightarrow 208$ to 207 this no. are the used as Identifier in data frame.

For Extended CAN →

2^{29}



- * CAN Protocol supports two topologies:
- ① Line Topology → The host directly connects all nodes on one bus line in a line topology.
 - ② Bus Topology. In the various devices in the network are connected to a single bus line.
- * Low cost, lightweight Network; Only two wires are used for ECU connectivity and one shield wire, that's why light weight network, nowadays ECU and MC are used that's why low cost.

Standard CAN support 8 bytes while CAN FD protocol supports 8 bytes data in one frame.

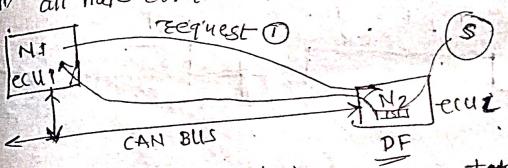
CAN Flexible Data Frame (CAN FD) → Advance version of CAN

Interview Question
 What is Difference Between (CAN) and (CAN FD)?

→ Standard CAN and Extended CAN is a CAN Both are CAN protocol versions, CAN FD is an advance version.

* CAN support Arbitration (IF multmaster happens in protocol then arbitration will come in place)

* Remote Request Facility
 all nodes can transmit & receive the data.



The ECU can send the request to other ECU to send the data frame.

* To reduce their traffic if they have given this Remote Request Facility.

Remote Request Facility: It says like that don't send me unnecessary data, whenever I gave the request then send me data.

This is not an automatic process, for that we have to write a logic.

- * Support auto-retransmission of frame.
- * DF → Data Frame. → Inside data frames there is priority. (Inside of DF).
- * Every Node can trans-Receive.
- * Any node detect five errors.
- * S2 time Nodes can transmit data. 3.3 node will get block. (some time only).
- * Protocol supports different errors, bit errors, CRC errors, frame errors, over errors, stuff errors.
- * ACK! - Acknowledgement
- * CRC! - Cyclical Redundancy check.



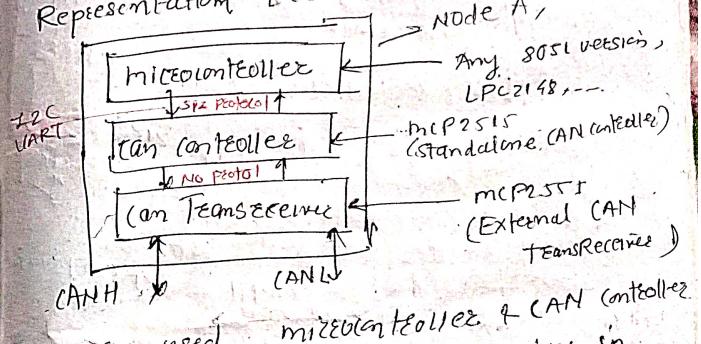
Application of CAN Protocol

Elevators, escalators, Building automation, Medical Instruments and equipment.

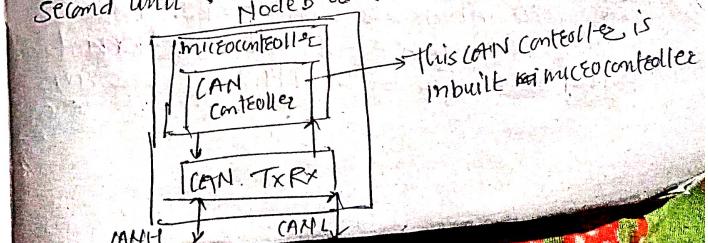
→ If voltage is +ve it will send '1' after five '0' bit, if -ve then it will send '0', after five '1', bit.

MARCH 24
Now, what is component inside of ECU.
* Inside every ECU you have microcontroller,
① CAN controller, ② CAN TransReceiver
③ Serial port (uart).
Some ECU having ① Sensors, ② LEDs & Bulbs
③ Motors ④ Display ⑤ Switches
⑥ used with ECU
done power window) ⑦ LED Bulbs ⑧ Components
sensor, actuator mandatory, But
microcontroller, CAN controller
CAN TransReceiver used compulsory, every car.

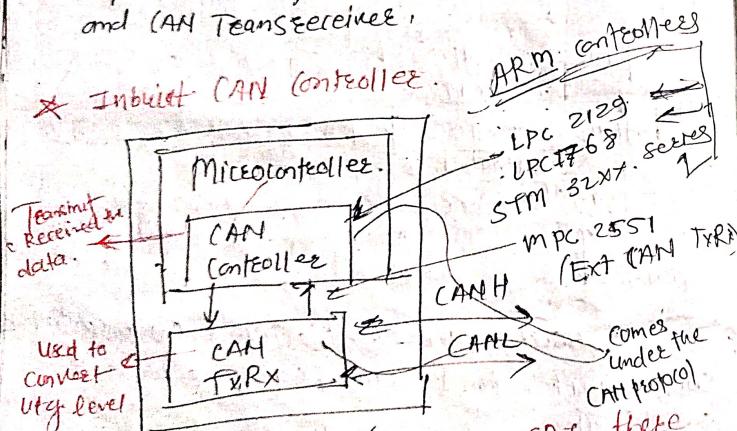
Representation ECU



* We can use microcontroller & CAN controller in single unit, and CAN TransReceiver in second unit. Node B of ECUB



- * Why company used external CAN controller?
 - Because there many companies used it, they work on that, they will not change total project operation they used externally. (Ans)
 - But, CAN externally used then we need to add protocol, if with want inbuilt CAN controller
 - no need protocol if want to change project but want to add new feature then company used the external CAN protocol controller, if used the external CAN controller then you have to used protocol for communicating SPI protocols here.
 - * There is no protocol used between CAN controller and CAN Transceiver.



- * If there's inbuilt I2C, UART SPI there is not IC numbers, If it is external protocol then there's I2C Number, LPC 2129, etc.

In ECU there is DLL and PCL
 ① Data link layer, ② Physical layer.

CAN controller → is called Data link layer, whatever data given to CAN controller, it link through the DLL.

CAN TX/RX → Physical layer.

A Role of CAN controller (called)

1. Create & send data frame (messages)
2. Receive & store messages (data / remote frame) (It has inbuilt buffer)
3. Error handling
4. Bit-stuffing
5. Bus-off handling
6. retransmission of lost message
7. Arbitration (Received all bus & then send which bus has higher priority)
8. Baudrate setting / speed of transmission
9. Message accepting setting
10. Interrupt microcontroller upon transmitting & receiving as well.

* Suppose you have 10 ECU for broadcast it's broadcast should be same (Otherwise they will not synchronised.)

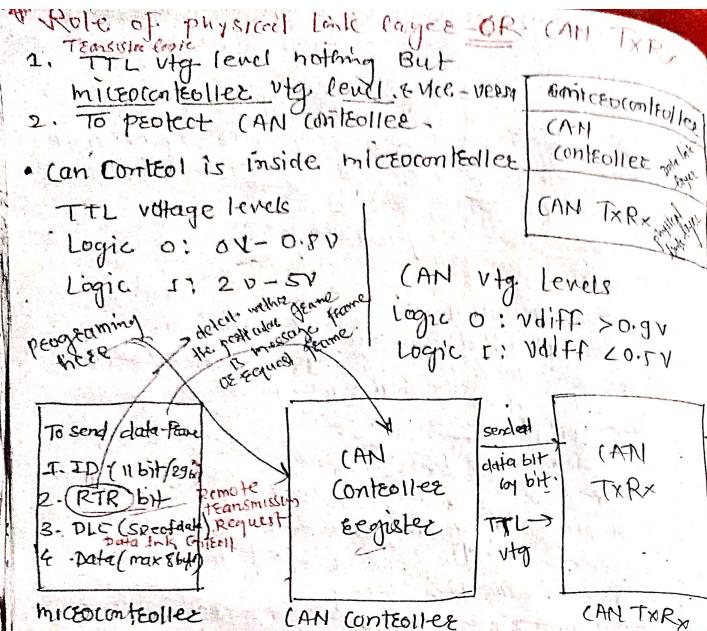
* How will you gate the broadcast?
 → Use one register, used formula: $\frac{\text{no. of bits per signal unit}}{\text{no. of bits per signal unit}}$.

* CAN controller interrupt facility also.

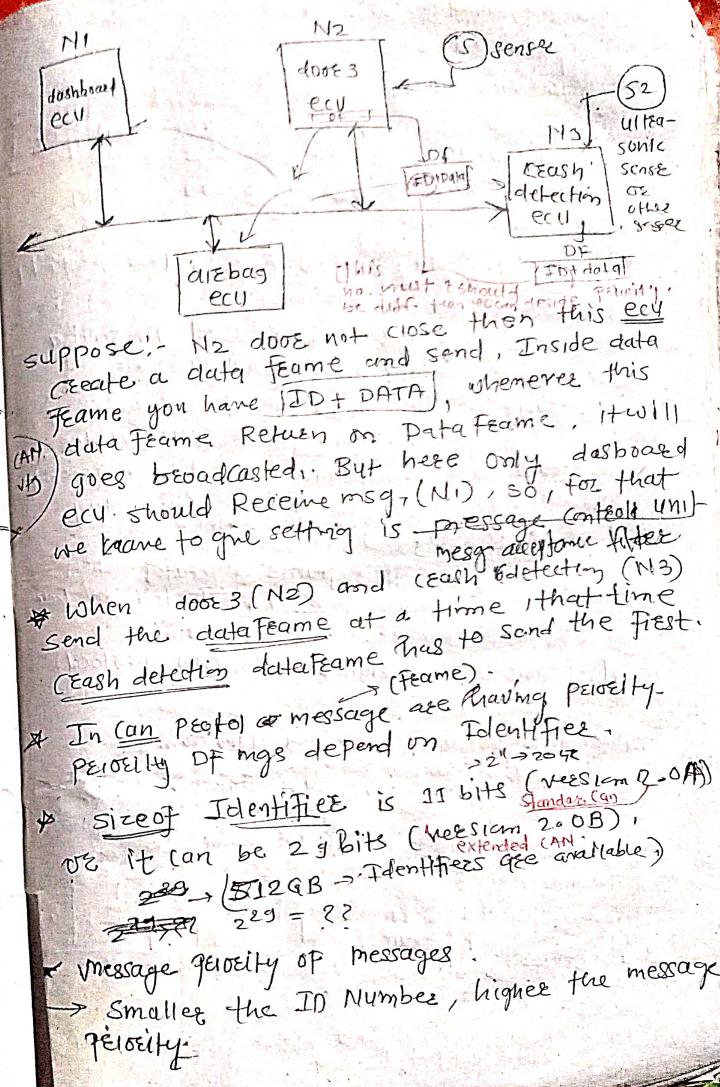
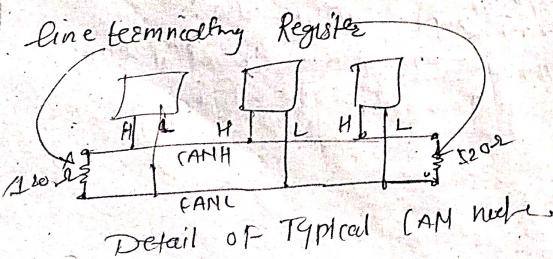
* CAN controller is nothing but Data link layer.

* ISO 11-8 → it is a form of data link layer.

* RQ



- * We can give data from Sensors also.
- * If DF msg is zero then also it will send 1 byte.
- * Data frame is nothing but sequence of binary.
- * Hardwired or also called bus setup



Example: Priority with ID

- 10, 500, 300, 25, ID → Highest priority
 1. Gear selection → ID → Less ID has less ID No.
 2. Door status (open/close) → 25
 3. Engine temperature → 500 (Priority)
 4. seat belt status → 300

* Identifies play major role in the Priority

* Data Frame :- This frame is used to send a data within a CAN Network.

* Remote Frame :- This frame is used to request a data frame from ECUs / Nodes.

* Error Frame :- This frame is used to represent error (transmitted when any ECU detect the error).

* Overload Frame :- This frame generated when node is busy. (In today's date ECU will not generate overload frame).
 (This frame less importance in the CAN protocol)

* Difference Between Standard data frame & Extended data frame.

→ This is the type of data frame in CAN.

* Standard data frame → Identifier (11 bits), Data (8 bits), CRC (8 bits), Ack (2 bits), EOF (1 bit), F (1 bit), S (1 bit)

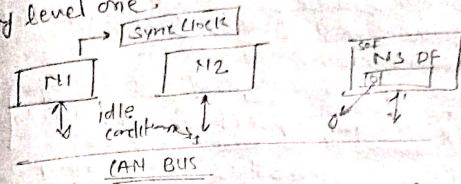
SOF	Arbitration Field	Control Field	Data Frame	CRC Field	Ack Field	E	I
1bit	12 bits	6 bits	8 bits	8 bits	2 bits	7 bits	8 bits

SOF → Start of frame
 EOF → End of frame
 → Cyclic redundancy check

Extended data frame

SOF	Arbitration Field	Control Field	Data Field	CRC Field	ACK Field	E	I	F
1bit	32 bit	8 bit	8-bit	16bit	2bit	3bit		

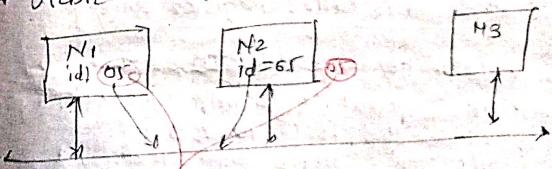
* SOF (1 bit) → idle condition of CAN Bus represented by level one.



clock synchronization when the any one node start of the frame, all node clock synchronised same. If (Data must be same) → if diff they will not synchronised.

In CAN protocol → 0 → Dominant Bit
 1 → Passive Bit
 It is single dominant bit (0).

* Arbitration Field (12 bits) (first 10 bits)



If both Id are same
 If both node becomes winner in arbitration process then both node will transmit data frame after arbitration field. That lead to "bit error".

Data length field → to send data + header to data frame
(How many bytes of data)

A Control Field (6 bits)

- | I. | TID (05) | Remote Request | 5 | 4 | 3 | 2 | 1 | 6 |
|----|--------------|----------------|------------------|-----|------|------|------|-----|
| 2. | RTR bit (0) | frame | IDF | RBD | DLC3 | DLC2 | DLC1 | DLC |
| 3. | DLC (2) | | 0 | 0 | 0 | 0 | 0 | 1 |
| 4. | Data (10,20) | | | | | | | |
| | | | <u>DLC Field</u> | | | | | |

* Data Field (max 8 bytes)
This field consists of a data to be transmitted within a data-frame.

This is for experience people value.
One date frame you can send 8 byte data

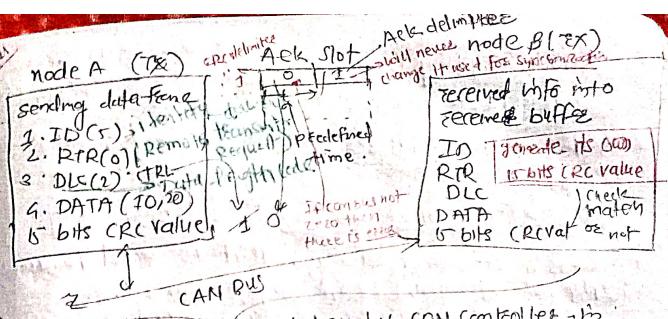
If you want send more than 8 byte data
CAN need CAN top layer

cyclic Redundancy check

ECC Field : (16 bits) (used for checking method)

Receiver Buffer → is called Register Only

Suppose node A generates the ID, RTR(0)
 DLC(2), DATA(10,20), & 15 bits CRC value
 and it sends to the node to B means
 It will be broadcasting, after that node (B)
 will receive this data and generate same
15 bit CRC value and matching with ~~node A~~
 Then if data value is same there will be
 no error, otherwise error, and no RTR, there
 will be acknowledgement ~~will be generated~~,
 RTR if it has to write '0'



2 CAN taken by CAN controller is
Error checking that Data link layer will be transmitted.

IF there is error, then node A will
the data, it happened automatically.

- + EOF → Bus Become free. (Represent end of transmission).
- + EOF → is sequence of 7 consecutive excessive bits (1111 111)

TPS (3 bits) (Inter Frame Spire)

JPS (3 bits) (Inter frame space)
It is used to provide a small delay (or pause) between the last frame & next frame.

CAPL Scripting is a C-like language.

Sending & Receiving a data-frame using CAPL Script

* CANoe & CAN Analyse.

"Kappler"

CAPL → Communication Access Programming Language

- ① It is a scripting language that is used to access CAN protocol with logical operation &.
- ② ~~but~~ CAN n/w using the script code which is almost like - c.
- ③ This script can be used with Vector CANoe & Vector CAN Analyse.
- ④ Using CAPL we can do simulation also,

It will work like Flash magic.

Example:-

```
includes {}  
variables {}  
int f;  
for(i=0;i<5;i++)  
    write ("Hello World!!");  
}
```

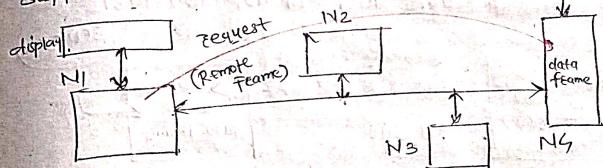
13/11/19

Data Frame:- To exchange data.

Remote Frame :- To request data from other nodes

Data Field → is not available in Remote frame

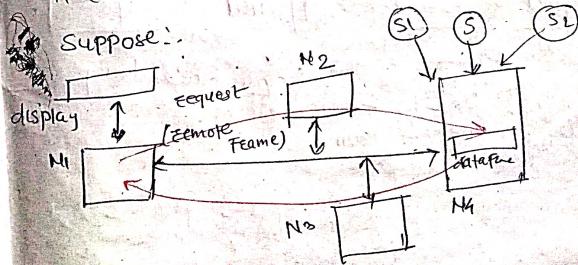
* Remote Frame :- the main purpose of Remote Frame to reduce the traffic, ^{sense output always 4 byte.} suppose!



when data transfer to N4 through the sense, But N4 not send that data automatically to all. E.g. if N1 want some data, N1 will request to the N4 to send the data. Because of that also traffic will reduced. If three Node want data that will request. Only, if so, that work ~~data~~ Remote frame, to request to send data.

Means N4 send the Remote Frame to N1. For that N4 has to create data frame then it can be send ~~data~~ remote frame.

Suppose:-

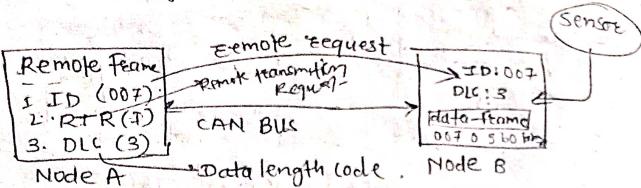


If there is two or more sensee in above diagram then ~~you have~~ Node N will send the Request to Node M but frame which sensee data should receive; so for that every sensee have separate Identifier.

DLC & ID;

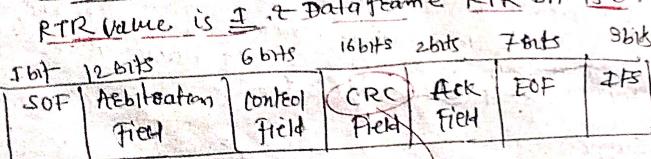
Rule to Respond Remote Frame

In Remote Frame data will not available.



Node B has to data frame to accept the Request to Remote frame. Both has to same ID, Identifier, same DLC, and +

Remote Frame & Data Frame is same except data Field - it is not in Remote Frame and RTR value is 1. & Data frame RTR bit is 0.

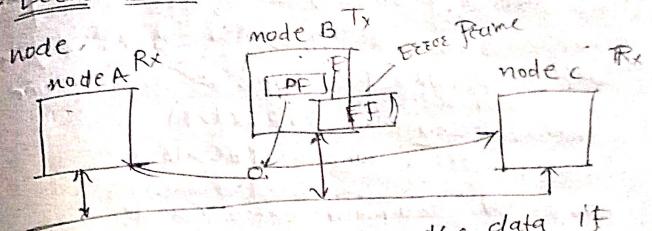


Types of Remote Frames

1. Standard Remote Frame
2. Extended Remote Frame

* Using dashboard, it can say that which door is close or open.

* EEOE Frame :- Suppose, there are 3



When node B transmitting the data if he detect the some error after ~~end~~ time he detect the some error then send the EEOE frame to Node B first send the EEOE frame and every node have capability to detect and receive the error transmitted as well as receiving the error transmitted by other node.

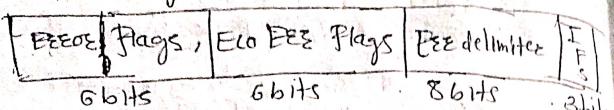
EEOE Handling is controlled by data frame totally depend on the data frame layer, EEOE handling is controlled by CAN controller.

Total fine EEOE in EEOE frame:
two are detected by transmitter → 2
three are detected by receiver → 3

If the transmitted node detect the error then transmitted node send EEOE frame. Some for receive, If the EEOE detected on the bus then all node will receive the EEOE.

If ongoing transmission is there, then all data will get stop. FEC has higher priority than Data Frame & Remote Frame.

Eeeoz has only three Field! -



~~Eros.~~ Flags depend on type of eros frame

1. Active EEECE Frame ($TEC < 128$)
 2. Passive EEECE Frame ($TEC > 128$)

(TEC) \Rightarrow Transmit Error Counter

This counter increments every time when a node sends an IEEE frame.

(REC) → Receiver EEE Counter &
→ This counter is incremented upon receiving
an error frame.

TEC & REC values, these are numbers

- * EEEor Confinement mechanism is CAN ECU's.

→ If EEEor generates contentionous frame then EEEor Confinement mechanism.

* The node who sent EEEor frame will increment REC value by 8.

* The node who receive EEE frame will increment REC val by 1.

* Upon successful transmission of data/remote frame TEC val. will decrement by 1.

* Upon receiving a data/remote frame REC val will decrement by 1.

$T_{EC} > 255 \rightarrow$ Bus-off state

- Then that node enters into bus-off state

16-11-21 A Overload Frame

This frame generated when node is busy.

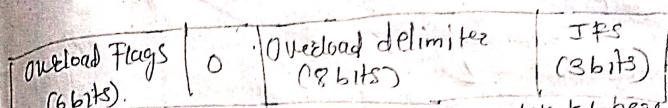
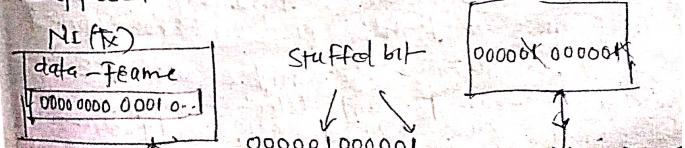


Diagram illustrating IEEE 802.3 CSMA/CD frame transmission:

- A Network Interface Card (NIC) sends a frame labeled "Data-Frame" to a hub.
- The hub contains two ports, Port 1 and Port 2, both connected to the same bus.
- Port 1 has a signal labeled "IFسؤ".
- Port 2 has a signal labeled "IFسؤ" and "IFS".
- A bracket on the right indicates that while sending the frame, if an IFSOK is sent after the IFS, it will say they stop here.
- Below the diagram, text states: "While sending these two bits on the bus, if zero detected, the OV frame will be generated."

- * what is Bit stuffing?
 - To ensure enough transitions to maintain synchronization, A bit of opposite polarity is inserted after five consecutive bits of the same polarity.
 - the stuffed data frames are destuffed by the receiver.

Suppose:



If Node transmit five consecutive bits after that. It will send opposite bit '1'. Then there ~~will~~ will not consider ~~any~~ ~~error~~
00000 1 → This bit is called stuffed bit and removing stuffed bit called destuffing.

* Bit stuffing can't be affected on following fields → giving Redundancy check

- CRC Delimiter → 16 bits
 - Ack Field → 2 bits
 - EOF → 1 bit

Above field sizes are fixed & are not stuffed.

* Different types of EEEOE in CAN

- ① Bit monitoring → Bit level
- ② Bit stuffing
- ③ Frame EEEOE (Frame check) → message level.
- ④ Acknowledgment check.
- ⑤ Cyclic Redundancy check

Transmitter node

- ⑥ Bit stuffing Error → If more than five level occurs on the bus, of same level → a stuff error signalled.
- ⑦ Frame EEEOE → Some part of CAN message have a fixed format. If a CAN controller detect an invalid value in one of these fixed fields, a frame EEEOE signalled.
- ⑧ Acknowledgment check → Transmitter wait for acknowledgement → If Tx can't detect dominant acknowledge

(5) CRC EEEOE :-

If Node 1 one sending some CRC value to Node 2, then Node 2 will try to match this value. If value not matched then Node 2 will think data is corrupted. A node will generate the EEEOE frame.
 → This called CRC EEEOE.

* RTOS : - Real-time System (e.g. airbag system, autopilot, missiles, etc...)

→ It is work differently as compare OS. RTOS focus on the tasks, but is specially designed to run applications with very precise timing and a high degree of reliability.

* Can bit time ? →

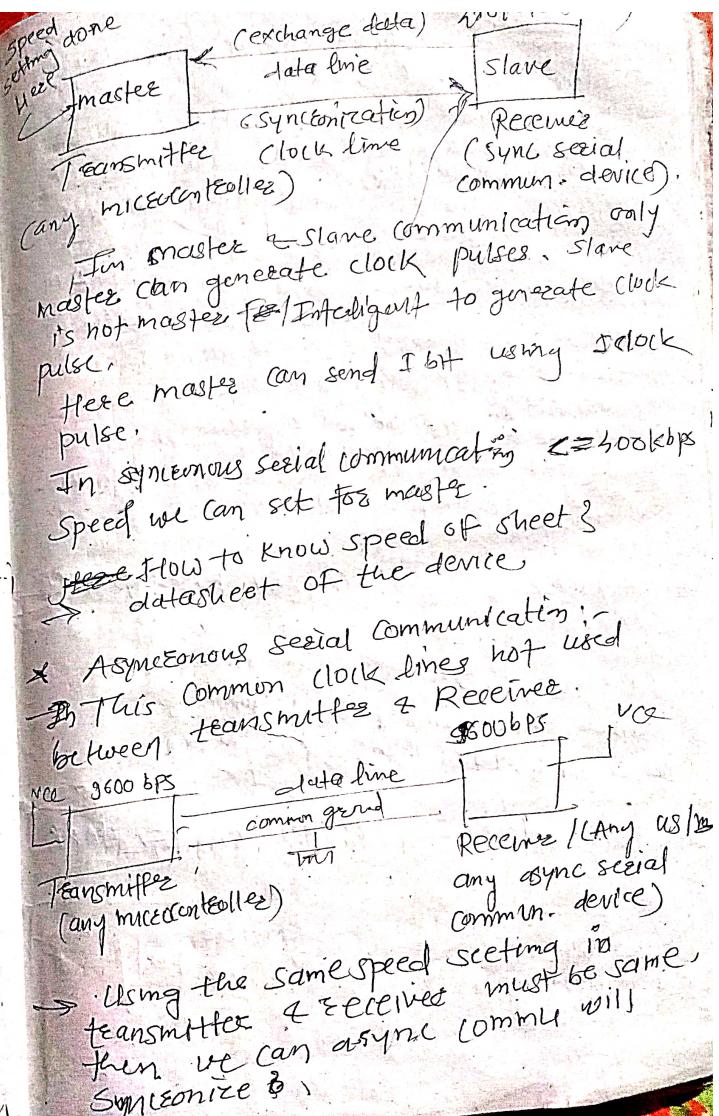
* How many SFR's in CAN ?

→ CAN 1 → 13 SFR's { 26 SFR's
 CAN 2 → 13 SFR's }

quanta → CAN time measuring in quanta

* Bus Arbitration → Will check priority which slave should select bus depend on this priority.

- Universal Asynchronous Receiving Transmitter (UART)
- * What is UART? (Based on serial communication)
 - UART designed for serial communication
 - There is built-in UART in microcontroller
 - If we have built-in UART we will get TXD and RXD two pins.
 - TxD → Transmits data.
 - RxD → Receives data.
 - * UART is asynchronous, on-chip peripheral of microcontroller designed for asynchronous serial communication where data format & speed of communication is configurable.
 - Baudrate is called communication speed.
 - * Serial communication :- is process of sending one bit at a time. (e.g. Bluetooth module, USB device, etc.)
 - * Parallel communication :- If it's a process of sending 1 byte at a time. e.g. old printers, alphanumeric LCDs, etc.
 - * There are two types of serial communication
 - + Synchronous serial communication
 - + Asynchronous serial communication
 - (1) Synchronous Serial Communication
 - A common clock line is used between transmitter & receiver for synchronization.
 - Here master can send 1 bit using 1 clock pulse.



ARM Controller work on 3.5V

- * List of asynchronous serial communication
 - + Bluetooth module
 - + GPS module
 - GSM module
 - RFID mod Reader ✓
 - * bcc module
 - wifi module
 - etc -
- * List of synchronous serial communication devices
 - + EEPROM
 - + Real-time clock (RTC)
 - + Digital accelerometer
 - + Digital thermometer (temperature sense)
 - + OLED display
 - etc -

LPC 2129
CAN available
SSTIET
LPC 2148 SSTIET
CAN available SSTIET
NOT LPC 2148 SSTIET
Flash memory & RAM
SSTIET available SSTIET
Commu devices

- * Asynchronous serial communication based protocols
 - UART
 - CAN
 - CAN FD & USB
- * Sync serial communication based protocols
 - I²C, SPI, LIN etc -
- * Understanding Real-time case of UART

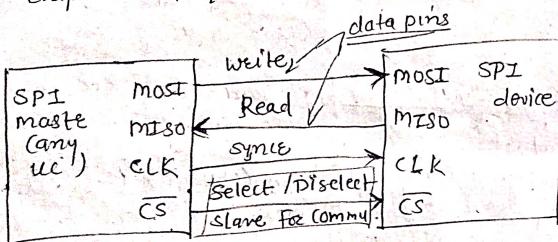


T SPI PROTOCOL → 10-04-2023

SPI is designed by Motorola for on-board IC communication (i.e. short distance communication).

Features :-

- 1. SPI was designed by Motorola for short distance communication.
- 2. SPI is a forced commu. protocol
 - Master Out, Slave In (MOSI)
 - Master In, Slave Out (MISO)
 - Serial clock (SCK / CLK)
 - Chip select / Slave Select (\overline{CS} / \overline{SS})



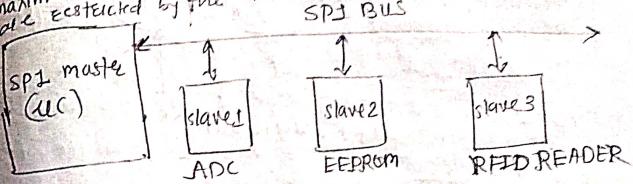
By giving logic '0' you can select slave commu. By giving '1' you can deselect.

- * Hardware complexity will increase when you increase the slave in SPI.
 - * In I₂C hardware complexity will not increase when you increase the slave.
- This four wire is called as SPI bus.

SPI

most pins & MISO pins are data pins.
It is full duplex commu. protocol because here two voice reach each other.
Because here used synchronous serial commu. protocol.

⑥ SPI is multislave communication protocol. maximum number of slaves that can be connected on the SPI bus, are restricted by the hardware.



⑦ Spec of communication

• SPI does not define any speed limit; implementations often go over 10 mbps.

⑧ Acknowledgement is not supported by the SPI.

⑨ Direction of data transmission can be MSB first or LSB first.

⑩ SPI has 4 modes of operation. And modes are depends on the two parameters.

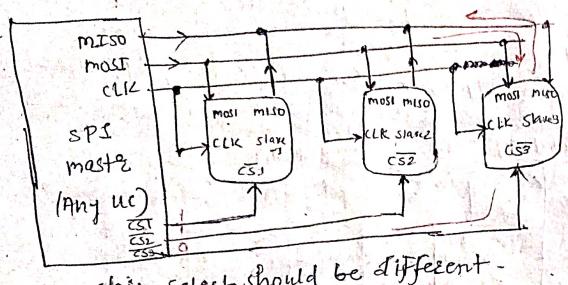
- clock phase (CPHA)
- clock polarity (CPOL)

CPHA: Represents on which edge (rising/falling) data has to be sampled (read).

CPOL: Represents the basic value of clock pulse.

mode	CPOL	CPHA	DATA Sampling	Data transmission
0	0	0	Rising edge	Falling edge
1	0	1	Falling edge	Rising edge
2	1	0	Falling edge	Falling edge
3	1	1	Rising edge	Rising edge

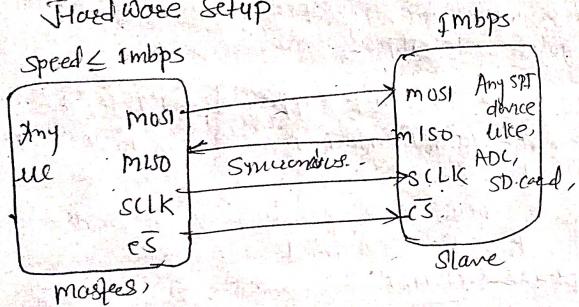
To connect three slaves to 6 pins acc. to question
H/W setup: Master & 3 independent slaves



chip select should be different -

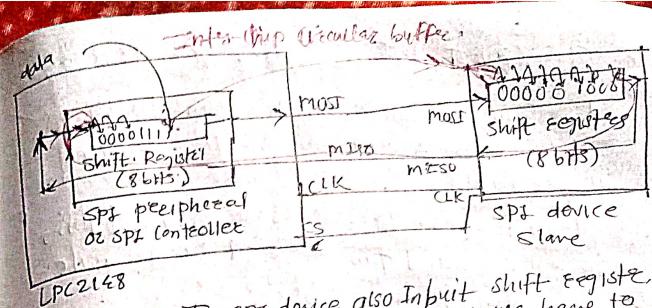
* Working of SPI Protocols

Hardware Setup



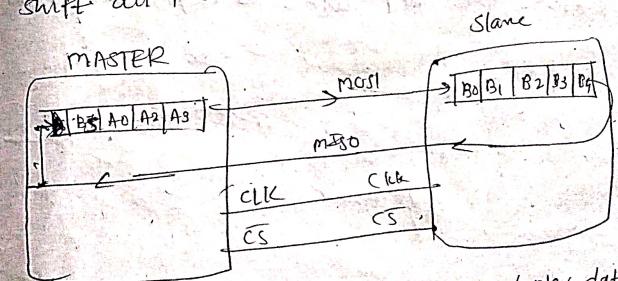
* Peripheral Method

In Any microcontroller, there are built-in SPI controllers or SPI peripheral.



Working :- In SPI device also Inbuilt shift Register. If you want send data, then we have to write data shift register only. Here Shift Register is nothing but the Special Function Register. After transmitting data internally flag will set. data will shifted serially, Serial output, parallel input - (PISO) or (SOP).

Whenever master generate one clock pulse, that full duplex communication occurs. After generating one clock pulse master will shift all the bits towards to right.



- During each SPI clock cycle, a full-duplex data transmission occurs. That means, the master sends a bit on the mosi line & the slave reads it, while the slave sends a bit on the miso line. The master reads it.