JADAVPUR UNIVERSITY

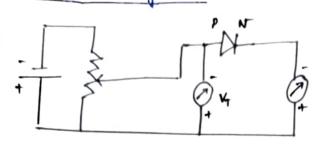
Faculty of Engineering & Technology

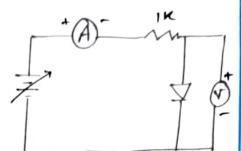
	eni Engg. Labo	
	ATHAGATA SUR	
Class CSE	VC1 Sec. Al Roll N	10.002310501030
Date of Experiment	Date of Sub	omission
		aminer
NAME	CO-WORKER	ROLL
Shyam Sundar Karma Samim Sekh Pratyay Kar Jayosmit Pal Abin Chakrabar Anirudh Modi	 .	0023105010 25 0023105010 26 0023105010 27 0023105010 28 0023105010 29 002310501031
Experiment No0.2 - A		
Commence at 11:00 Am	Compl	leted at 2:00 pm

TITLE: I-V Characteristics of Semiconductor diodes (Si and Cle) under forward and reverse-biased conditions.

Name of Teacher concerned

OBJECT: To plot I-V characteristic curve to become familiar with the operating characteristic of Ge and Si diodes under both forward and reverse biased conditions. Cirruit Diogram:





Apparatus: Bread board, Silicon d'ode, Germanium diode, DC power supply, Ammeter, voltmerter, Connection wires.

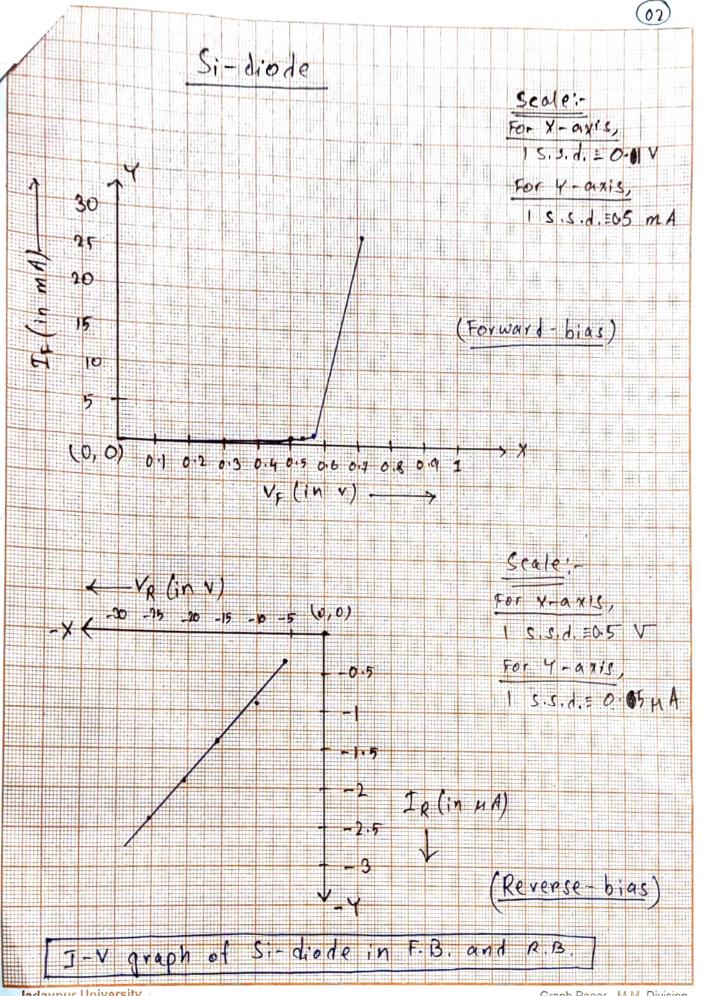
Observation Tuble and Graphs:

- 1. Silicon diode
- a) Forward Bias:

No.of	Forward Voltage (VF) (in V)	Forward Current (IF) (In mA)
1)	0	0
t)	0.498	0.1
3)	0.535	0.4
4) =)	0.548	0.6
5)	O. 415	27.5

b) Reverse Bias:

O) KEVEL	Dias :	
No.of. obs. 1	everse Voltage (VR)	(in v) Reverse Current (IR)
1)	5.8	0.4
2)	10	0.8
3)	15.6	1.4
4)	20.3	1.9
5)	25.2	2.4



Tathagata Sur, 002310501030, (SE UCI, Electronits Expt-02 A

- 2. Germanium Diode
 - a) Forward bias:

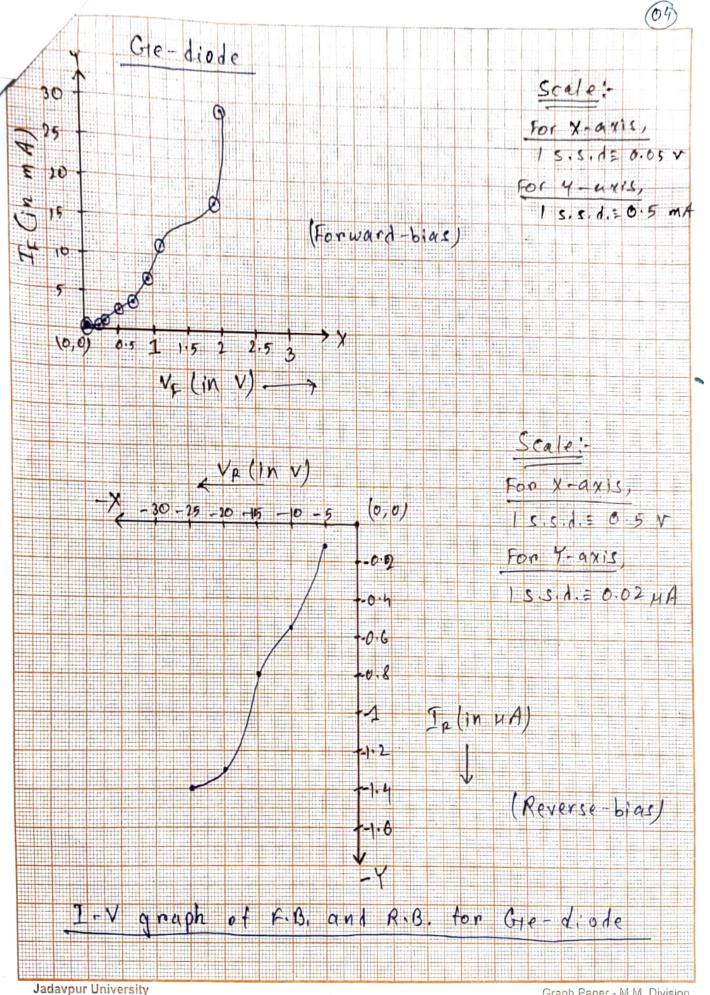
No.of obs.	Forward Voltage (F)	Forward Current
	(in v)	(IF) (in mA)
1)	0	0
2)	0.28	0.5
3)	Ø·3	1
4)	0.5	2. 7
5)	0 .7	4.8
(۶)	0·q	7-1
7) 0)	1.1	11
. 8)	1.6	17
9)	2	24

b) Reverse biors:

No.04 0bs.	Reverse voltage	Reverse (urrent (IR) Lin HA)
1)	5	Ø·13
2)	10	0.55
3)	15	Ø ⋅ €
4)	20	1.3
5)	25	1.4

Conclusion: We find that:

- a) For Si-diode, the turn-on and cut-off voltage are about 0.48 and 0.6 V respectively
- b) For Ge-divde, these are about 0.25 and 0.78 v



Tathagata Sur, 002310501030, (SE VGI, Electronics Exptro2 A