

There is 0 jumper on board XEM8310 attach VCCO bank to on board VIO supply (default).  
If want to supply a bank externally, supply through MC1 and MC2  
VIO1 (1.0-1.8V) – Bank 64 – HP – Default 1V  
VIO2 (1.2-3.3V) – Bank 84 – HD – Default 1.2V  
VIO3 (1.2-3.3V) – Bank 86.87 – HD – Default 1.2V

HP bank supports LVDS inputs and outputs.  
VCCO = 1.8 on bank 64 => the pins could be used as:  
– inputs with/without internal termination  
– outputs

HD banks support LVDS inputs only.  
– with internal termination (not supported)  
– without internal termination: set any between 1.2V–3.3V. Set 2.5V

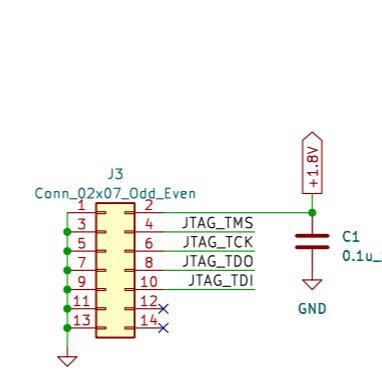
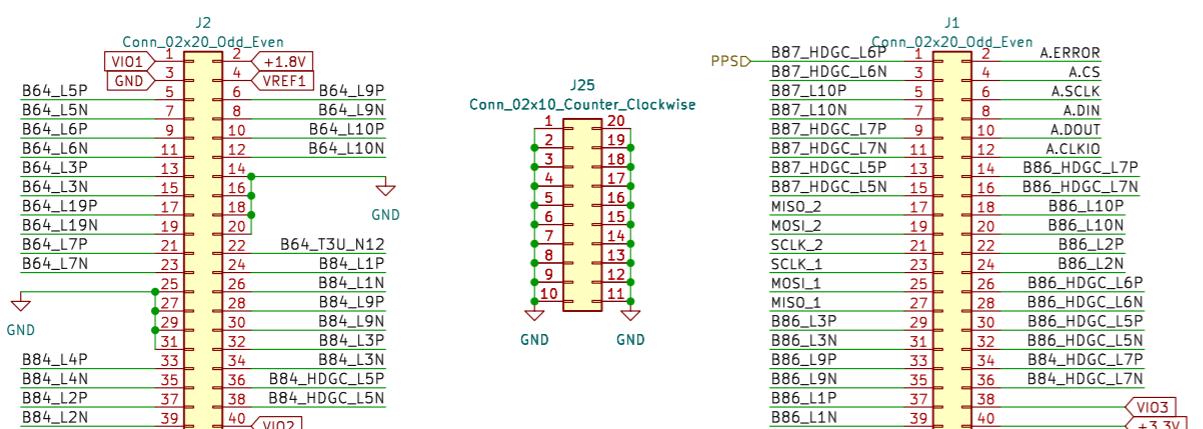
Set voltage for VIOx can be done through Frontpanel>SelectIO ??

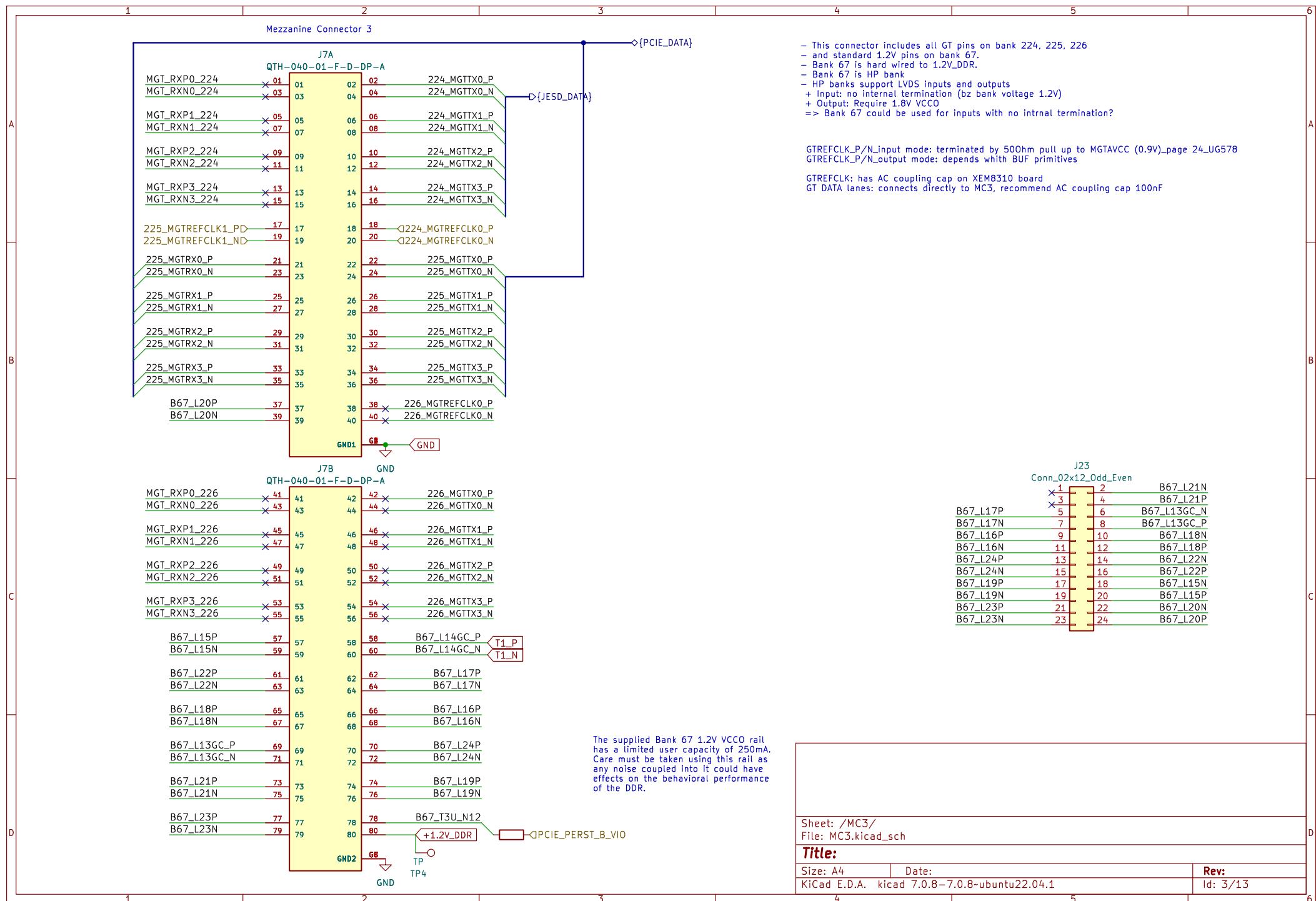
VREF1: voltage threshold for some input signal standards on bank 64  
VRP: on pin B64\_TOU\_N12: supports certain IOSTANDARD on bank 64 ???

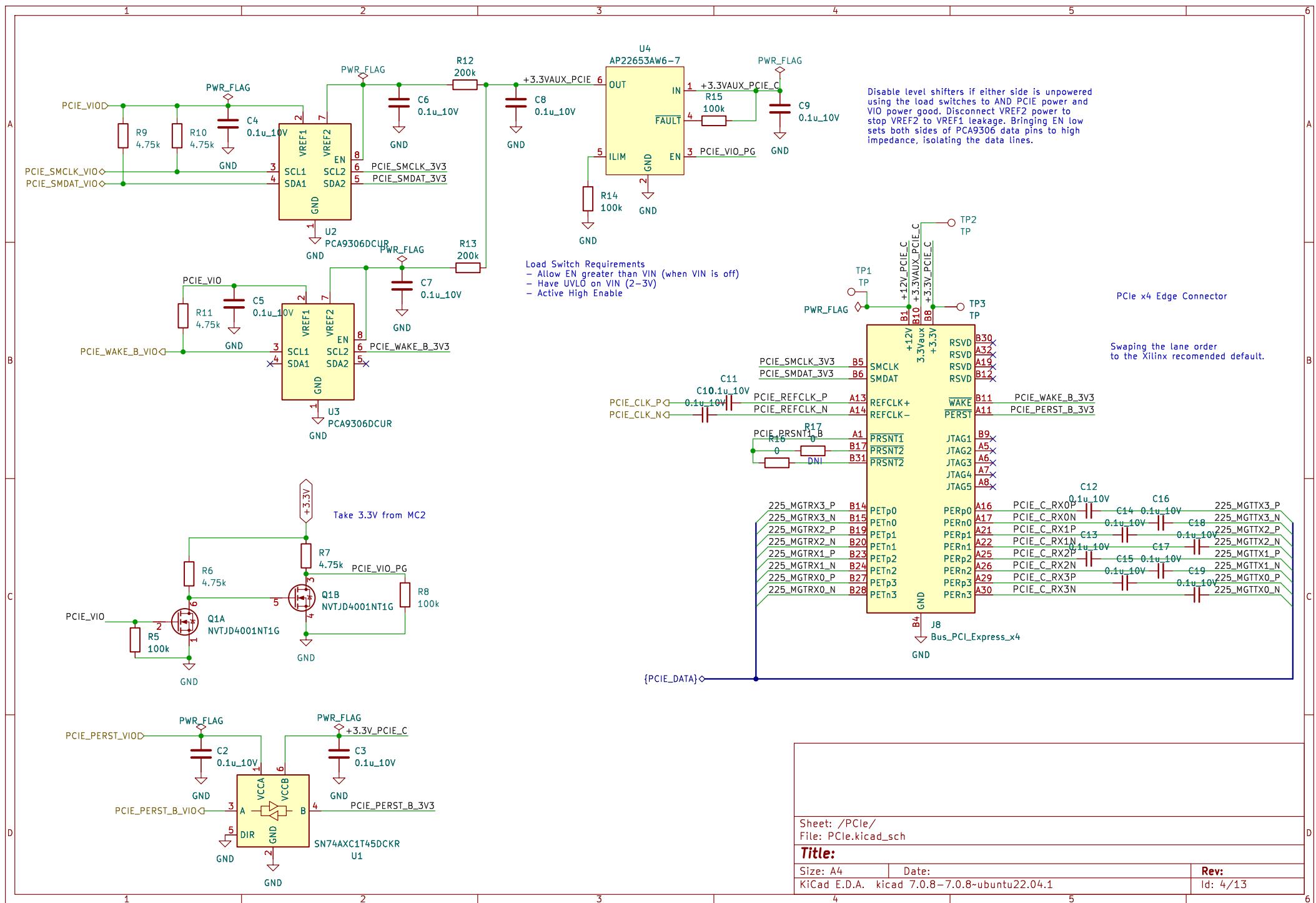
VBATT: default is disabled by R2 connected to GND. Remove R2 to use it

using internal Vref  
Short Vref with GNDADC on XEM8310  
GNDADC and GND are isolated by FB2 (short pin 77 and 79)

VCCADC id supplied by XEM8310 via FB1





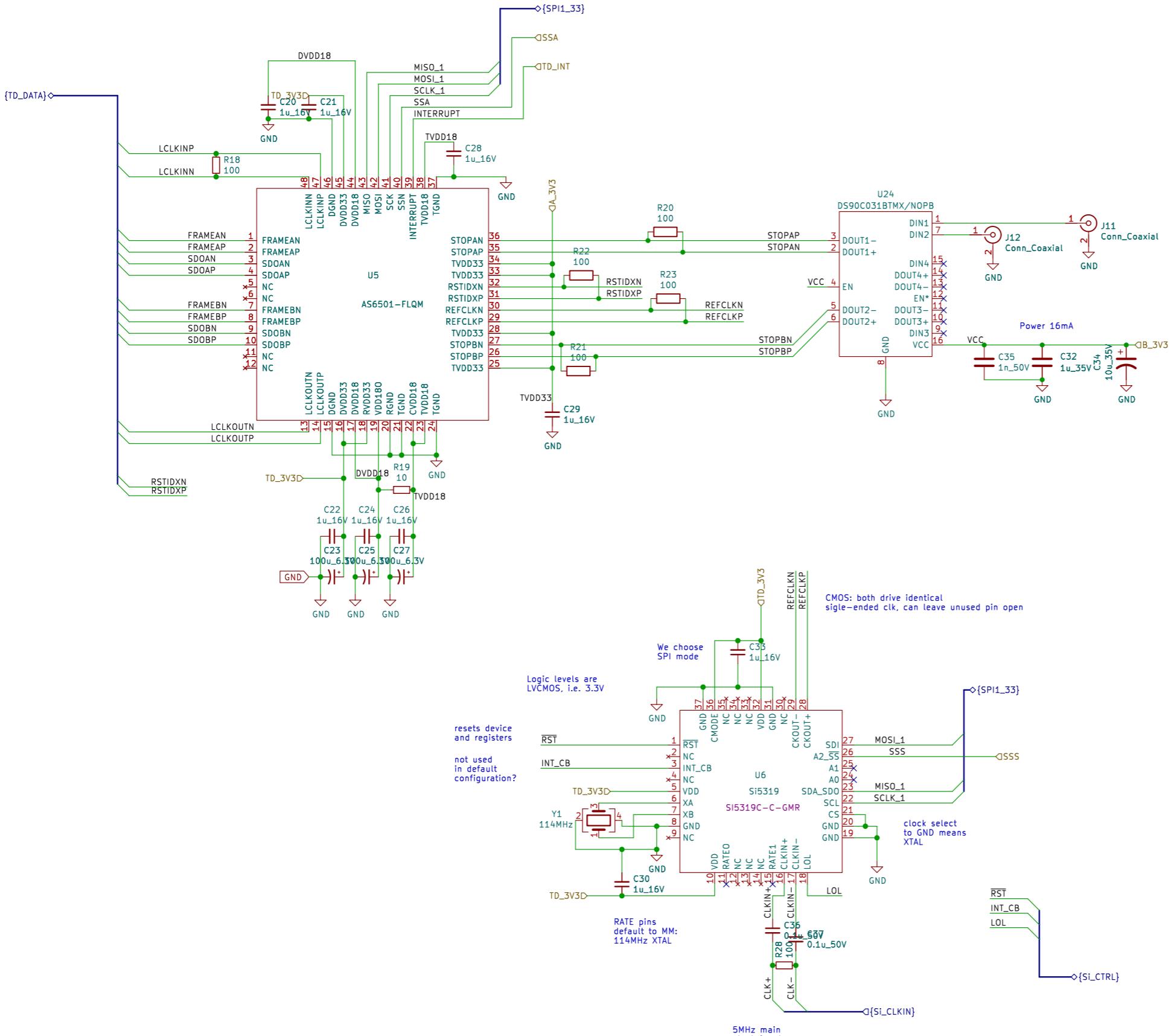


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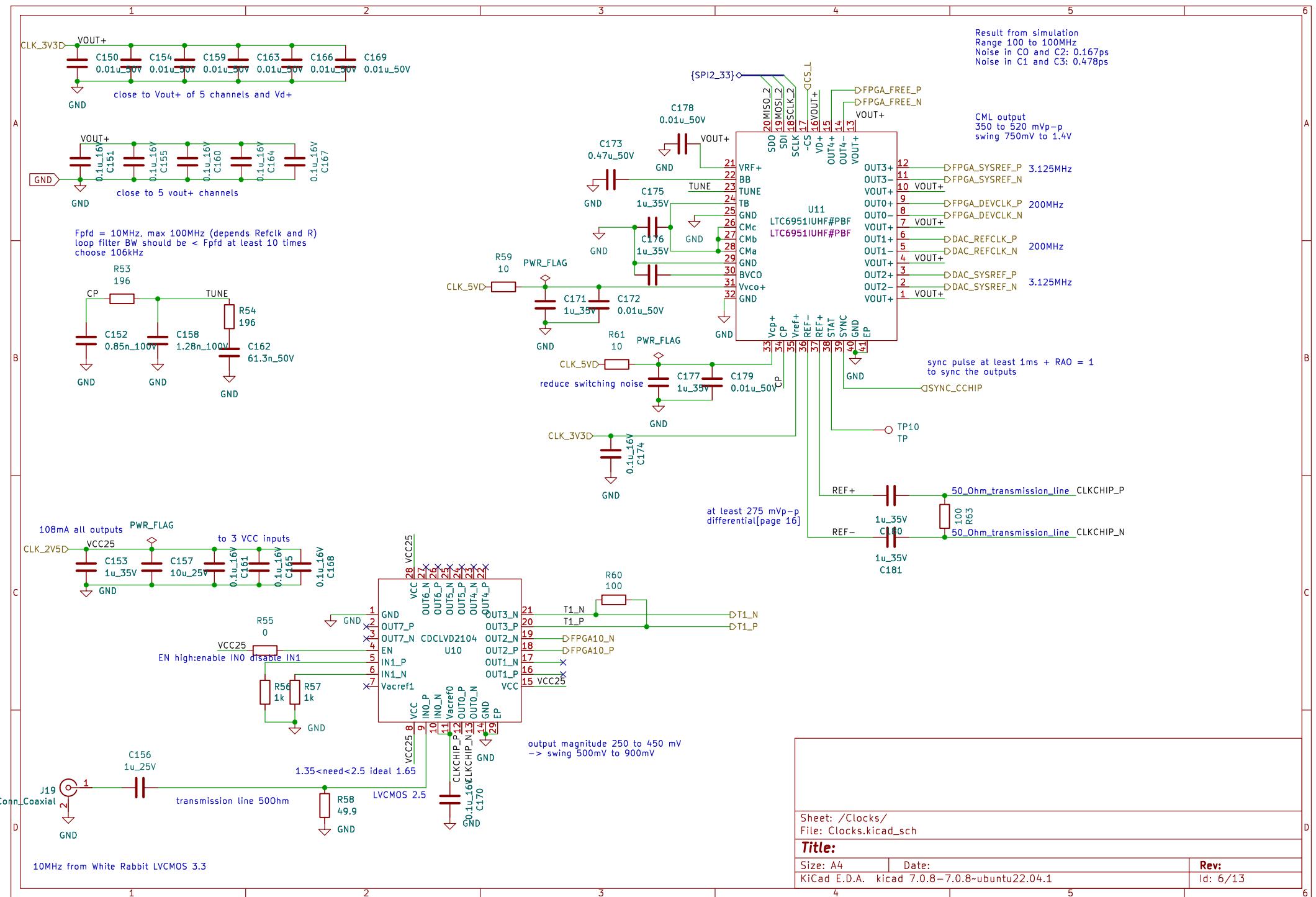


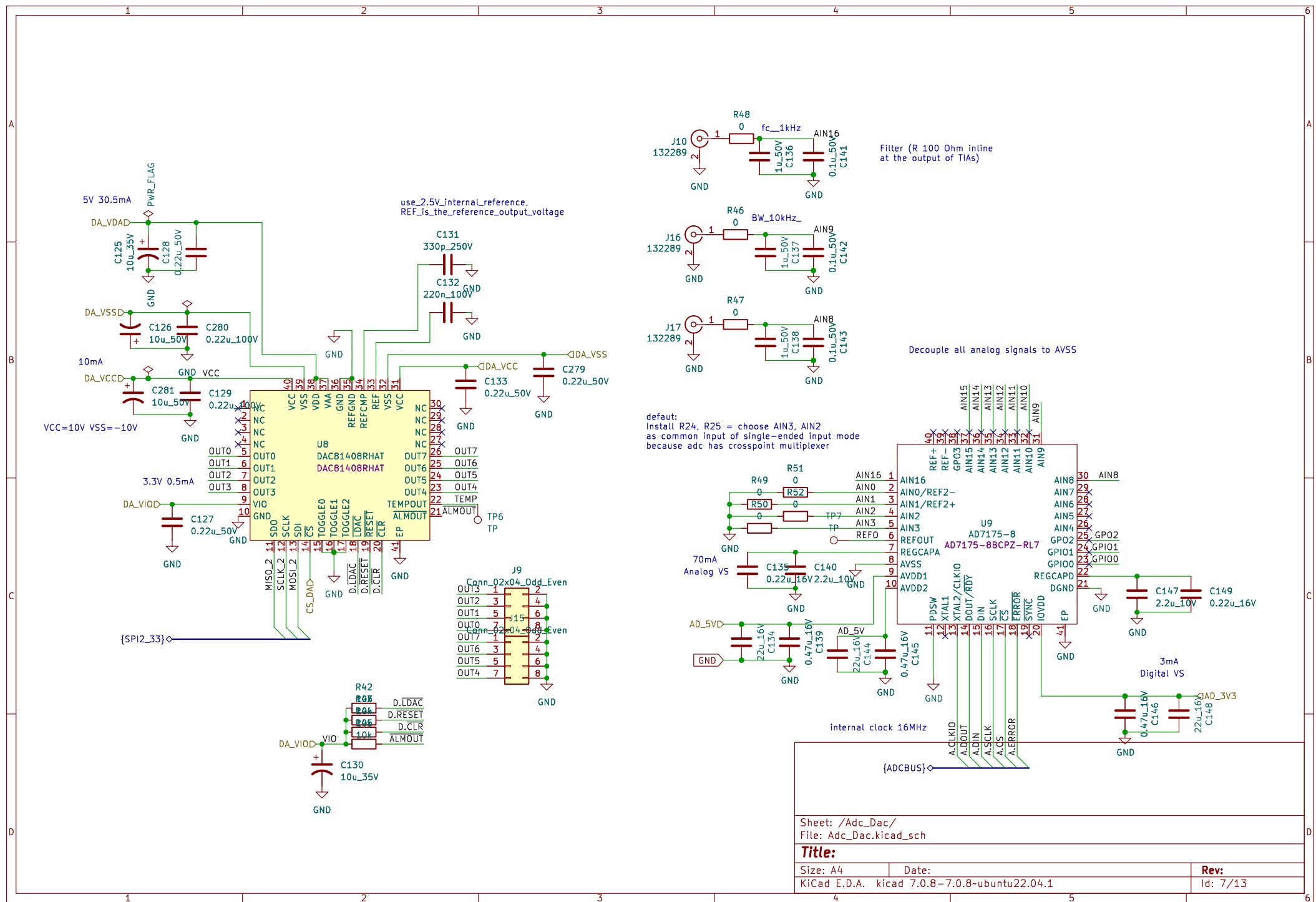
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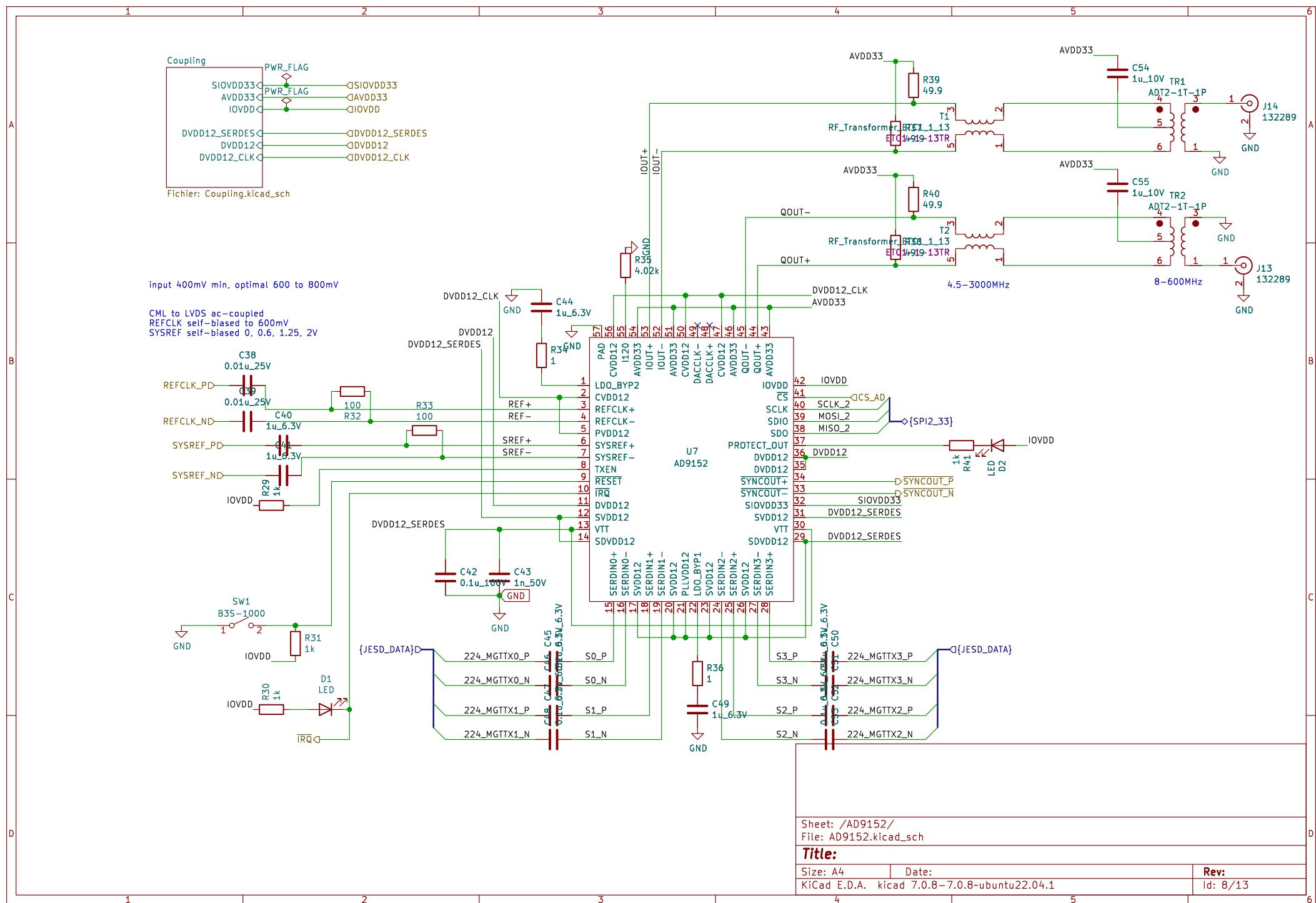
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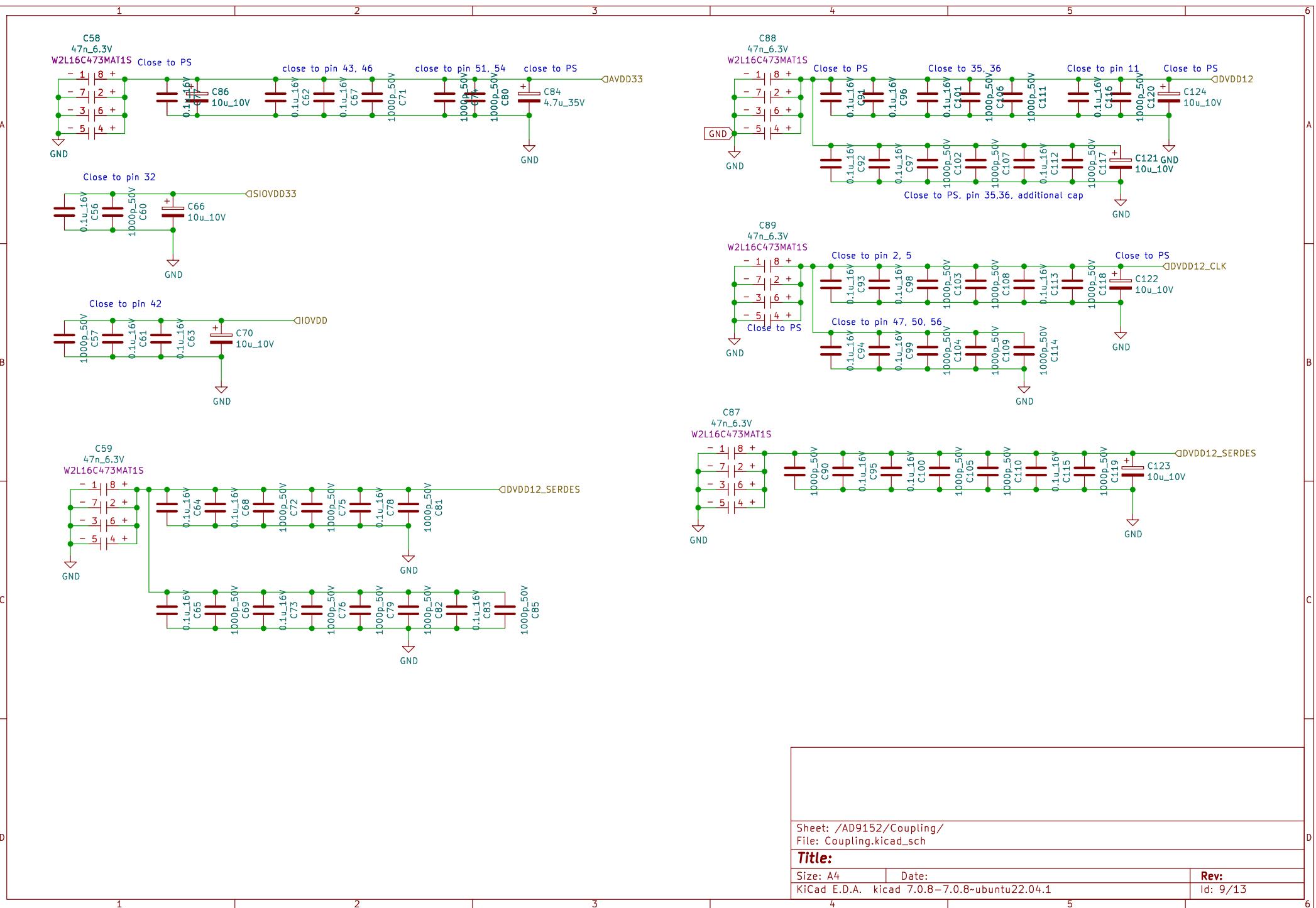
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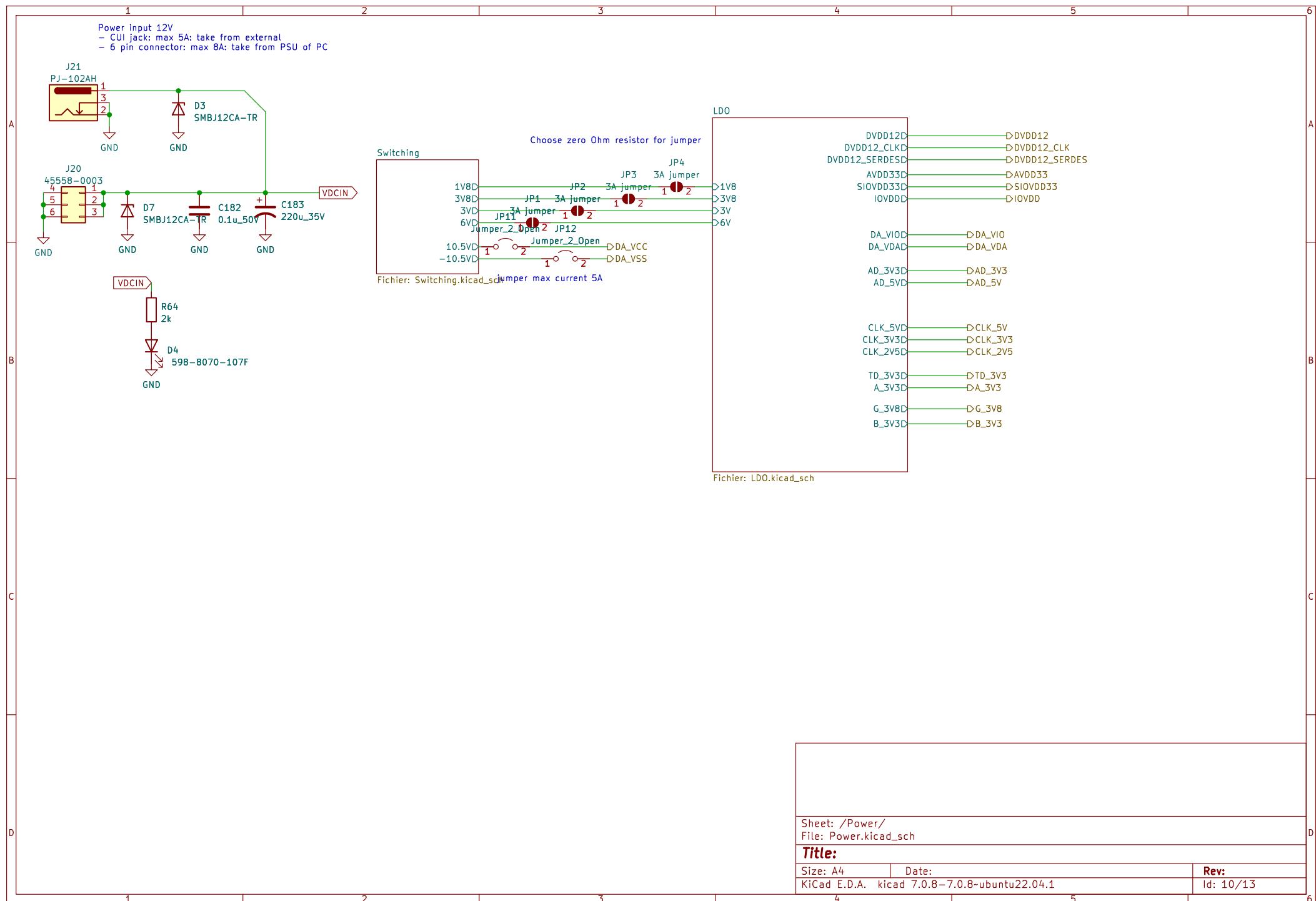
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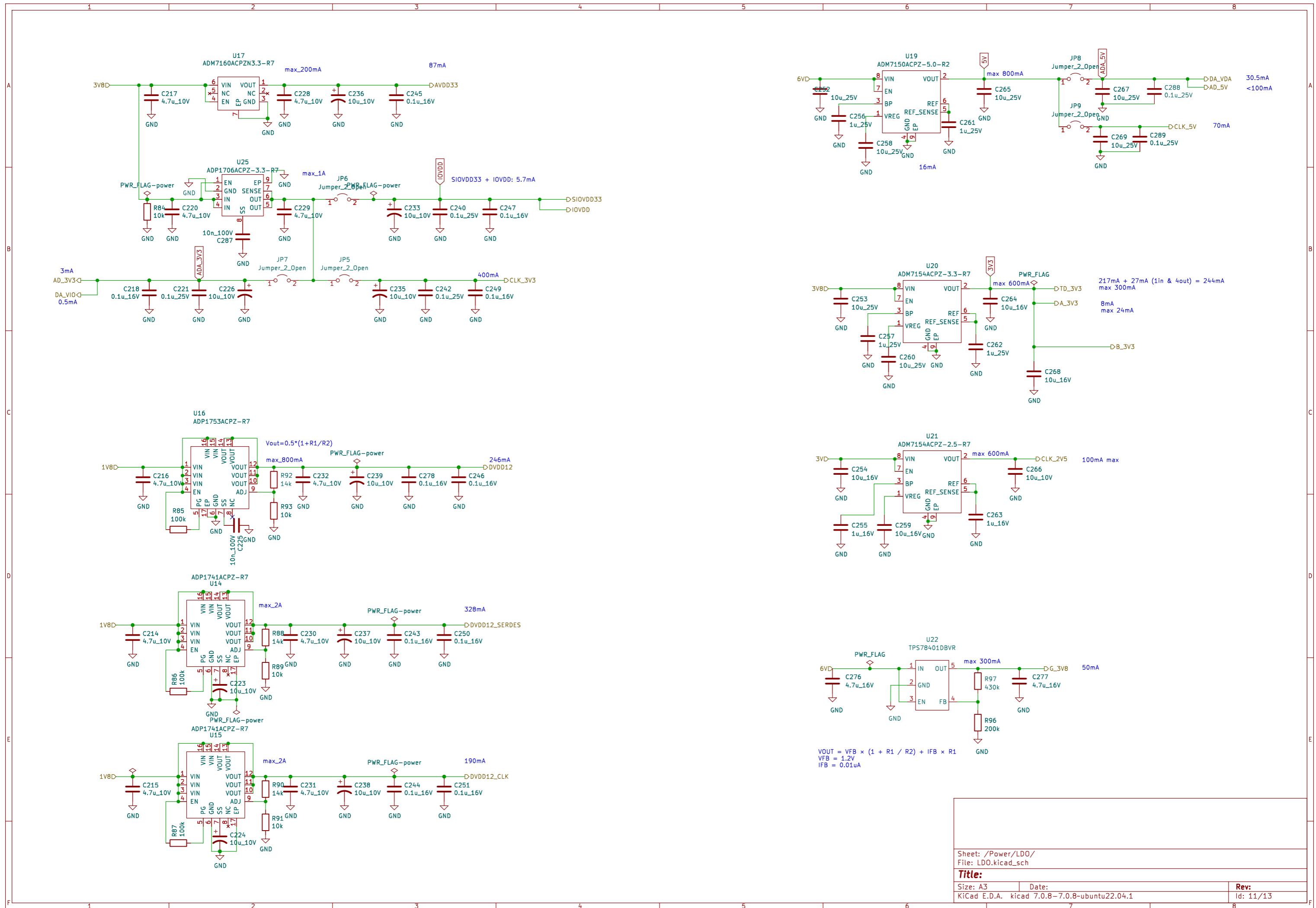


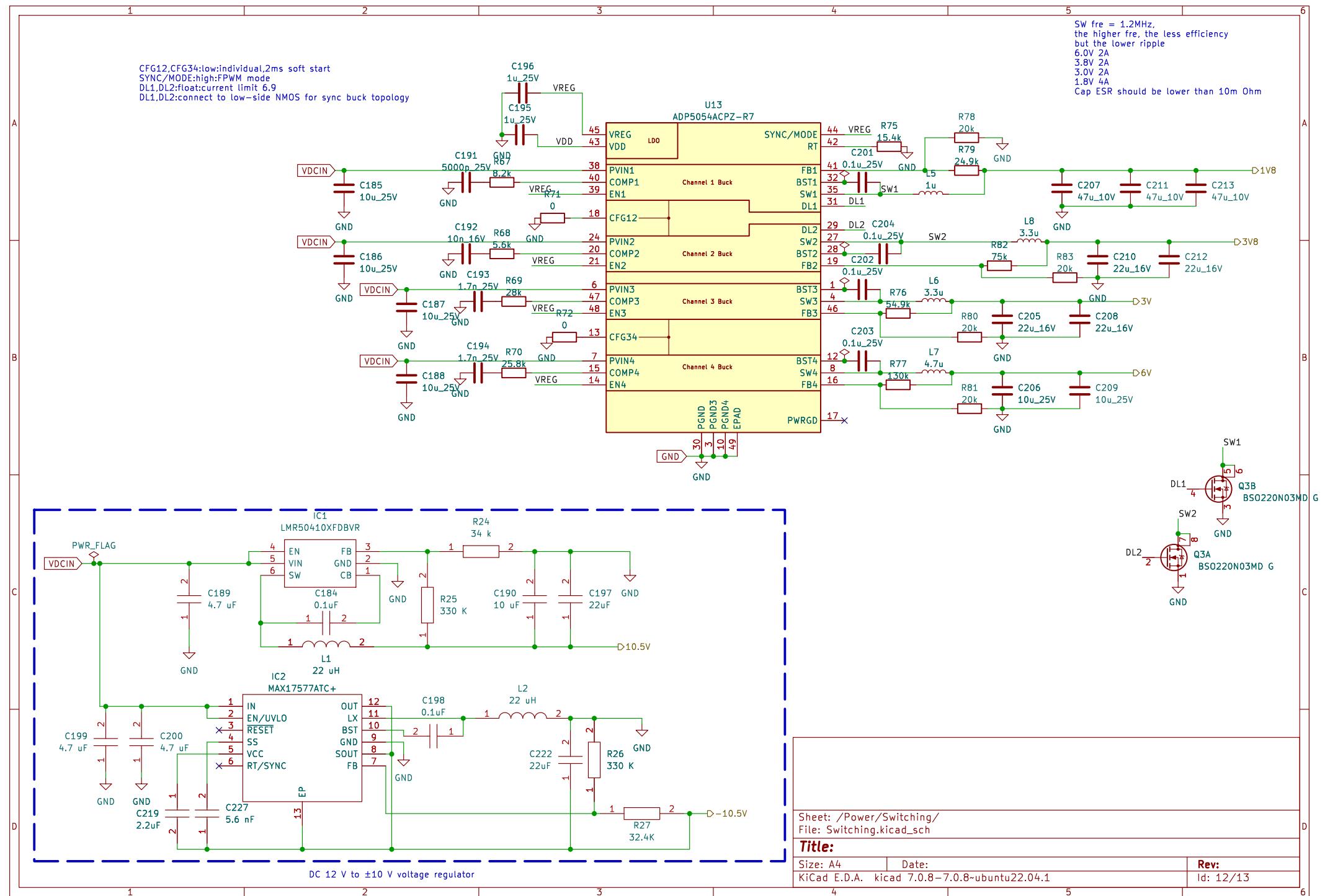












1 2 3 4 5 6

A

A

B

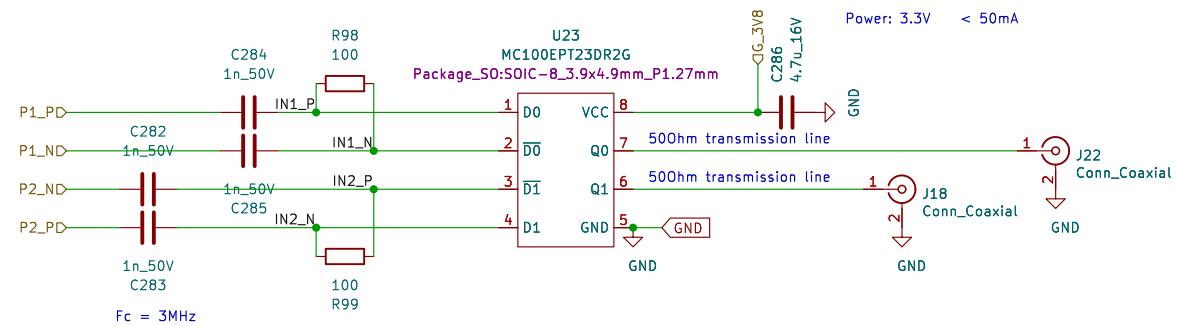
B

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D

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