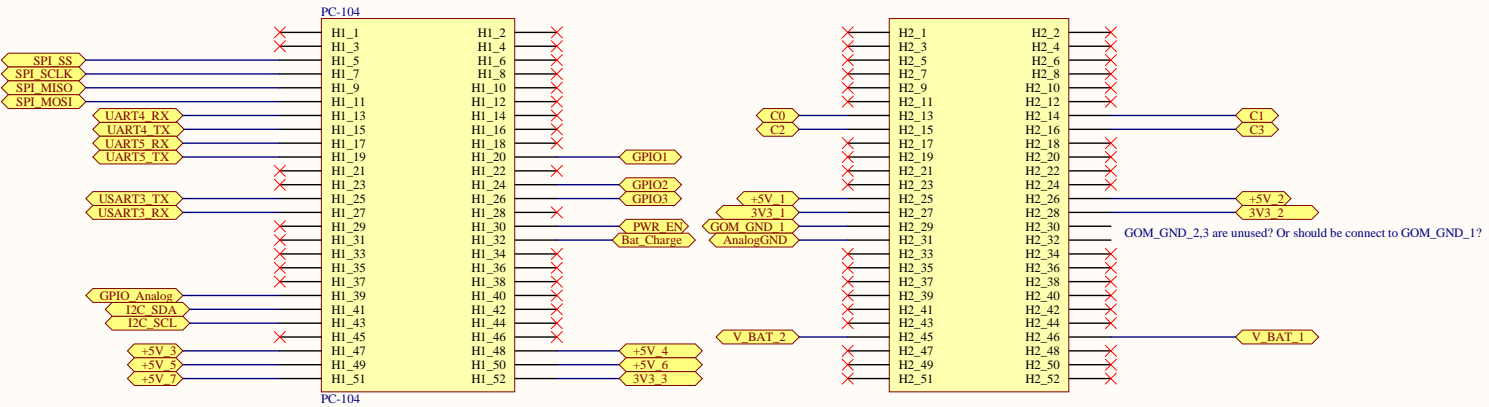
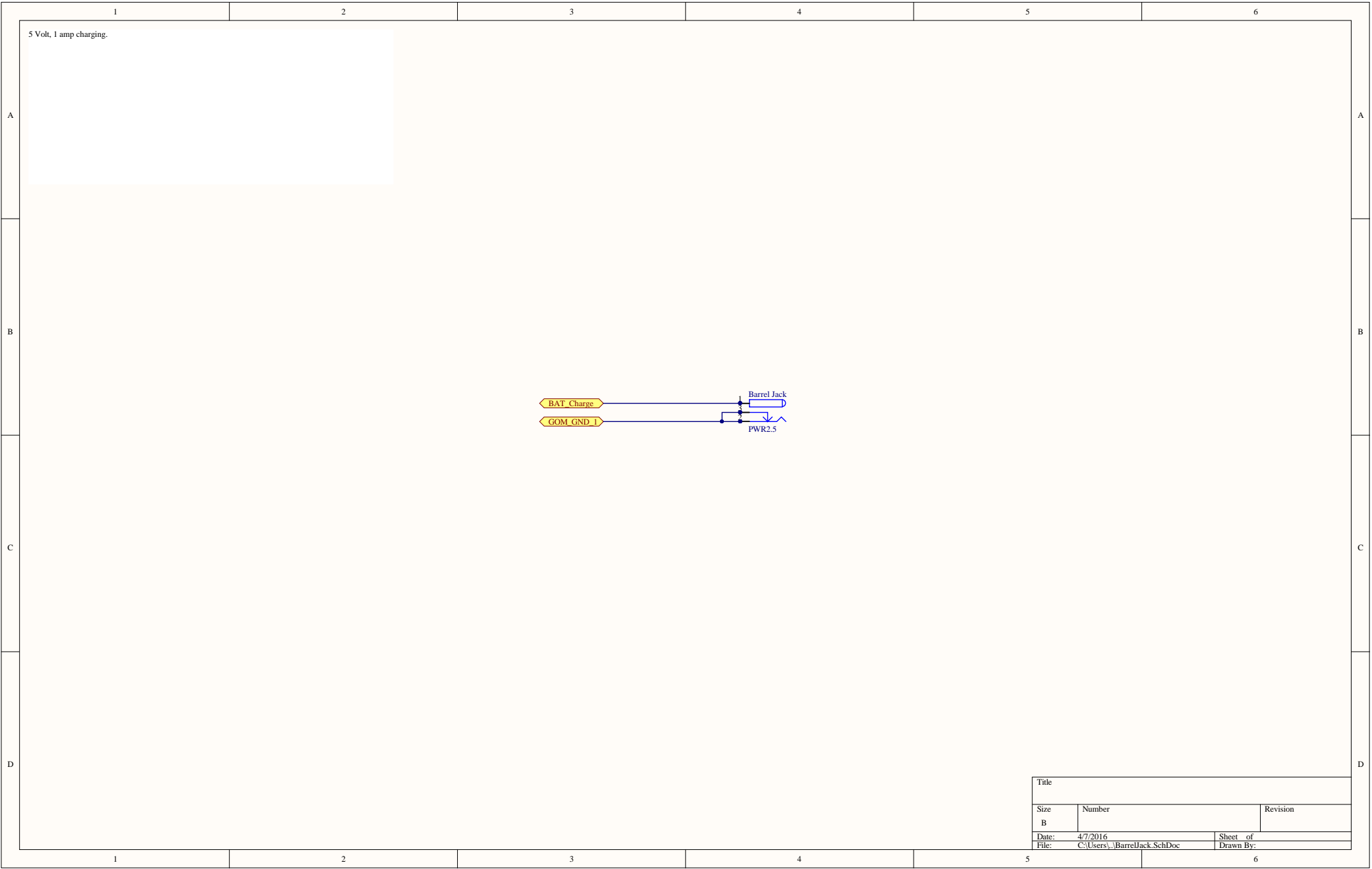


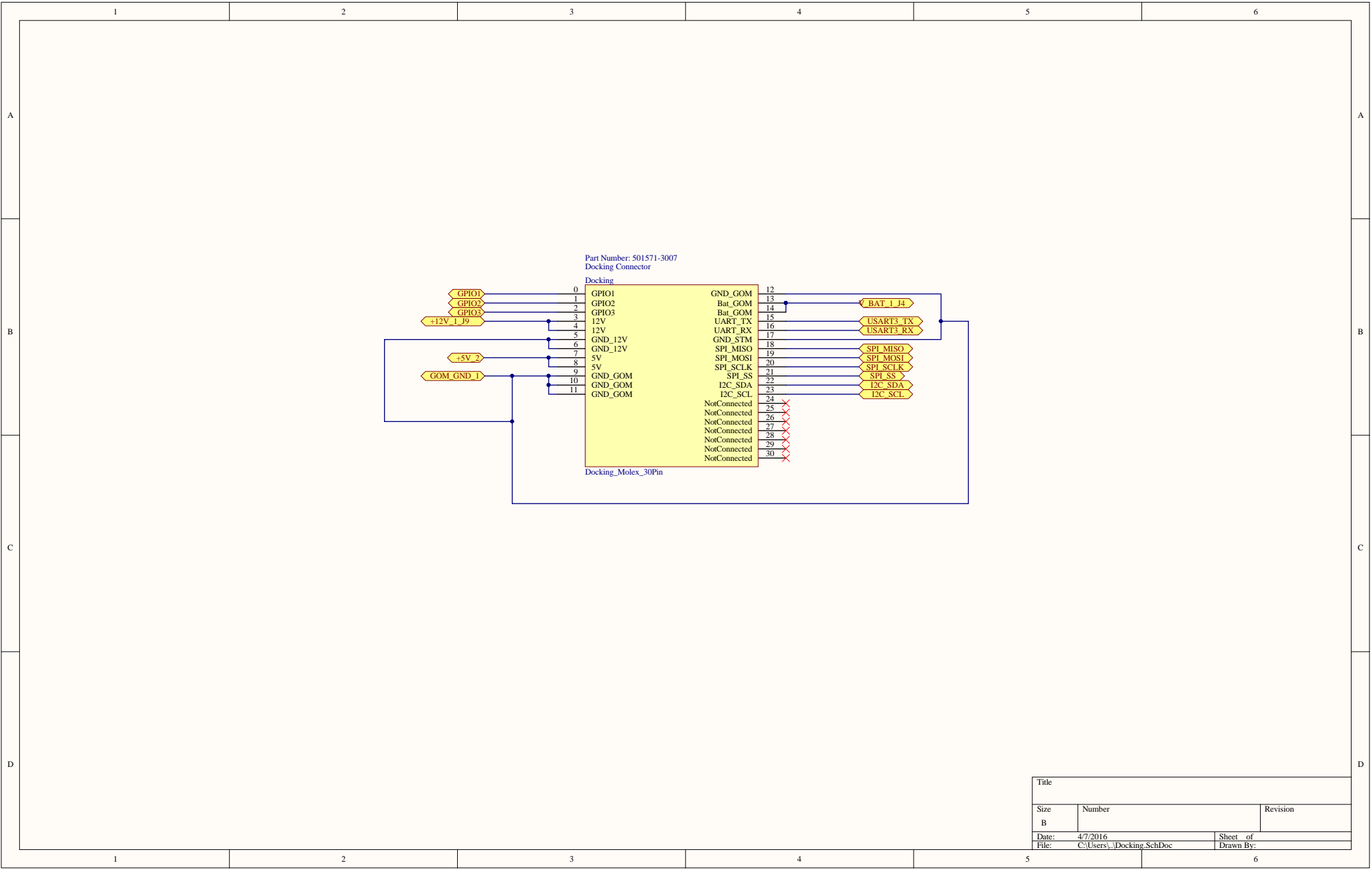
Notes:  
1.  
The I2C\_SDA and I2C\_SCL are connected together to form an I2C bus  
2.  
UART 5 RX and TX have been switched.  
3.  
USART3 is switched



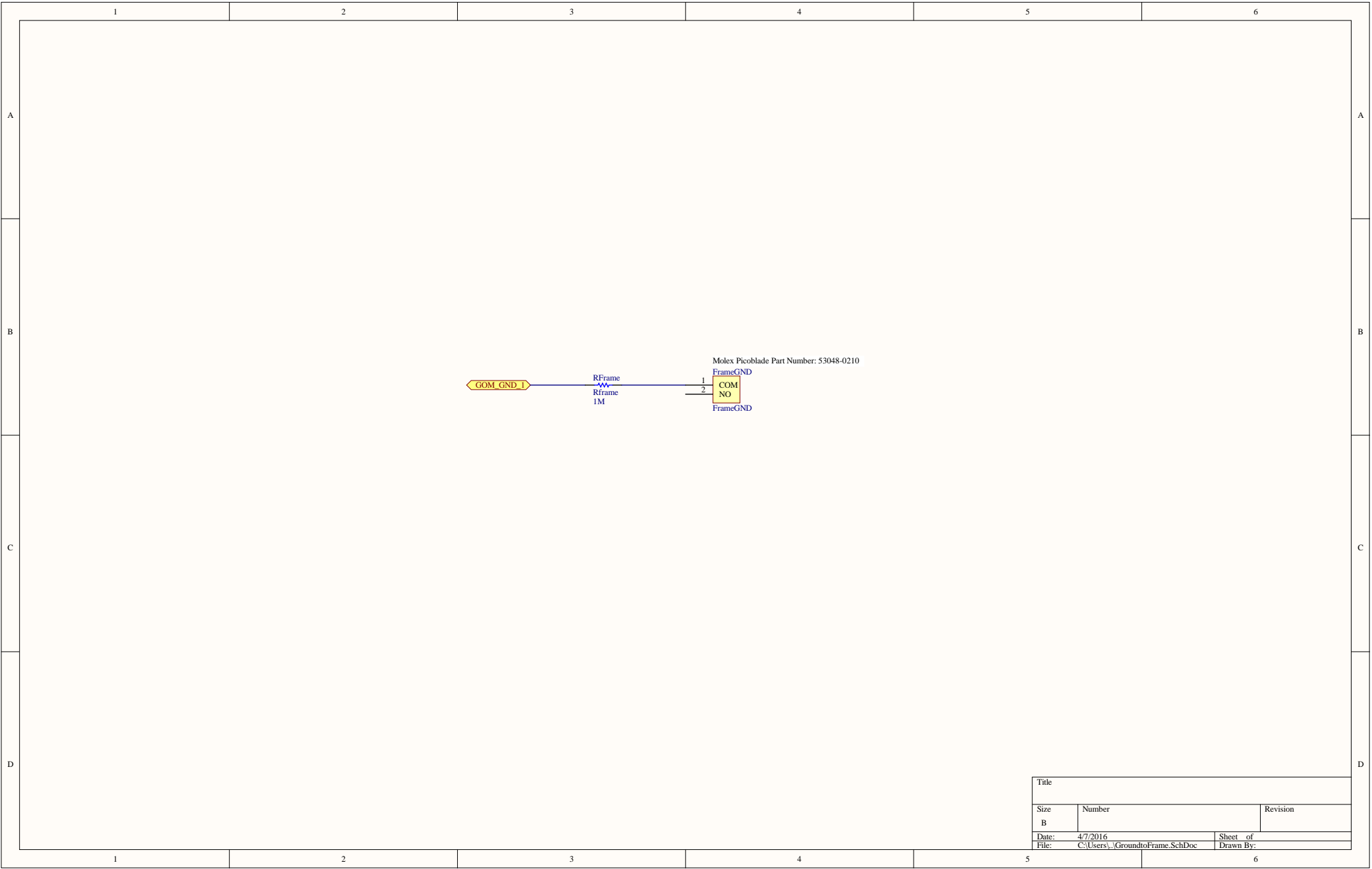
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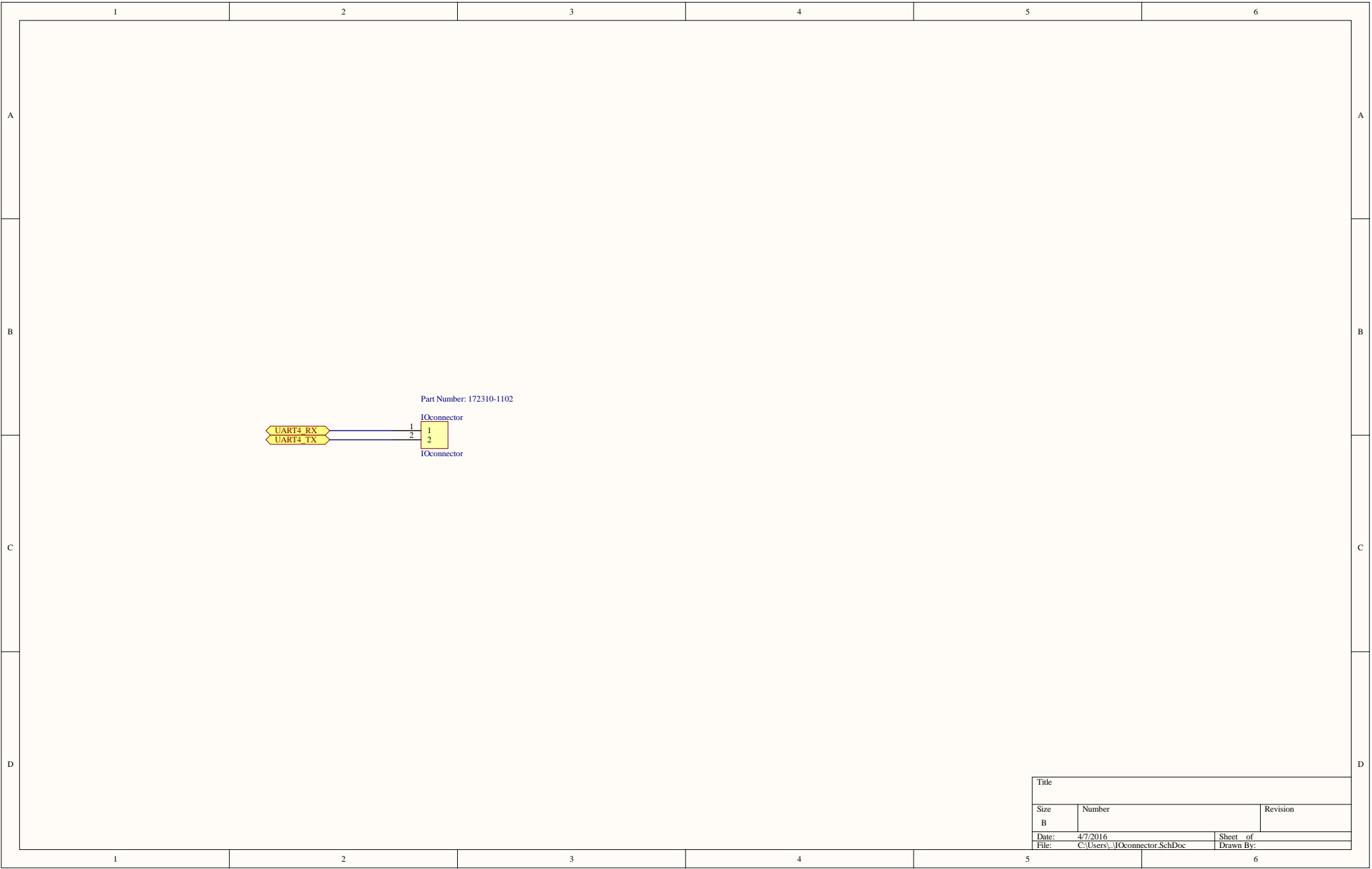


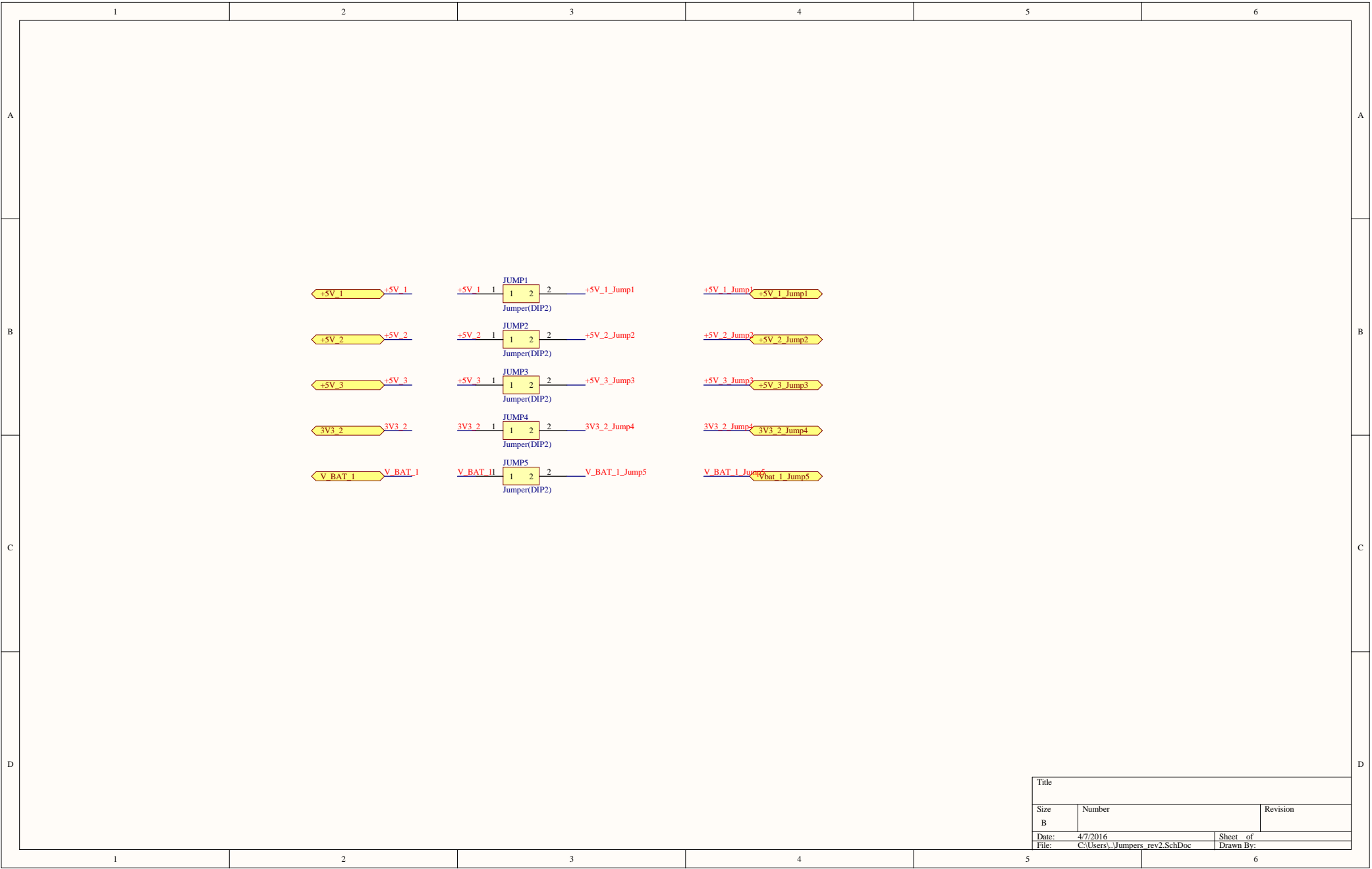
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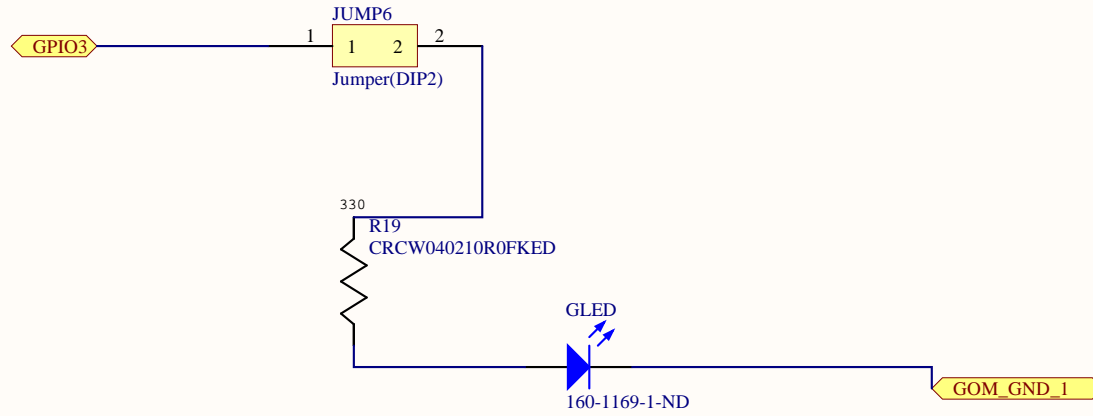
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Date:	4/7/2016	Sheet of
File:	C:\Users\...\Docking.SchDoc	Drawn By:





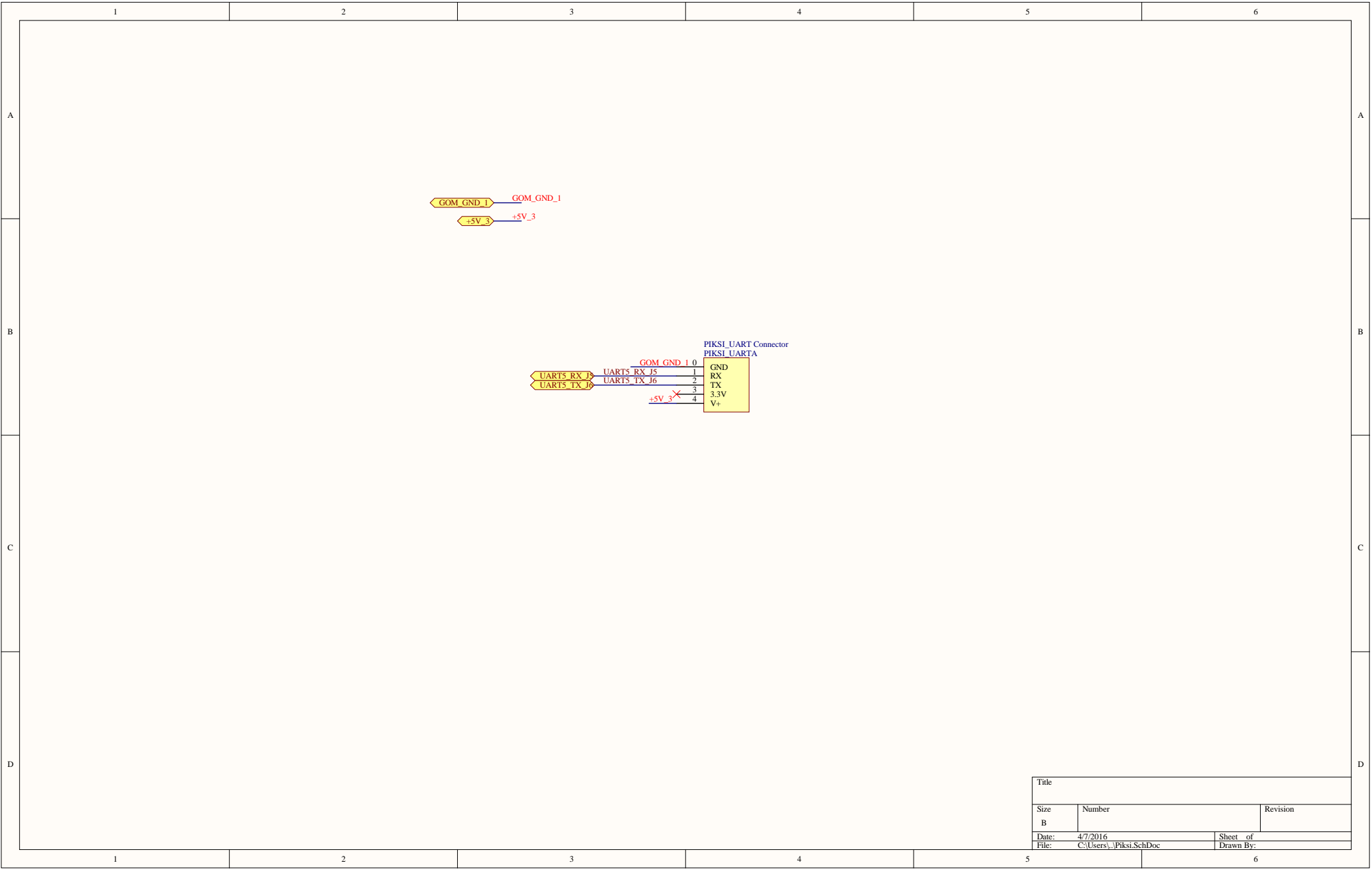


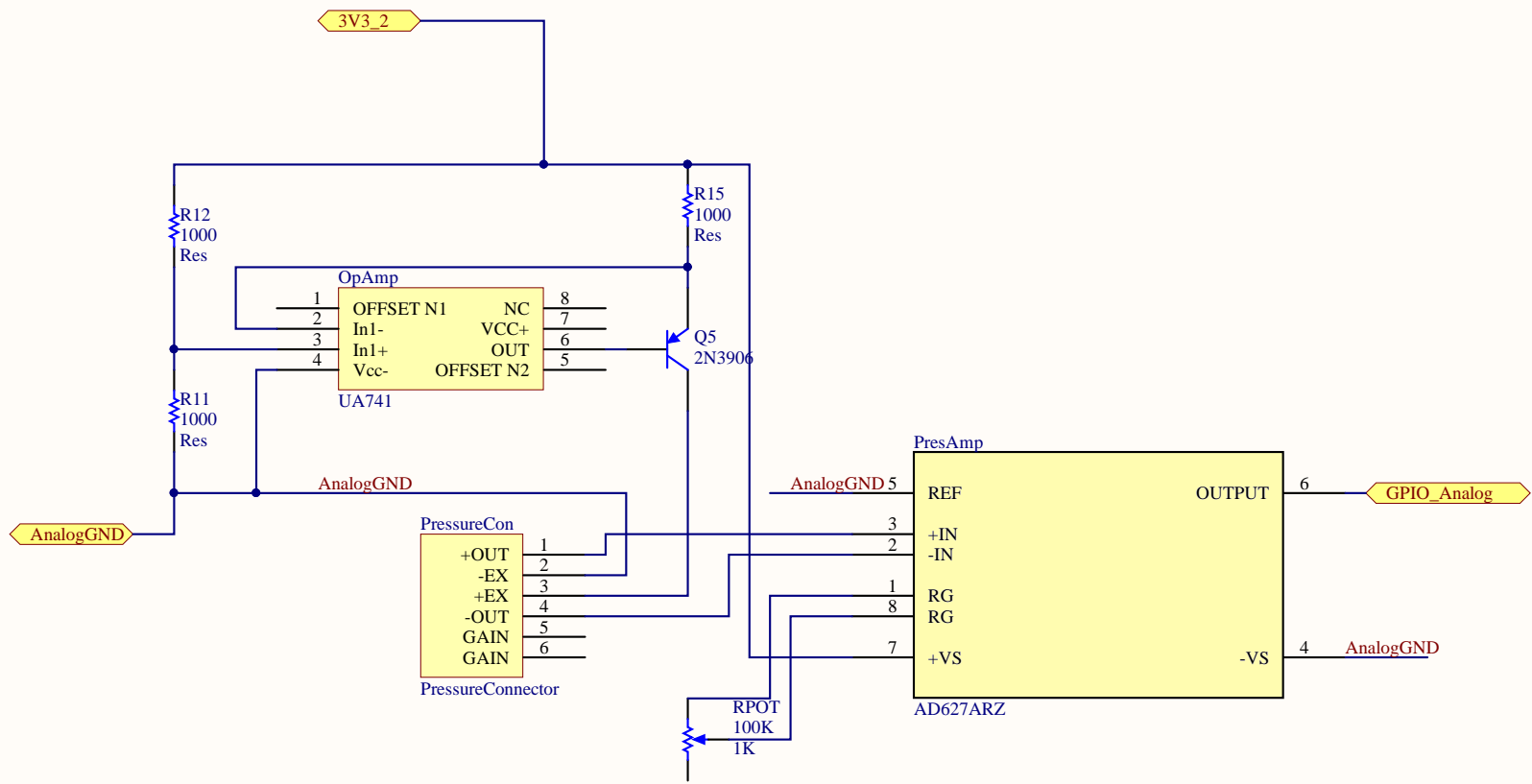
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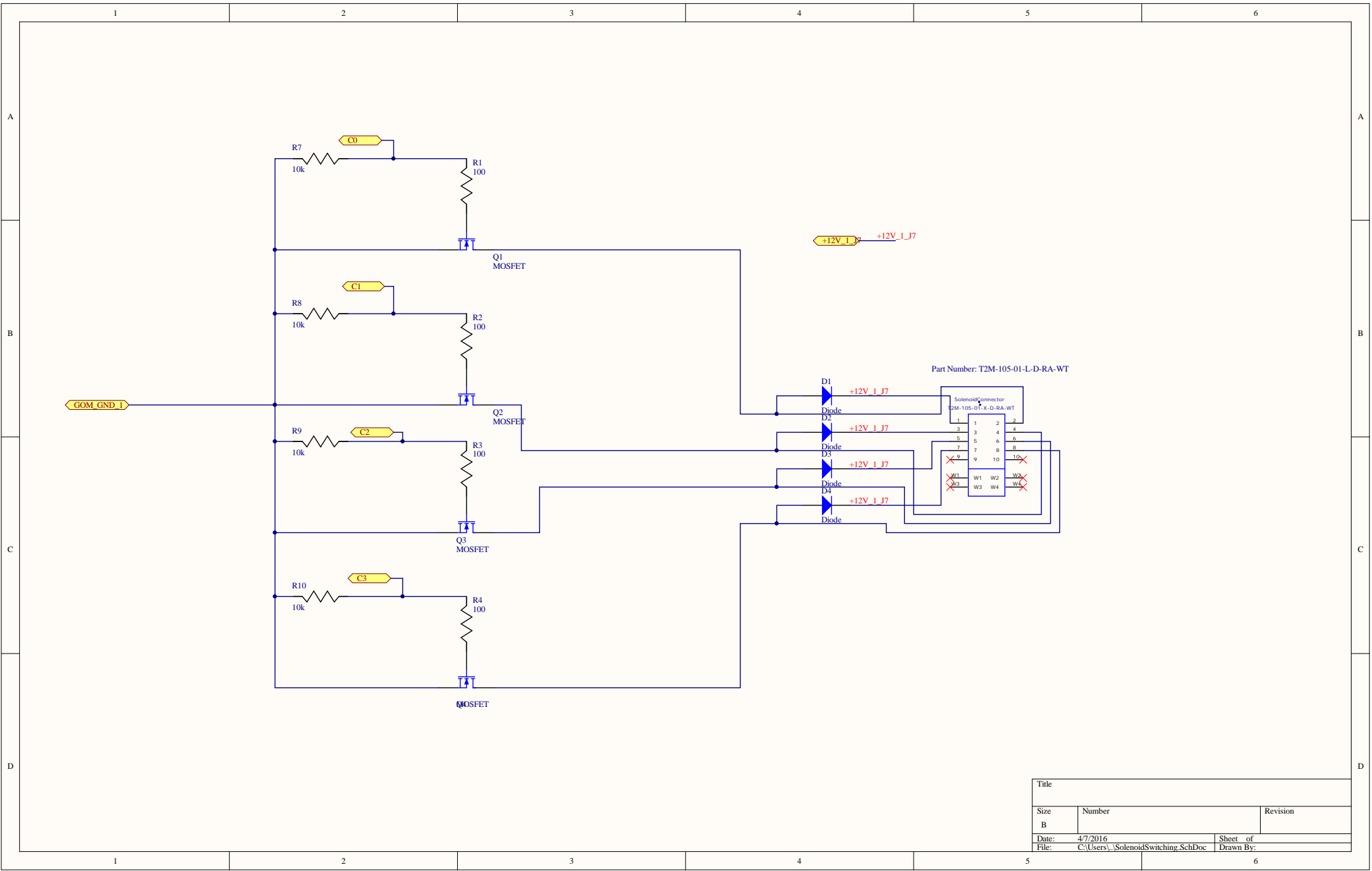
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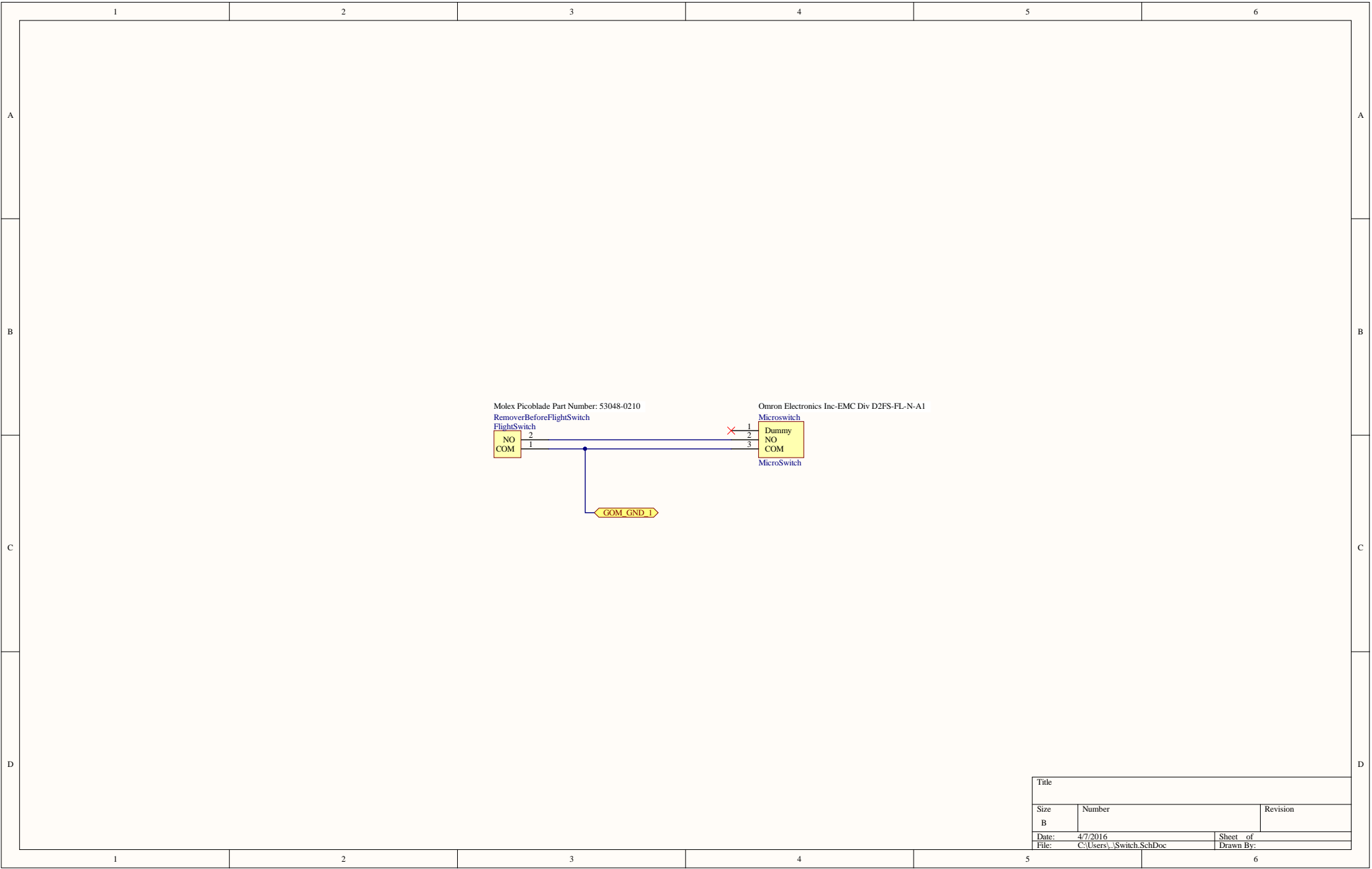




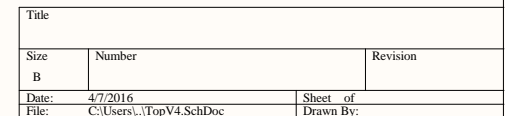


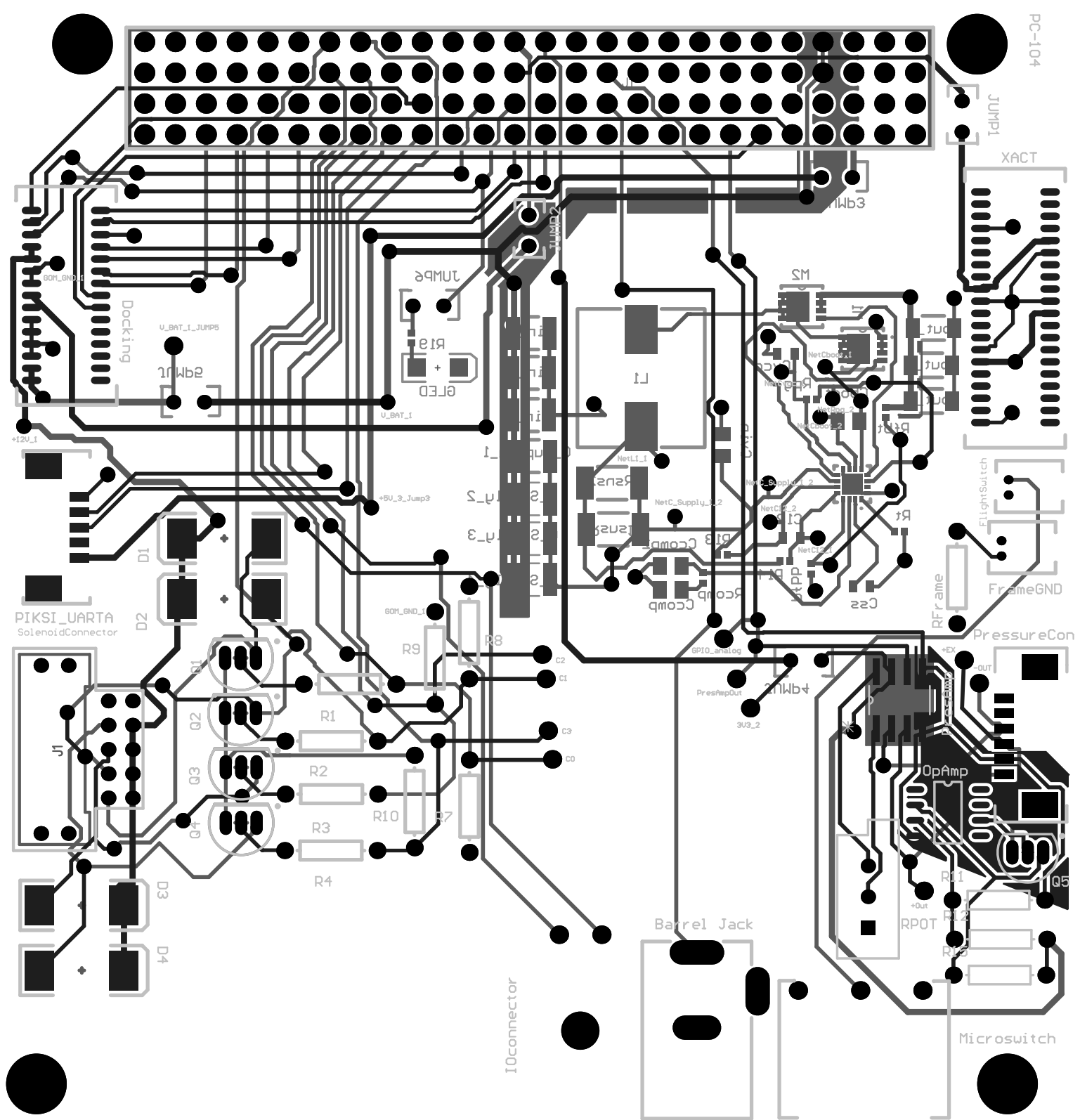
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Date:	4/7/2016	Sheet of
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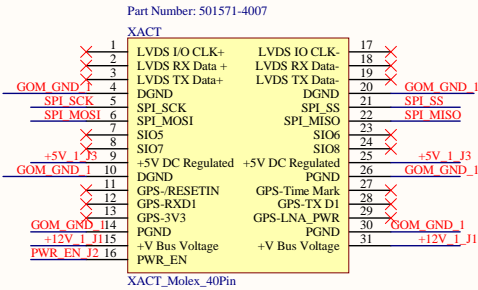
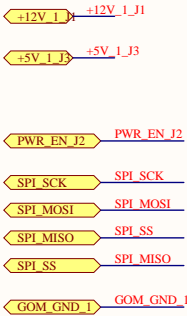


1. Will need a large resistor to be able to GND the chassis to the frame.





Unsure of LVDS, GPS- possibly unconnected  
PWR\_EN (Pin J1-16)-goes to microcontroller GPIO lines, max of 3.3V  
All grounds for the XACT are connected to the DC-DC power ground.



Title		
Size	Number	Revision
B		
Date:	4/7/2016	Sheet of
File:	C:\Users\...\XACT_SchDoc	Drawn By: