

## **HAT2215R, HAT2215RJ**

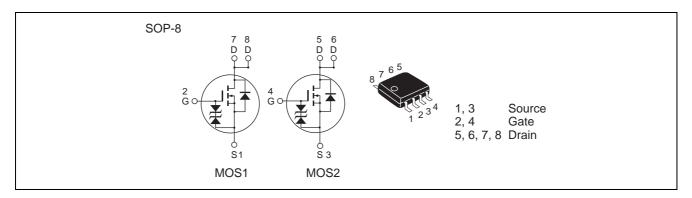
# Silicon N Channel Power MOS FET High Speed Power Switching

REJ03G0486-0300 Rev.3.00 Dec.22.2004

### **Features**

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting

### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Itam	Cumbal	Rat	1114		
Item	Symbol	HAT2215R	HAT2215RJ	Unit	
Drain to source voltage	V <sub>DSS</sub>	80	80	V	
Gate to source voltage	V <sub>GSS</sub>	±20	±20	V	
Drain current	I <sub>D</sub>	3.4	3.4	А	
Drain peak current	I <sub>D(pulse)</sub> Note1	20.4	20.4	А	
Reverse drain current	I <sub>DR</sub>	3.4	3.4	А	
Avalanche current	I <sub>AP</sub> Note 2	_	3.4	А	
Avalanche energy	E <sub>AR</sub> Note 2	_	1.54	mJ	
Channel dissipation	Pch Note3	1.5	1.5	W	
Channel dissipation	Pch Note4	2.2	2.2	W	
Channel temperature	Tch	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

- 2. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50~\Omega$
- 3. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s
- 4. 2 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s

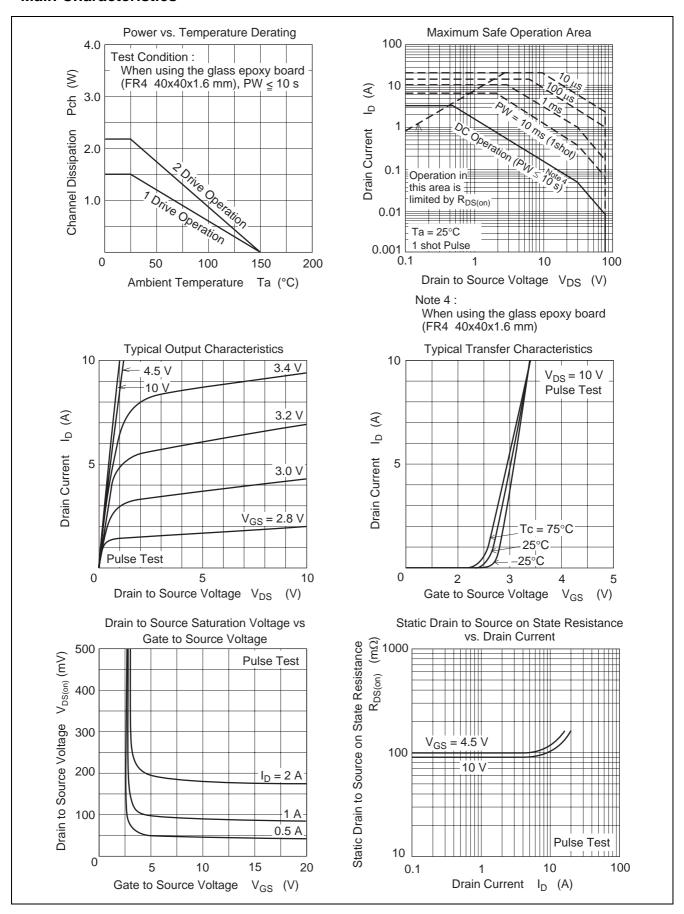
### **Electrical Characteristics**

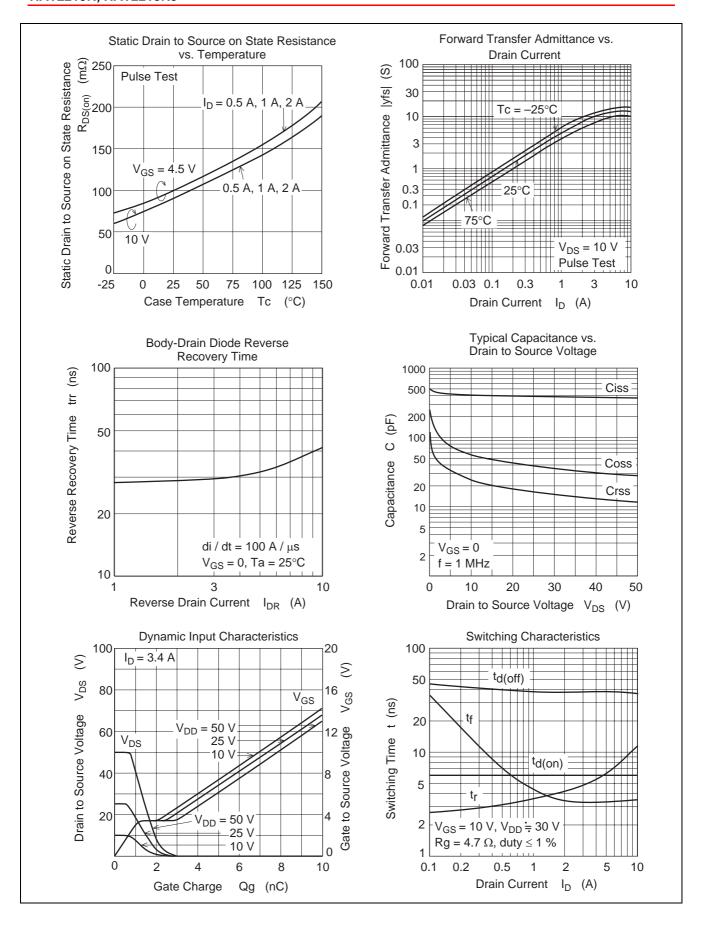
 $(Ta = 25^{\circ}C)$ 

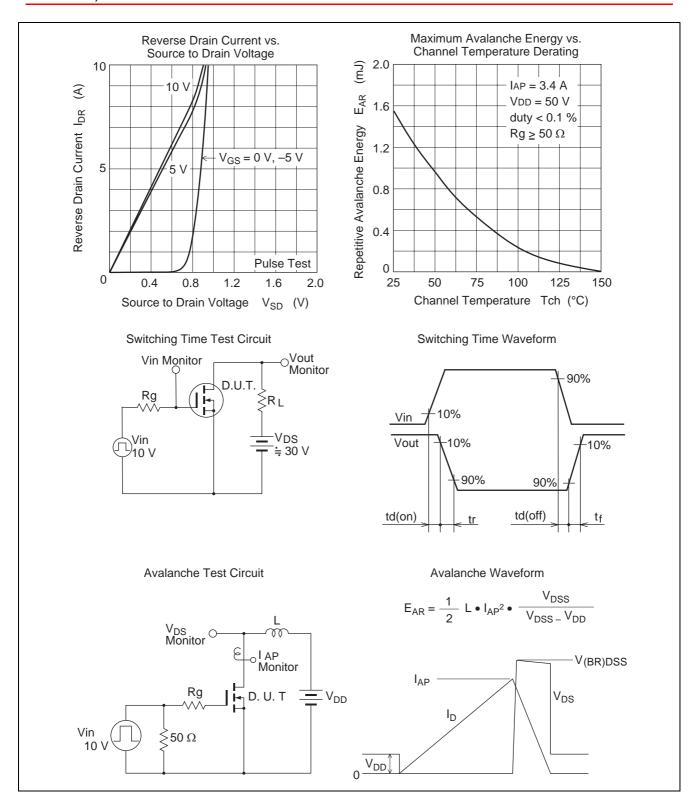
Item		Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage		$V_{(BR)DSS}$	80	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakd	own voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{GS} = 0$
Gate to source leak cu	ırrent	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drai	n current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 80 \text{ V}, V_{GS} = 0$
Zero gate voltage	HAT2215R	I <sub>DSS</sub>	_	_	_	μΑ	$V_{DS} = 64 \text{ V}, V_{GS} = 0$
drain current	HAT2215RJ	I <sub>DSS</sub>	_	_	10	μΑ	Ta = 125°C
Gate to source cutoff voltage		$V_{GS(off)}$	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source	on state	R <sub>DS(on)</sub>	-	88	115	mΩ	$I_D = 1.7 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance		R <sub>DS(on)</sub>	-	100	145	mΩ	$I_D = 1.7 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note5}}$
Forward transfer admittance		y <sub>fs</sub>	4.2	7.0	_	S	$I_D = 1.7 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note5}}$
Input capacitance		Ciss	_	400	_	pF	V <sub>DS</sub> = 10 V
Output capacitance		Coss	_	57	_	pF	$V_{GS} = 0$
Reverse transfer capacitance		Crss	_	24	_	pF	f = 1MHz
Total gate charge		Qg	_	7.3	_	nC	V <sub>DD</sub> = 25 V
Gate to source charge		Qgs	_	1.1	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge		Qgd	_	1.3	_	nC	$I_D = 3.4 \text{ A}$
Turn-on delay time		t <sub>d(on)</sub>	_	6.0	_	ns	$V_{GS} = 10 \text{ V}, I_D = 1.7 \text{ A}$
Rise time		t <sub>r</sub>	_	4.0	_	ns	$V_{DD} \approx 30 \text{ V}$
Turn-off delay time		t <sub>d(off)</sub>	_	39	_	ns	$R_L = 17.6 \Omega$
Fall time		t <sub>f</sub>		3.5	_	ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage		$V_{DF}$	_	0.83	1.08	V	$IF = 3.4 \text{ A}, V_{GS} = 0^{\text{Note5}}$
Body-drain diode reverse		t <sub>rr</sub>	_	30	_	ns	IF =3.4 A, V <sub>GS</sub> = 0
recovery time							diF/ dt = 100 A/μs

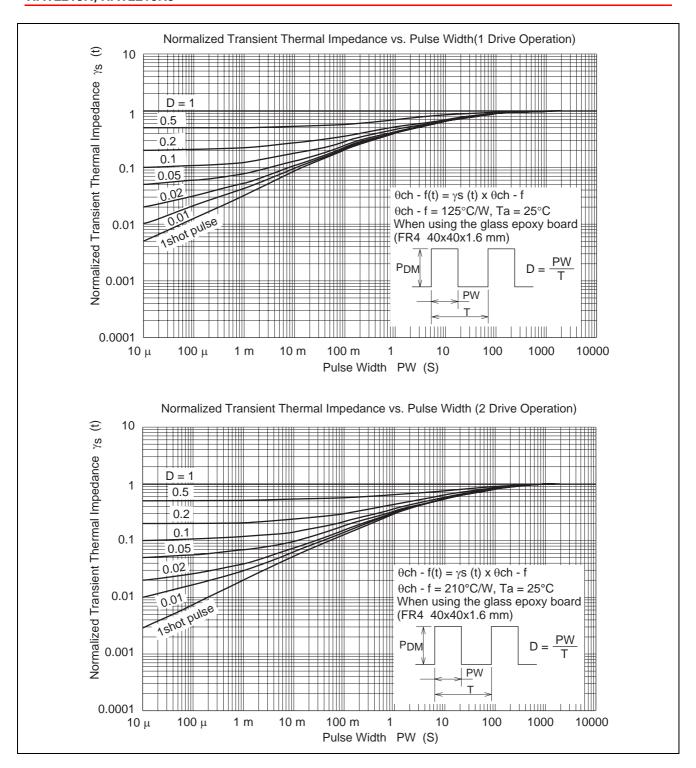
Notes: 5. Pulse test

### **Main Characteristics**

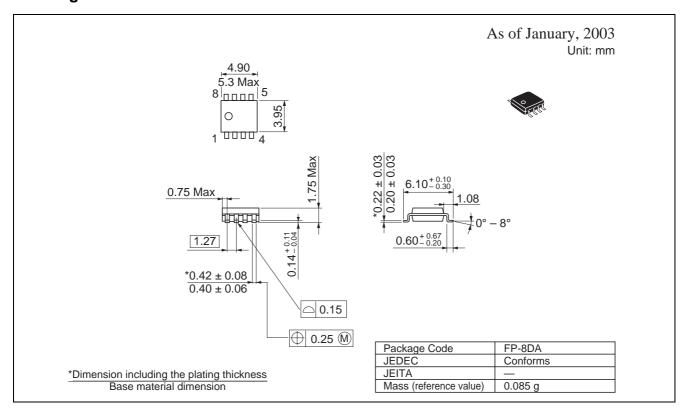








### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container		
HAT2215R-EL-E	2500 pcs	Taping		
HAT2215RJ-EL-E	2500 pcs	Taping		

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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### Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.

Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

**Renesas Technology Taiwan Co., Ltd.** 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001