



MICROCHIP

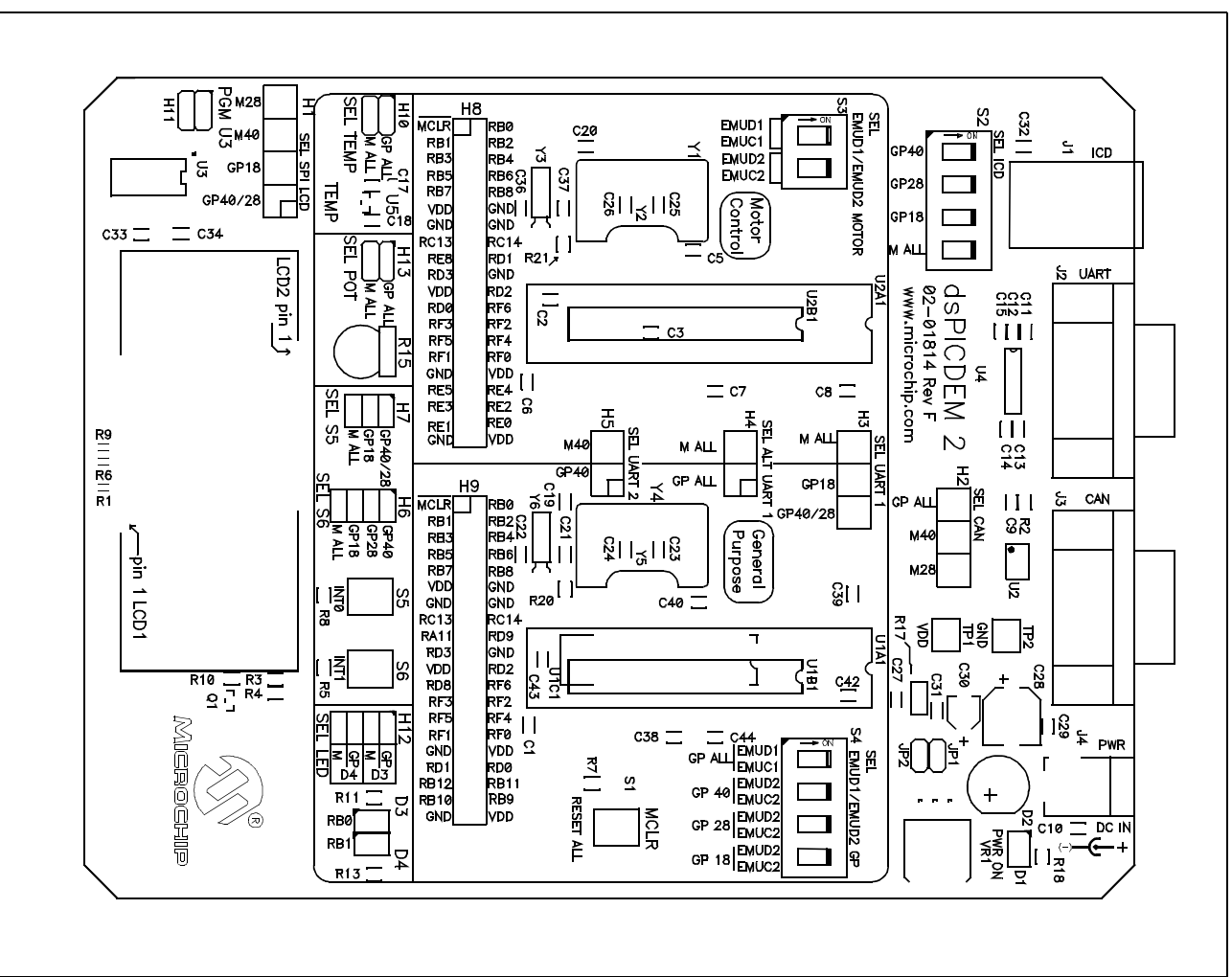
dsPICDEM™ 2.0 DEVELOPMENT BOARD USER'S GUIDE

Appendix A. Hardware Drawings and Schematics

A.1 INTRODUCTION

This Appendix provides a layout drawing of the printed circuit board followed by schematics for the dsPICDEM™ 2 Development Board.

FIGURE A-1: dsPICDEM™ 2 DEVELOPMENT BOARD LAYOUT



10F7)

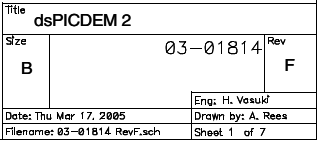


FIGURE A-3: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 2 OF 7)

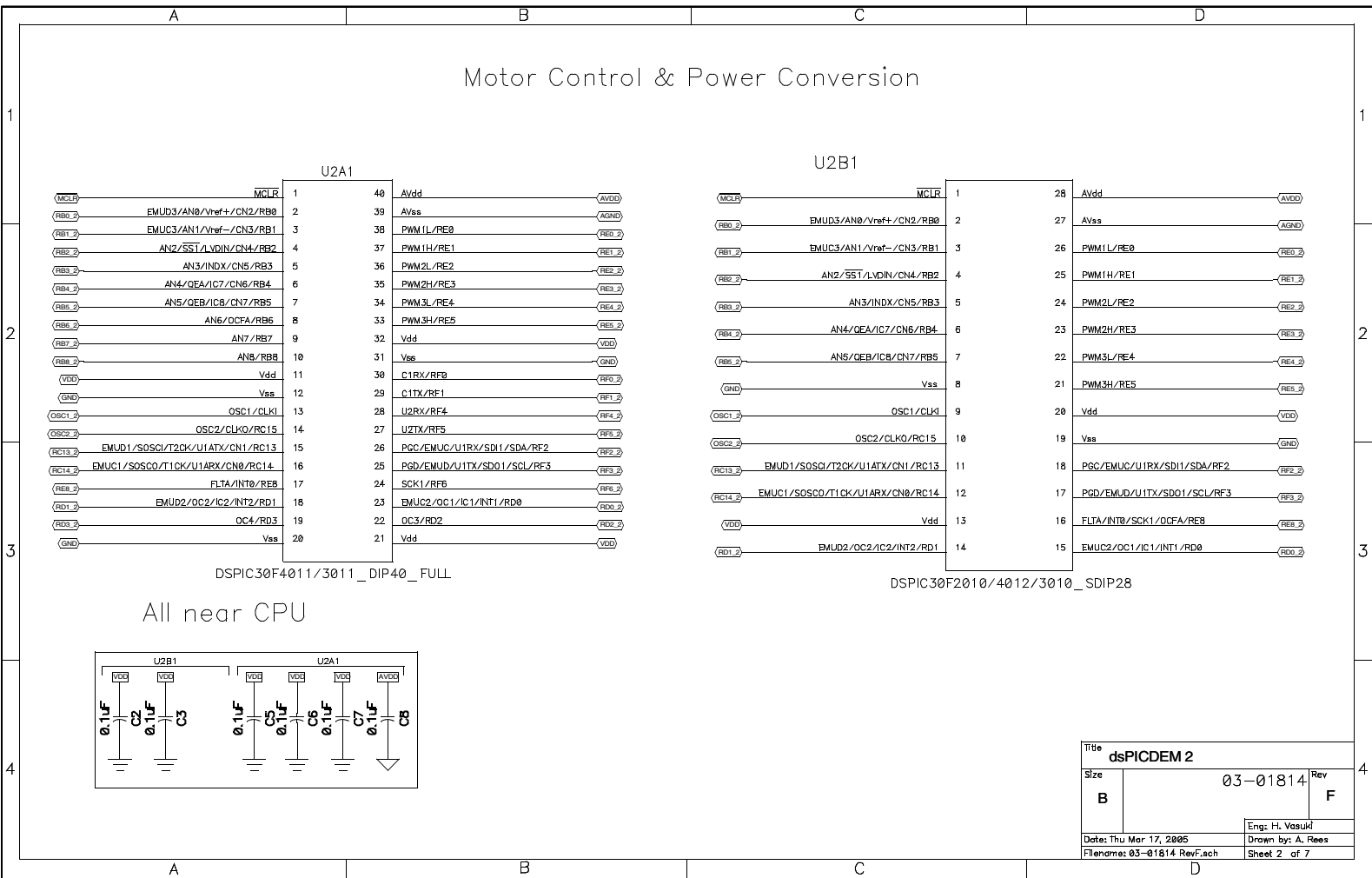


FIGURE A-4: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 3 OF 7)

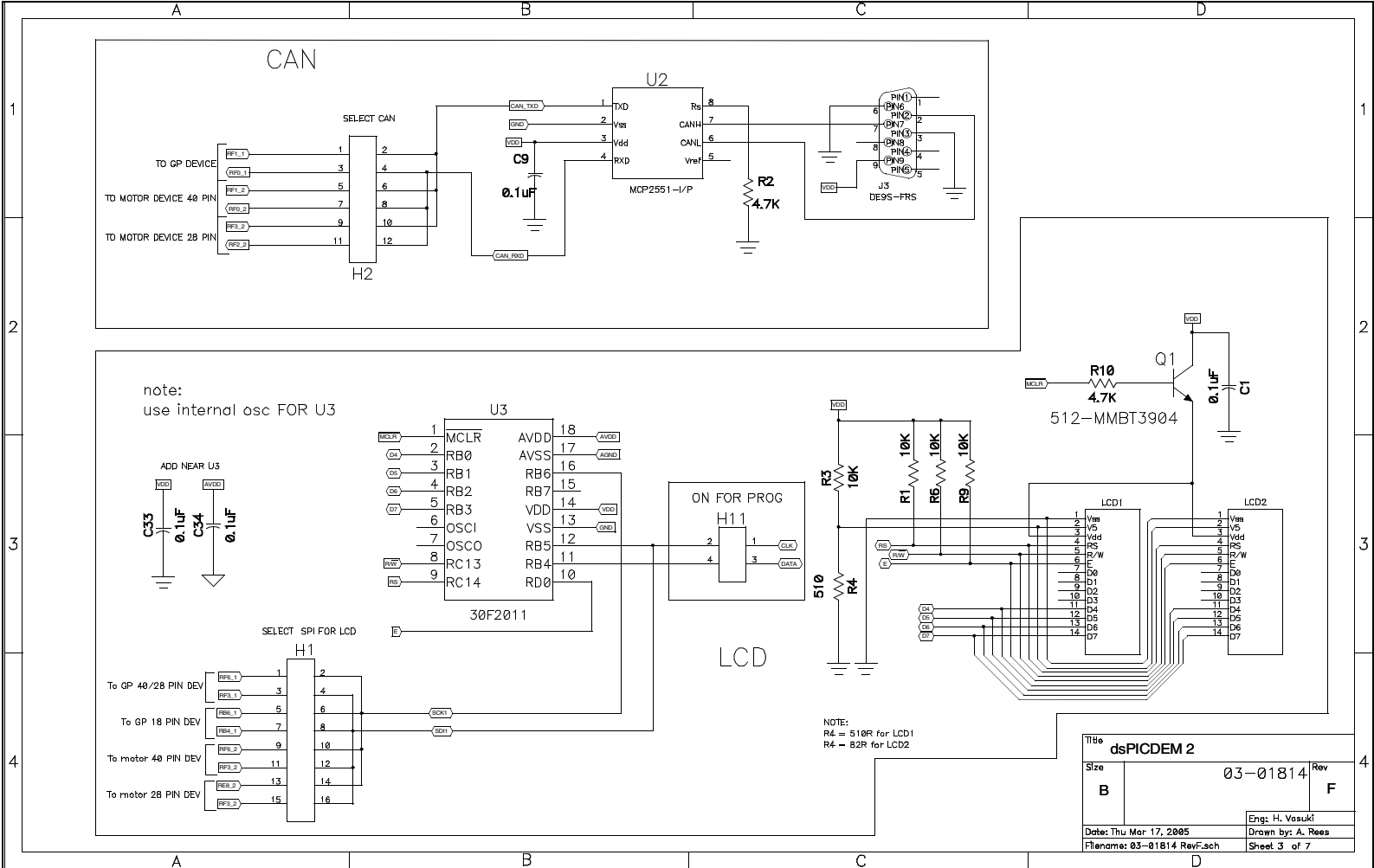
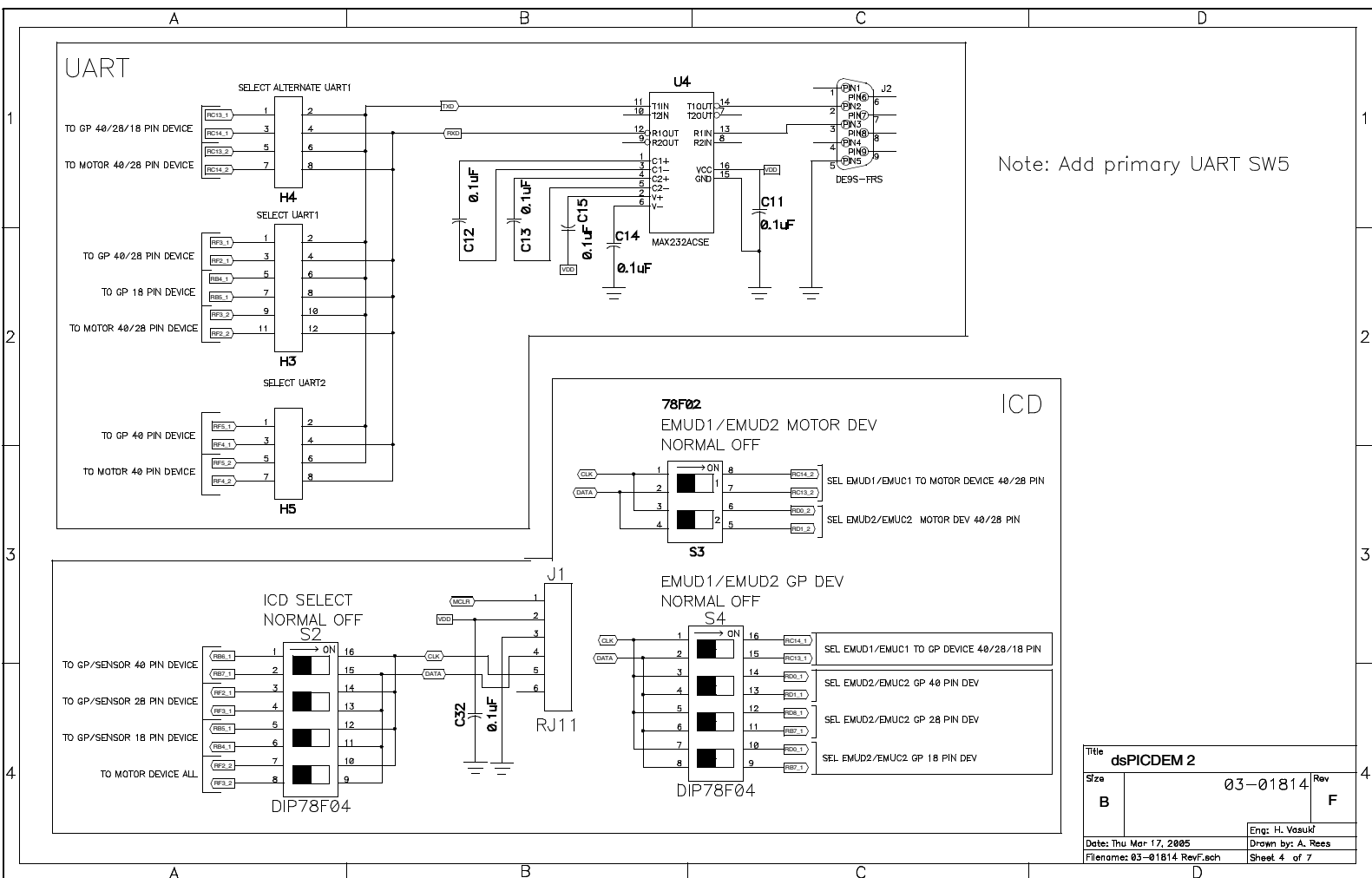


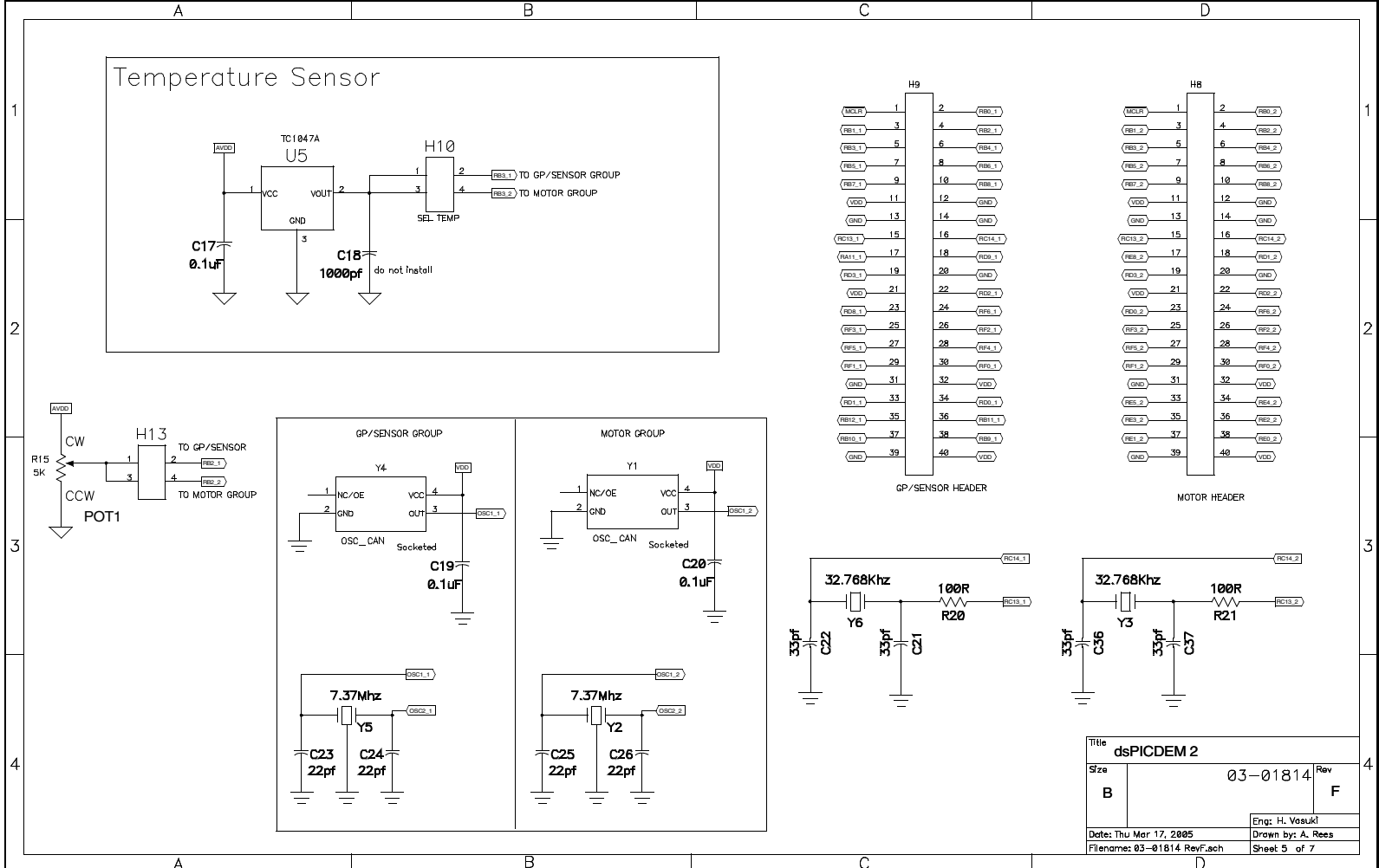
FIGURE A-5: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 4 OF 7)



Title dsPICDEM 2		
Size B	03-01814	Rev F
Eng: H. Vosuk		
Date: Thu Mar 17, 2005		
Drawn by: A. Rees		
Filename: 03-01814 RevF.sch		
Sheet 4 of 7		

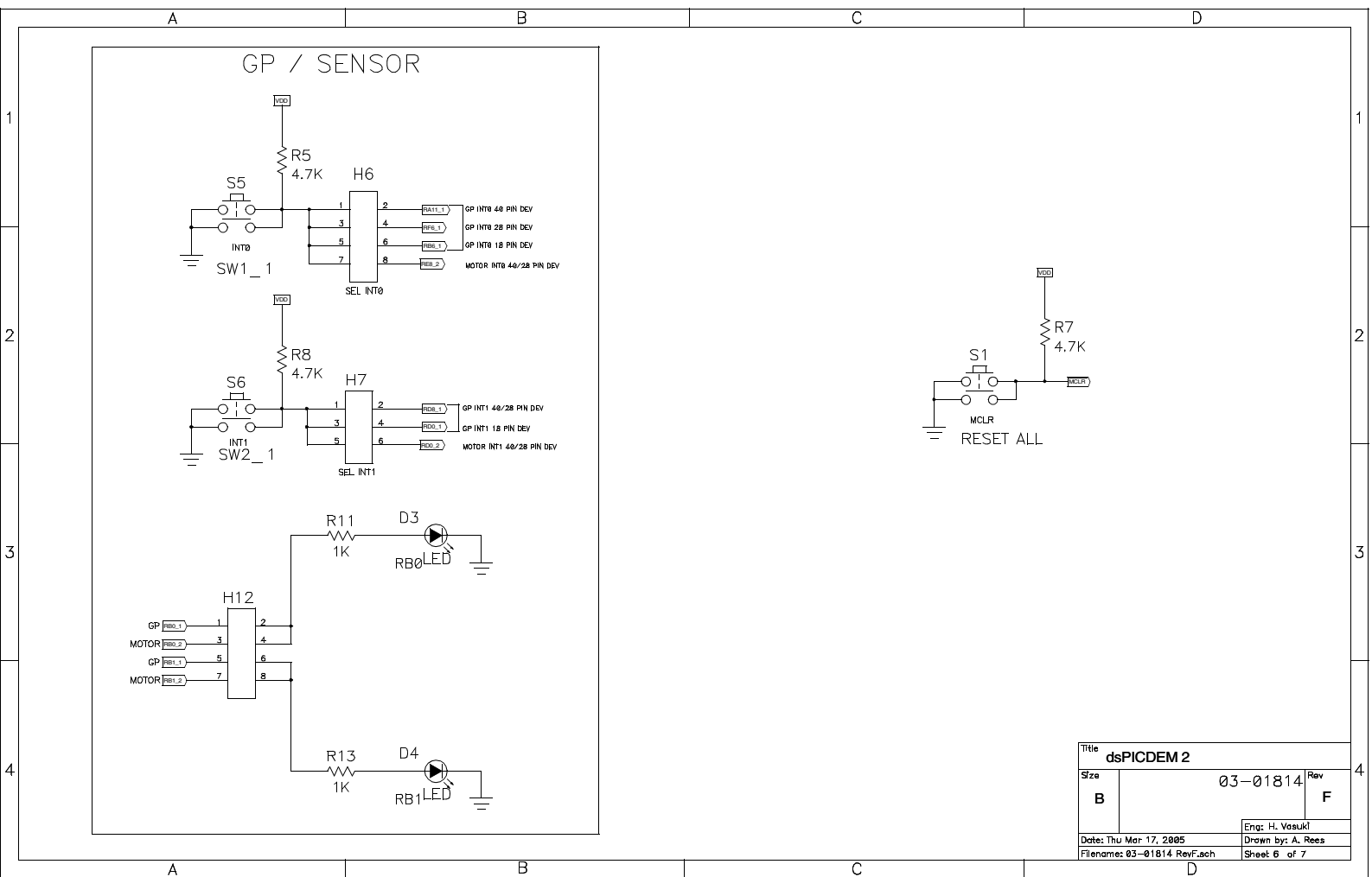
dsPICDEM 2 Development Board User's Guide

FIGURE A-6: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 5 OF 7)



Hardware Drawings and Schematics

FIGURE A-7: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 6 OF 7)



dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC(SHEET
7 OF 7)