

dsPICDEM™ 2.0 DEVELOPMENT BOARD USER'S GUIDE

Appendix A. Hardware Drawings and Schematics

1.1 INTRODUCTION

This Appendix provides a layout drawing of the printed circuit board followed by schematics for the dsPICDEM TM 2 Development Board.

SEL EMUD1/EMUD2 MOTOR EMUD1 EMUC1 EMUD2 EMUC2 OSP ALL 1-11 RB2 37 CS 77 5 L3 GP18 C37 ___ CP28 EΝΡ 8118118 GP40/28 Motor Control GP18 C33 _ _ C34 SEL POT R15 LCD2 pin 1 S UART U4

dsPICDEM 2

02-01814 Rev F

www.microchip.com 222 []] H7 GP40/28 GP18 M ALL св 🗀 **_** C7 ᅜ M ALL SEL CAN H9
| MCLR | RB1 | RB2 | RB5 | RB7 | VDD | RD8 | RF5 | RB1 | RB2 | RB5 | RB1 | C19 C21 ____ GP ALL GP18 င CAN General Purpose 월미_정미월 pin 1 LCD1 ပ္ပေ၂ C40 = ₽ 猛 C30 C<u>42</u> - 100 100 € . MICHOCHIP GP 18 EMUD2
GP 18 EMUD2
GP 18 EMUD2
GP 18 EMUD2
GP 18 EMUD2 SEL S4 EMUD1/EMUD2 ₹ T Q **≥** Q 4 D3 1 ___ R7[] — RESET ALL ś R11 RB0 MCLR PWR D1 RB1 ᇡ R13

FIGURE A-1: dsPICDEM™ 2 DEVELOPMENT BOARD LAYOUT

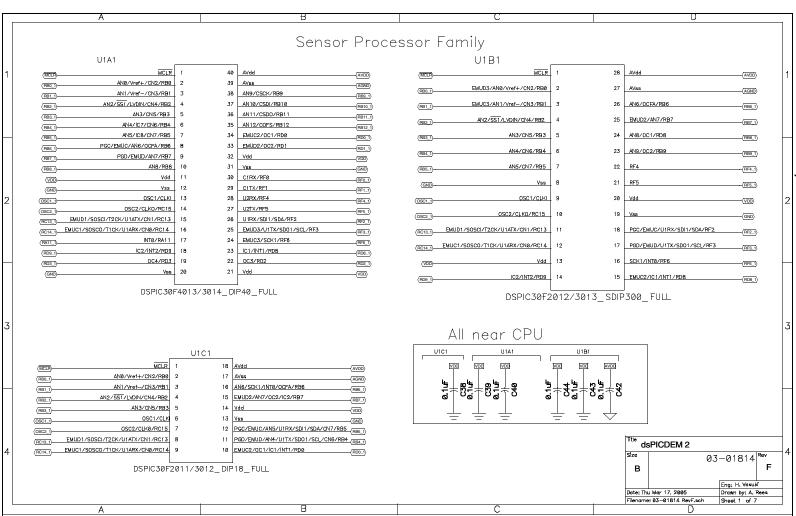


FIGURE A-2: $\mathsf{dsPICDEM^{rm}}\,2$ DEVELOPMENT BOARD SCHEMATIC (SHEET 1 OF 7) dsPICDEM 2 Development Board User's

DS51558A-page 97

FIGURE A-3: ${\sf dsPICDEM^{\sf TM}}\,2$ DEVELOPMENT BOARD SCHEMATIC (SHEET 2 OF 7)

U2

MCP2551-I/P

 \equiv

VDD

CAN

RF1_1

(RF0_1

RF1_2

TO GP DEVICE

SELECT CAN

GND VDD

C9

0.1uF

FIGURE A-4: ${\sf dsPICDEM^{TM}\,2}$ DEVELOPMENT BOARD SCHEMATIC (SHEET 3 OF 7)

dsPICDEM 2 Development Board User's Guide

DS51558A-page 99

FIGURE A-5: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 4 OF 7)

dsPICDEM 2 Development Board User's Guide

