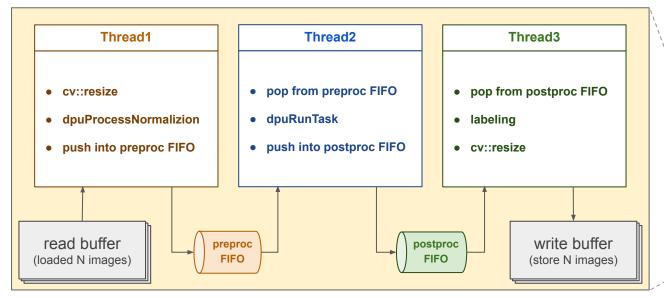
## **Inference Processes**



- Separated the inference processes into three threads so that the software pre/post processes can be run behind the inference process by the DPU.
- Implemented the thread-safe FIFO to access data between threads efficiently.

