

Viktor Toman

<https://vitosvk.github.io/>

Work Address

IST Austria, Am Campus 1
3400 Klosterneuburg, Austria
viktor.toman@ist.ac.at

Home Address

Billrothstraße 75A/3/5
1190 Vienna, Austria
vtoman07@gmail.com

PERSONAL INFORMATION

Date of Birth: 10. July 1993
Place of Birth: Nové Zámky, Slovakia
Nationality: Slovak

EXPERIENCE

Research Intern at Google Research, Zürich, Switzerland remote-covid19 Aug 2020 – Nov 2020

- On-Device Machine Learning
- Advisors: Anna Goralska, Félix de Chaumont Quitry
- Programming: C++, MapReduce, Python, TensorFlow

Research Intern at Google Research, Mountain View, California, USA Jul 2019 – Oct 2019

- Machine Learning in Higher-Order Theorem Proving
- Advisors: Kshitij Bansal, Markus Rabe
- Programming: MapReduce, Python, TensorFlow

Research Intern at Google Research, Mountain View, California, USA Jul 2018 – Oct 2018

- Machine Learning in Higher-Order Theorem Proving
- Advisors: Sarah Loos, Christian Szegedy
- Programming: Python, TensorFlow

EDUCATION

Ph.D. at IST Austria, Klosterneuburg, Austria Sep 2016 – Sep 2021 (expected)

- Computer Science – Formal Methods
 - Focus: Verification of concurrent programs, Symbolic model checking
- Advisors: Krishnendu Chatterjee, Andreas Pavlogiannis

Mgr. at Masaryk University, Brno, Czech Republic Sep 2014 – Jun 2016

- Computer Science – Parallel and Distributed Systems
 - Graduated with honours, CGPA 1.00, Dean's award

Bc. at Masaryk University, Brno, Czech Republic Sep 2011 – Jun 2014

- Computer Science – Mathematical Informatics
 - Graduated with honours, CGPA 1.34, Dean's award

PUBLICATIONS

T. L. Bui, K. Chatterjee, T. Gautam, A. Pavlogiannis, VT. In preparation
The Reads-From Equivalence for the TSO and PSO Memory Models. [C++]

K. Bansal, C. Szegedy, M. N. Rabe, S. M. Loos, VT. Submitted
Learning to Reason in Large Theories without Imitation. [pdf]

K. Chatterjee, A. Pavlogiannis, VT. OOPSLA 2019
Value-centric Dynamic Partial Order Reduction. [pdf] [C++]

P. Ashok, T. Brázdil, K. Chatterjee, J. Křetínský, C. H. Lampert, VT. QEST 2019
Strategy Representation by Decision Trees with Linear Classifiers. [pdf] [Python]

K. Chatterjee, M. Henzinger, V. Loitzenbauer, S. Oraee, VT. CAV 2018
Symbolic Algorithms for Graphs and MDPs with Fairness Objectives. [pdf] [C++]

T. Brázdil, K. Chatterjee, J. Křetínský, VT. TACAS 2018
Strategy Representation by Decision Trees in Reactive Synthesis. [pdf] [Java]

LEADERSHIP	Supervised Shreya Pathak – IST Austria Research Intern remote-covid19 ■ Project in progress	Apr 2020 – Jul 2020
	Supervised Pratyush Agarwal – IST Austria Research Intern remote-covid19 ■ Project in progress	Apr 2020 – Jul 2020
	Supervised Truc Lam Bui – IST Austria Research Intern ■ The Reads-From Equivalence for the TSO and PSO Memory Models	Dec 2019 – Mar 2020
	Supervised Tushar Gautam – IST Austria Research Intern ■ The Reads-From Equivalence for the TSO and PSO Memory Models	May 2019 – Jul 2019
TEACHING	Teaching Assistant – Formal Methods for Learned Systems ■ Ph.D. course at IST Austria	Feb 2020 – Apr 2020
	Teaching Assistant – Randomized Algorithms ■ Ph.D. course at IST Austria	Feb 2019 – May 2019
	Teaching Assistant – Randomized Algorithms ■ Ph.D. course at IST Austria	Feb 2018 – May 2018
LANGUAGES	■ C++, Java, Python ■ English, Czech, Slovak (native), German (pre-intermediate)	
REFERENCES	Krishnendu Chatterjee IST Austria krishnendu.chatterjee@ist.ac.at	Anna Goralska Google Research annago@google.com
	Andreas Pavlogiannis Aarhus University pavlogiannis@cs.au.dk	Christian Szegedy Google Research szegedy@google.com

[CV last update: October 2020]