基于机器学习的高速缓存预取

Leaper: A Learned Prefetcher for Cache Invalidation in LSM-tree based Storage Engines

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背景:

- 1. 数据库缓存
 - a. 缓存淘汰算法: LRU, LFU...
- 2. LSM-tree
 - a. Log-Structured Merge-Tree(日志结构合并树)
 - b. 对比B树,将随机读取磁盘变成了顺序读写磁盘
- 3. 缓存失效:
 - a. This(flush, compaction) invalidates their corresponding entries and statistics in the cache and leads to cache miss for their lookups. (LSM-tree中的追加写和写时复制技术需要引入可变的存储数据块和频繁的后台操作来重新组织数据块。这些后台操作会使得数据块对应的缓存失效,从而导致缓存命中率骤降和访问延迟的急剧升高。)
 - b. frequency of compactions decreased cache hit rates

设计思路:

Fundamentally, the cache invalidation problem can be addressed by minimizing |C| and |C| during each compaction (or flush). It requires us to predict which records are accessed in the near future, and then swap them into the cache in advance.

- It is a prediction problem that predicts the future access of each record in the set |C and Mi|. 提出了使用在整体数据流中不会被影响的粒度,也就是key range的粒度作为整体的统计信息采集粒度,并且根据这些统计信息在compaction和flush的过程中使用机器学习方法去预测哪些key range会被访问
 - 1. binary classification problem:
 - a. given the access of a record in the latest $x \cdot t$ minutes, predict whether this record is accessed in the following T minutes, where t, x, and T refer to a statistical time interval, number of intervals, and the prediction duration.

系统设计

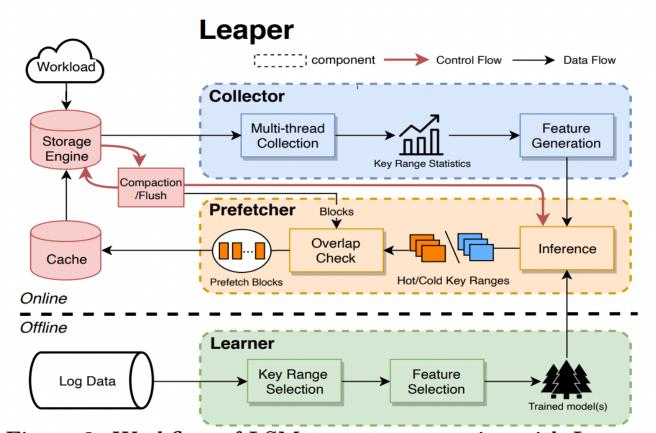


Figure 3: Workflow of LSM-tree storage engine with Leaper.

实现细节

Algorithm 1: Key Range Selection

Input: Total key range T, initial size A, access information and decline threshold α

Output: Most suitable granularity A^*

- 1 Initialize access matrix M ($N \times \frac{T}{A}$ bits for N time intervals and $\frac{T}{A}$ key ranges) for key range size A;
- **2** Define the number of zeroes in M as Z(A);
- 3 while $2 \cdot Z(2A) > \alpha \cdot Z(A)$ do
- $A \mid A \leftarrow 2A;$
- 5 Binary search to find the maximum value A^* satisfying $A^* \cdot Z(A^*) > \alpha A \cdot Z(A)$ from A to 2A;
- 6 return A^*

模型

Gradient Boosting Decision Tree (GDBT)

input: a feature vector with 18 dimensions (i.e., 6 read arrival rates, 6 write arrival rates, 3 timestamp features, and 3 precursor arrival rates). (其中的

output: a binary digit (i.e., 1 or 0) indicating whether this key range would be accessed soon the loss functions: Square Loss and Logarithmic Loss.

实验结论

在天猫买家库订单表、钉钉以及sysbench生成的zipf分布的workload下比较了论文的方法和已有方法。实验结果表明,论文中的方法相比于传统方法可以多消除70%以上的cache invalidation,同时能消除几乎所有的latency抖动。

结论

- 1. 定义了cache invalidation问题,并且识别出了在LSM-tree问题中产生这个问题的根本原因。同时我们提出了基于机器学习的高速缓存预取策略来解决这个问题。
- 2. 保证准确度,同时尽可能降低在离线训练和在线推断方面的开销
- 3. 在多种workload上进行了比较

读者观点

- 1. 设计有太多自定义的东西在,基本只能看个设计思想
- 2. 细节太多了