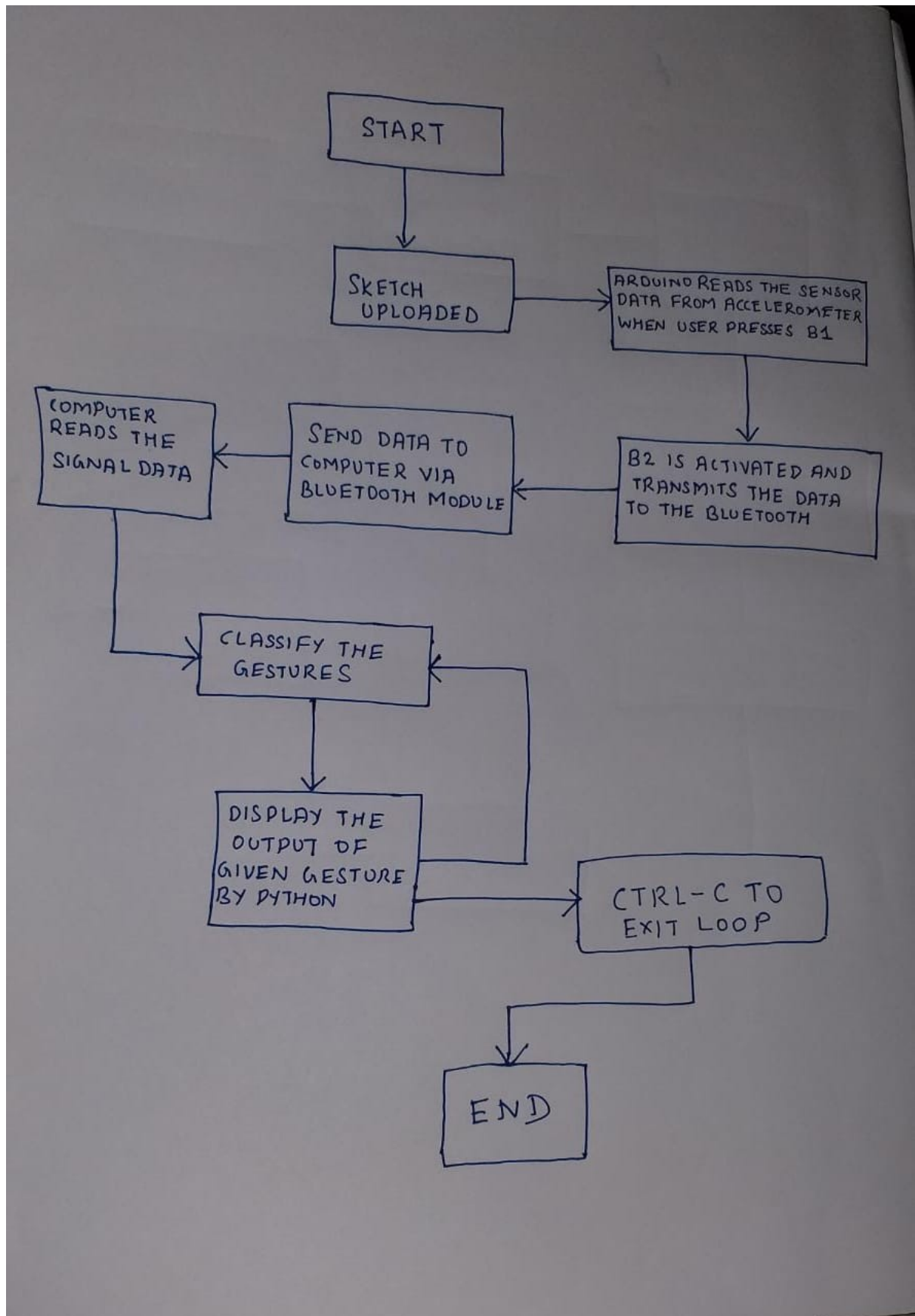




Block diagram:



Circuit design:

