

# CSE2006 Quiz 01: FS: 21-22

1. The 16-bit register of 8086 consists of \_\_\_\_\_ flags \*  
(0.5 Points)

- ☒ 9
- ☐ 8
- ☐ 12
- ☐ 16

2. Physical address of 8086 is \_\_\_\_ bits \*  
(0.5 Points)

- ☐ 32
- ☐ 16
- ☒ 20
- ☐ 8

3. During comparison operation, the result of comparing or subtraction is stored in \*  
(0.5 Points)

- ☐ Memory
- ☐ Stack
- ☐ Registers
- ☒ Nowhere

4. MOV AX, [BX] is example of \*  
(0.5 Points)

- ☐ Immediate Addressing Mode
- ☐ Direct Addressing Mode
- ☐ Register Addressing Mode
- ☒ Indirect Addressing Mode

5. What is the content of Stack Pointer? \*  
(0.5 Points)

- ☐ Address of Current Instruction
- ☐ Address of Next Instruction
- ☒ Address of top element in the stack
- ☐ None of these

6. MOV AX, [1234H] is example of \*  
(0.5 Points)

- ☐ Register Addressing Mode
- ☐ Direct Addressing Mode
- ☒ Immediate Addressing Mode
- ☐ Indirect Addressing Mode

7. After execution of the following instructions, the content of AX register is  
MOV AL,07;  
MOV BL,09;  
MUL BL \*  
(0.5 Points)

- ☐ 0063

- ☒ 003F
- ☐ 3F00
- ☐ 6300

8. If the data is available in a register and it is referred using a particular register, it is \*

(0.5 Points)

- ☐ Register Addressing Mode
- ☒ Direct Addressing Mode
- ☐ Indexed Addressing Mode
- ☐ Immediate Addressing Mode

9. The instruction that performs logical AND operation and the result of the operation is not available is \*

(0.5 Points)

- ☒ TEST
- ☐ AND
- ☐ AAA
- ☐ XOR

10. Contents of a base register are added to the contents of index register in \*

(0.5 Points)

- ☐ Indexed addressing mode
- ☐ Based indexed addressing mode
- ☐ Relative based indexed addressing mode
- ☒ Based indexed and Relative based indexed addressing mode

11. The instruction that loads the AH register with the lower byte of the flag register is \*

(0.5 Points)

- ☒ LAHF
- ☐ SAHF
- ☐ AH
- ☐ PUSHF

12. 2's complement of AX is \*

(0.5 Points)

- ☐ NOT AX
- ☐ CMP AX
- ☒ NEG AX
- ☐ XOR AX

13. 16 bit address bus can generate \_\_\_\_\_ addresses \*

(0.5 Points)

- ☐ 65536
- ☐ 256
- ☐ 1048576
- ☒ 32768

14. Example for Non Maskable Interrupt \*

(0.5 Points)

- ☐ RST6.5
- ☐ RST6.6

☒ TRAP☐ INTR

15. Which of the following instructions is not true? \*  
(0.5 Points)

- ☐ MOV AX, [2000]
- ☐ MOV AX, BX
- ☒ MOV [2000], [4000]
- ☐ MOV [2000], AX

16. Size of Instruction Pointer (bits) \*  
(0.5 Points)

- ☒ 16
- ☐ 32
- ☐ 8
- ☐ 64

17. In the RCL instruction, the contents of the destination operand undergo function as \*  
(0.5 Points)

- ☐ parity flag is pushed into MSB & LSB is pushed into the carry flag
- ☒ carry flag is pushed into LSB & MSB is pushed into the carry flag
- ☐ carry flag is pushed into MSB & LSB is pushed into the carry flag
- ☐ auxiliary flag is pushed into LSB & MSB is pushed into the carry flag

18. The physical address when DS = 2345H and IP= 1000H is \*  
(0.5 Points)

- ☐ 2345H
- ☐ 23450H
- ☐ 12345H
- ☒ 24450H

19. Which of the following is Invalid Instruction? \*  
(0.5 Points)

- ☐ MOV AX, 5000H
- ☒ MOV DS, 5000H
- ☐ PUSH AX
- ☐ MOV AX, BX

20. Size of each segment in 8086 memory \*  
(0.5 Points)

- ☐ 32kb
- ☒ 64kb
- ☐ 16kb
- ☐ 128kb

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