# Agenda

1. literature survey [3 papers + X]
2. adaption of push-pull concept from Maksimovic
3. GaN25 parameter simulation [S-parameter,ON/OFF state]
4. determine load impedance [input of PPA - GaN25]
5. determine dimension of transistors
6. tuning schematic parameter for optimal simulation
7. enhancement/extension of 1-bit push-pull to 3-bit push-pull stage
8. digital input control voltage
9. determine eight slopes of the current sources in schematic
10. Riemanncode generation with MatLab
11. control schematic with theoretical input [Riemanncode]

# Problems

1. frequency dependent load impedance
2. $\rightarrow$ slopes ambiguous
3. $\rightarrow$ riemanncode generation not possibleb