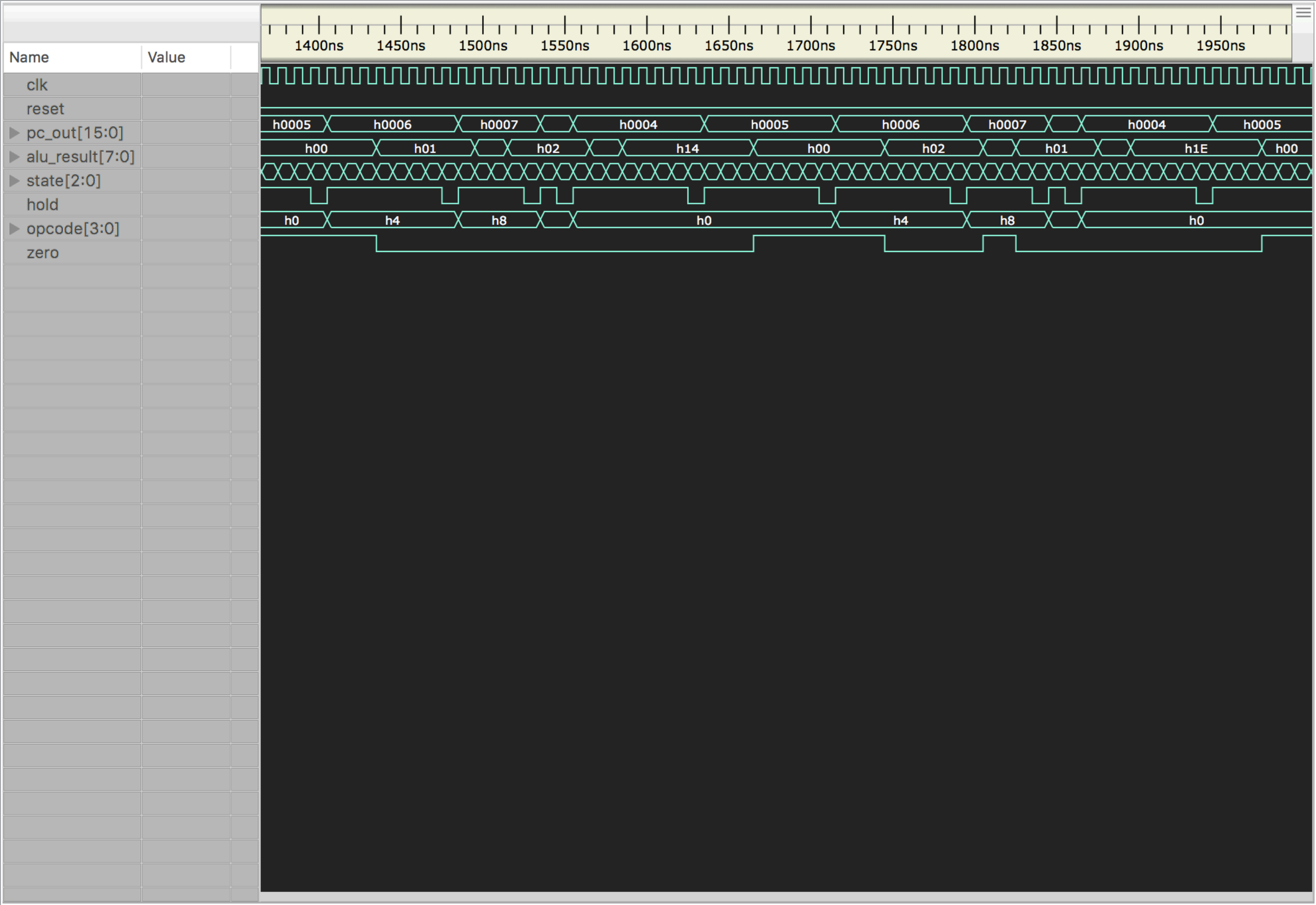
Name
Value
clk
reset
pc_out[15:0]
alu_result[7:0]
state[2:0]
hold
opcode[3:0]
zero
1400ns
1450ns
1500ns
1550ns
1600ns
1650ns
1700ns
1750ns
1800ns
1850ns
1900ns
1950ns
h0005
h0006
h0007
h0004
h0005
h0006
h0007
h0004
h0005
h00
h01
h02
h14
h00
h02
h01
h1E
h00
h0
h4
h8
h0
h4
h8
h0