

DOE Analysis: Doe Analysis Report

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DOE Analysis Report Design of Experiments Analysis Report Actual by Predicted Plot Model Formula

ttemp = 65.087259 - (1.327588)*Transceiver_Manufacturer[Acceligh] -
(2.865921)*Transceiver_Manufacturer[CENTERA] +
(995603574387.397095)*Transceiver_Manufacturer[Eoptolink] -
(1.332634)*Transceiver_Manufacturer[Finisar] - (1.723358)*Transceiver_Manufacturer[Hisense] -
(89043285333.383804)*Transceiver_Manufacturer[Intel Corp] -
(225036984293.564178)*Transceiver_Manufacturer[Ligent Photonics] -
(2.830278)*Transceiver_Manufacturer[NON-JNPR] - (2.493208)*Transceiver_Manufacturer[O-NET] -
(0.091696)*Rack_Unit[2] - (0.318191)*Rack_Unit[3] - (0.559426)*Rack_Unit[4] -
(0.407374)*Rack_Unit[5] - (1.245049)*Rack_Unit[6] - (0.803411)*Rack_Unit[7] -
(0.726129)*Rack_Unit[8] - (25316310970.130054)*Rack_Unit[9] - (157393877450.204346)*Rack_Unit[10] -
(12261302087.422798)*Rack_Unit[11] + (39108032648.411598)*Rack_Unit[12] -
(42600213214.245415)*Rack_Unit[13] + (47542123827.449921)*Rack_Unit[14] - (2.589378)*Rack_Unit[15]
+ (130600262483.733505)*Rack_Unit[16] - (2.391941)*Rack_Unit[17] -
(22238103387.481415)*Rack_Unit[18] + (20518340874.508564)*Rack_Unit[19] +
(11409702187.228548)*Rack_Unit[20] + (17027339264.842680)*Rack_Unit[21] -
(52777871406.249481)*Rack_Unit[22] - (2.216127)*Rack_Unit[23] - (2.493841)*Rack_Unit[24] -
(1.389647)*Rack_Unit[25] - (1.596277)*Rack_Unit[26] + (504671490217.245789)*Rack_Unit[27] +
(0.608561)*Rack_Unit[28] - (745987231788.282471)*Rack_Unit[29] - (2.316164)*Rack_Unit[30] +
(60773109731.877510)*Rack_Unit[31] - (29329808202.733528)*Rack_Unit[32] -
(127028477285.293381)*Rack_Unit[33] + (23950865230.437737)*Rack_Unit[34] -
(93451790716.674728)*Rack_Unit[36] - (153356658974.682312)*Rack_Unit[37] -
(716438153396.390137)*Rack_Unit[38] - (23967478313.450790)*Rack_Unit[39] +
(77478374274.818604)*Rack_Unit[40] + (177520992350.604248)*Rack_Unit[41] -
(105277276692.061096)*Rack_Unit[42] + (0.309313)*Fan_Speed_Range[L] +
(0.055849)*Transceiver_Manufacturer*Rack_Unit[Acceligh,2] +
(6846997554.203388)*Transceiver_Manufacturer*Rack_Unit[CENTERA,2] +
(239270543036.483032)*Transceiver_Manufacturer*Rack_Unit[Eoptolink,2] -
(0.028531)*Transceiver_Manufacturer*Rack_Unit[Finisar,2] -
(0.872444)*Transceiver_Manufacturer*Rack_Unit[Hisense,2] +
(89043285332.997589)*Transceiver_Manufacturer*Rack_Unit[Intel Corp,2] +
(2901564163.362468)*Transceiver_Manufacturer*Rack_Unit[Ligent Photonics,2] -
(0.165506)*Transceiver_Manufacturer*Rack_Unit[NON-JNPR,2] -
(95869263318.421158)*Transceiver_Manufacturer*Rack_Unit[O-NET,2] -
(0.290958)*Transceiver_Manufacturer*Rack_Unit[Acceligh,3] -
(11126735750.694855)*Transceiver_Manufacturer*Rack_Unit[CENTERA,3] -
(145314086253.012909)*Transceiver_Manufacturer*Rack_Unit[Eoptolink,3] +
(0.182158)*Transceiver_Manufacturer*Rack_Unit[Finisar,3] -
(0.038297)*Transceiver_Manufacturer*Rack_Unit[Hisense,3] +
(89043285332.063049)*Transceiver_Manufacturer*Rack_Unit[Intel Corp,3] +
(6625085651.482124)*Transceiver_Manufacturer*Rack_Unit[Ligent Photonics,3] -
(7451281382.638187)*Transceiver_Manufacturer*Rack_Unit[NON-JNPR,3] -
(9524141533.845520)*Transceiver_Manufacturer*Rack_Unit[O-NET,3] +
(0.425156)*Transceiver_Manufacturer*Rack_Unit[Acceligh,4] +
(1435884028.645404)*Transceiver_Manufacturer*Rack_Unit[CENTERA,4] -
(52100161012.456764)*Transceiver_Manufacturer*Rack_Unit[Eoptolink,4] +
(0.404277)*Transceiver_Manufacturer*Rack_Unit[Finisar,4] +
(0.195316)*Transceiver_Manufacturer*Rack_Unit[Hisense,4] +
(89043285332.186386)*Transceiver_Manufacturer*Rack_Unit[Intel Corp,4] +
(1385194016.126970)*Transceiver_Manufacturer*Rack_Unit[Ligent Photonics,4] +
(76061351.454695)*Transceiver_Manufacturer*Rack_Unit[NON-JNPR,4] -
(63144711640.945198)*Transceiver_Manufacturer*Rack_Unit[O-NET,4] -
(0.043430)*Transceiver_Manufacturer*Rack_Unit[Acceligh,5] -
(1589881172.302978)*Transceiver_Manufacturer*Rack_Unit[CENTERA,5] -
(70495997551.912918)*Transceiver_Manufacturer*Rack_Unit[Eoptolink,5] +
(0.094667)*Transceiver_Manufacturer*Rack_Unit[Finisar,5] -
(0.457838)*Transceiver_Manufacturer*Rack_Unit[Hisense,5] +
(89043285332.175201)*Transceiver_Manufacturer*Rack_Unit[Intel Corp,5] +
(459186830.209145)*Transceiver_Manufacturer*Rack_Unit[Ligent Photonics,5] +
(65.149377)*Transceiver_Manufacturer*Rack_Unit[NON-JNPR,5] +
(7905235134.592700)*Transceiver_Manufacturer*Rack_Unit[O-NET,5] +
(0.715413)*Transceiver_Manufacturer*Rack_Unit[Acceligh,6] +
(1086526005.542204)*Transceiver_Manufacturer*Rack_Unit[CENTERA,6] +
(127139657344.987488)*Transceiver_Manufacturer*Rack_Unit[Eoptolink,6] +