

# DOE Analysis: Doe Analysis Report

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DOE Analysis Report Design of Experiments Analysis Report Actual by Predicted Plot Model Formula  
ttemp = 65.087259 - (1.327588)\*Transceiver\_Manufacturer[Accelight] -  
(2.865921)\*Transceiver\_Manufacturer[CENTERA] +  
(995603574387.397095)\*Transceiver\_Manufacturer[Eoptolink] -  
(1.332634)\*Transceiver\_Manufacturer[Finisar] - (1.723358)\*Transceiver\_Manufacturer[Hisense] -  
(89043285333.383804)\*Transceiver\_Manufacturer[Intel Corp] -  
(225036984293.564178)\*Transceiver\_Manufacturer[Ligent Photonics] -  
(2.830278)\*Transceiver\_Manufacturer[NON-JNPR] - (2.493208)\*Transceiver\_Manufacturer[O-NET] -  
(0.091696)\*Rack\_Unit[2] - (0.318191)\*Rack\_Unit[3] - (0.559426)\*Rack\_Unit[4] -  
(0.407374)\*Rack\_Unit[5] - (1.245049)\*Rack\_Unit[6] - (0.803411)\*Rack\_Unit[7] -  
(0.726129)\*Rack\_Unit[8] - (25316310970.130054)\*Rack\_Unit[9] - (157393877450.204346)\*Rack\_Unit[10] -  
(12261302087.422798)\*Rack\_Unit[11] + (39108032648.411598)\*Rack\_Unit[12] -  
(42600213214.245415)\*Rack\_Unit[13] + (47542123827.449921)\*Rack\_Unit[14] - (2.589378)\*Rack\_Unit[15]  
+ (130600262483.733505)\*Rack\_Unit[16] - (2.391941)\*Rack\_Unit[17] -  
(22238103387.481415)\*Rack\_Unit[18] + (20518340874.508564)\*Rack\_Unit[19] +  
(11409702187.228548)\*Rack\_Unit[20] + (17027339264.842680)\*Rack\_Unit[21] -  
(52777871406.249481)\*Rack\_Unit[22] - (2.216127)\*Rack\_Unit[23] - (2.493841)\*Rack\_Unit[24] -  
(1.389647)\*Rack\_Unit[25] - (1.596277)\*Rack\_Unit[26] + (504671490217.245789)\*Rack\_Unit[27] +  
(0.608561)\*Rack\_Unit[28] - (745987231788.282471)\*Rack\_Unit[29] - (2.316164)\*Rack\_Unit[30] +  
(60773109731.877510)\*Rack\_Unit[31] - (29329808202.733528)\*Rack\_Unit[32] -  
(127028477285.293381)\*Rack\_Unit[33] + (23950865230.437737)\*Rack\_Unit[34] -  
(93451790716.674728)\*Rack\_Unit[36] - (153356658974.682312)\*Rack\_Unit[37] -  
(716438153396.390137)\*Rack\_Unit[38] - (23967478313.450790)\*Rack\_Unit[39] +  
(77478374274.818604)\*Rack\_Unit[40] + (177520992350.604248)\*Rack\_Unit[41] -  
(105277276692.061096)\*Rack\_Unit[42] + (0.309313)\*Fan\_Speed\_Range[L] +  
(0.055849)\*Transceiver\_Manufacturer\*Rack\_Unit[Accelight,2] +  
(6846997554.203388)\*Transceiver\_Manufacturer\*Rack\_Unit[CENTERA,2] +  
(239270543036.483032)\*Transceiver\_Manufacturer\*Rack\_Unit[Eoptolink,2] -  
(0.028531)\*Transceiver\_Manufacturer\*Rack\_Unit[Finisar,2] -  
(0.872444)\*Transceiver\_Manufacturer\*Rack\_Unit[Hisense,2] +  
(89043285332.997589)\*Transceiver\_Manufacturer\*Rack\_Unit[Intel Corp,2] +  
(2901564163.362468)\*Transceiver\_Manufacturer\*Rack\_Unit[Ligent Photonics,2] -  
(0.165506)\*Transceiver\_Manufacturer\*Rack\_Unit[NON-JNPR,2] -  
(95869263318.421158)\*Transceiver\_Manufacturer\*Rack\_Unit[O-NET,2] -  
(0.290958)\*Transceiver\_Manufacturer\*Rack\_Unit[Accelight,3] -  
(11126735750.694855)\*Transceiver\_Manufacturer\*Rack\_Unit[CENTERA,3] -  
(145314086253.012909)\*Transceiver\_Manufacturer\*Rack\_Unit[Eoptolink,3] +  
(0.182158)\*Transceiver\_Manufacturer\*Rack\_Unit[Finisar,3] -  
(0.038297)\*Transceiver\_Manufacturer\*Rack\_Unit[Hisense,3] +  
(89043285332.063049)\*Transceiver\_Manufacturer\*Rack\_Unit[Intel Corp,3] +  
(6625085651.482124)\*Transceiver\_Manufacturer\*Rack\_Unit[Ligent Photonics,3] -  
(7451281382.638187)\*Transceiver\_Manufacturer\*Rack\_Unit[NON-JNPR,3] -  
(9524141533.845520)\*Transceiver\_Manufacturer\*Rack\_Unit[O-NET,3] +  
(0.425156)\*Transceiver\_Manufacturer\*Rack\_Unit[Accelight,4] +  
(1435884028.645404)\*Transceiver\_Manufacturer\*Rack\_Unit[CENTERA,4] -  
(52100161012.456764)\*Transceiver\_Manufacturer\*Rack\_Unit[Eoptolink,4] +  
(0.404277)\*Transceiver\_Manufacturer\*Rack\_Unit[Finisar,4] +  
(0.195316)\*Transceiver\_Manufacturer\*Rack\_Unit[Hisense,4] +  
(89043285332.186386)\*Transceiver\_Manufacturer\*Rack\_Unit[Intel Corp,4] +  
(1385194016.126970)\*Transceiver\_Manufacturer\*Rack\_Unit[Ligent Photonics,4] +  
(76061351.454695)\*Transceiver\_Manufacturer\*Rack\_Unit[NON-JNPR,4] -  
(63144711640.945198)\*Transceiver\_Manufacturer\*Rack\_Unit[O-NET,4] -  
(0.043430)\*Transceiver\_Manufacturer\*Rack\_Unit[Accelight,5] -  
(1589881172.302978)\*Transceiver\_Manufacturer\*Rack\_Unit[CENTERA,5] -  
(70495997551.912918)\*Transceiver\_Manufacturer\*Rack\_Unit[Eoptolink,5] +  
(0.094667)\*Transceiver\_Manufacturer\*Rack\_Unit[Finisar,5] -  
(0.457838)\*Transceiver\_Manufacturer\*Rack\_Unit[Hisense,5] +  
(89043285332.175201)\*Transceiver\_Manufacturer\*Rack\_Unit[Intel Corp,5] +  
(459186830.209145)\*Transceiver\_Manufacturer\*Rack\_Unit[Ligent Photonics,5] +  
(65.149377)\*Transceiver\_Manufacturer\*Rack\_Unit[NON-JNPR,5] +  
(7905235134.592700)\*Transceiver\_Manufacturer\*Rack\_Unit[O-NET,5] +  
(0.715413)\*Transceiver\_Manufacturer\*Rack\_Unit[Accelight,6] +  
(1086526005.542204)\*Transceiver\_Manufacturer\*Rack\_Unit[CENTERA,6] +  
(127139657344.987488)\*Transceiver\_Manufacturer\*Rack\_Unit[Eoptolink,6] +