

```

1 //sevenSeg.do
2 //author: Jiakai Shi
3 //student #: 1003986760
4
5 vlib work
6
7 vlog -timescale 1ns/1ns sevenSeg.v
8
9 vsim sevenSeg
10
11 log {/*}
12
13 add wave {/*}
14
15 force {SW[0]} 0
16 force {SW[1]} 0
17 force {SW[2]} 1
18 force {SW[3]} 0
19 run 5ns
20
21 force {SW[0]} 0
22 force {SW[1]} 1
23 force {SW[2]} 1
24 force {SW[3]} 1
25 run 5ns
26
27 force {SW[0]} 0
28 force {SW[1]} 0
29 force {SW[2]} 0
30 force {SW[3]} 0
31 run 5ns
32
33 force {SW[0]} 0
34 force {SW[1]} 1
35 force {SW[2]} 0
36 force {SW[3]} 1
37 run 5ns
38
39 force {SW[0]} 0
40 force {SW[1]} 0
41 force {SW[2]} 0
42 force {SW[3]} 1
43 run 5ns
44
45 force {SW[0]} 1
46 force {SW[1]} 0
47 force {SW[2]} 0
48 force {SW[3]} 1
49 run 5ns

```