

Training ASMPT

Program session 2

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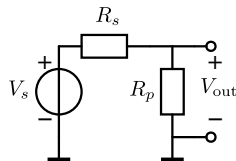
Percitec BV

April 19, 2021

- ① Q & A
- ② Poster session: noise
- ③ Examples: Budgeting of noise (hand calculations)
- ④ Examples: Budgeting and verification of noise (SLiCAP and LTspice)
- ⑤ Discussion ASMPT product specification
- ⑥ Homework:
 - ① Exercises similar as examples
 - ② Book chapter 2 and 7

Example 1

Noise Figure

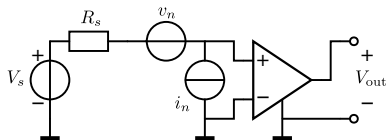


A signal (voltage) source with a source impedance R_s is terminated with a resistor with resistance R_p .

- 1 Give an expression for the noise figure for this arrangement.
- 2 Does such termination increase or decrease the total output voltage noise?
- 3 Verify your answer with SLiCAP.
- 4 Use LTspice for numerical verification with $R_s = 600\Omega$ and $R_p = 1k\Omega$.

Example 2

Noise Figure

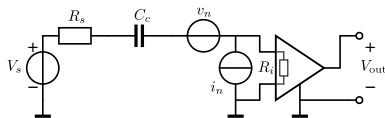


A signal (voltage) source with a source impedance R_s is connected to an amplifier. The noise performance of this amplifier is modeled with its input-referred noise v_n (voltage noise) and i_n (current noise) with spectral densities $S_{v_n} \left[\frac{\text{V}^2}{\text{Hz}} \right]$ and $S_{i_n} \left[\frac{\text{A}^2}{\text{Hz}} \right]$, respectively.

- 1 Give an expression for the noise figure for this arrangement.
- 2 Verify your answer with SLiCAP.
- 3 Use LTspice for numerical verification with $R_s = 600\Omega$, $\sqrt{S_{v_n}} = 2 \frac{\text{nV}}{\sqrt{\text{Hz}}}$ and $\sqrt{S_{i_n}} = 3 \frac{\text{pA}}{\sqrt{\text{Hz}}}$.

Example 3

Coupling capacitor

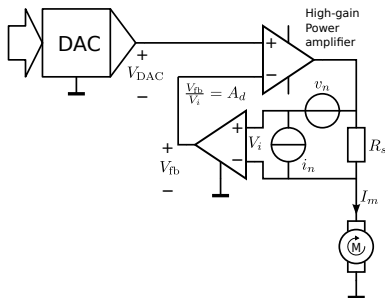


A coupling capacitor is used to connect a signal (voltage) source with a source impedance of 600Ω to an amplifier. The input resistance of the amplifier is $100\text{k}\Omega$. The noise performance of the amplifier is modeled with its input-referred noise v_n (voltage noise) and i_n (current noise) with spectral densities $\sqrt{S_{v_n}} = 2 \frac{\text{nV}}{\sqrt{\text{Hz}}}$ and $\sqrt{S_{i_n}} = 3 \frac{\text{pA}}{\sqrt{\text{Hz}}}$, respectively.

- 1 Propose a value for the coupling capacitance if this arrangement should have a low-noise transfer from 10Hz to 100kHz .

Example 4

Low-noise current driver



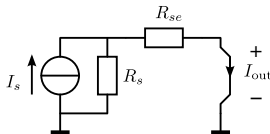
A current driver consists of a high-gain, low-noise, high-power voltage amplifier with output current feedback.

A current sense resistor with resistance R_s converts the output current into a voltage. This voltage is amplified by a differential voltage amplifier and compared with the setpoint provided by a DAC. The noise performance of the differential amplifier is modeled with its input-referred noise v_n (voltage noise) and i_n (current noise) with spectral densities $S_{v_n} \left[\frac{V^2}{Hz} \right]$ and $S_{i_n} \left[\frac{A^2}{Hz} \right]$, respectively.

- 1 Give an expression for motor current noise of this arrangement.
- 2 Verify your answer with SLiCAP.

Homework 1

Noise Figure

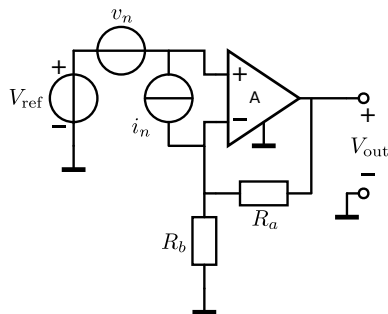


A resistor with resistance R_{se} is placed in series with a signal (current) source with a source impedance R_s .

- 1 Give an expression for the noise figure for this arrangement.
- 2 Does such termination increase or decrease the total output current noise?
- 3 Verify your answer with SLiCAP.
- 4 Use LTspice for numerical verification with $R_{se} = 600\Omega$ and $R_s = 1k\Omega$.

Homework 2

Low-noise supply

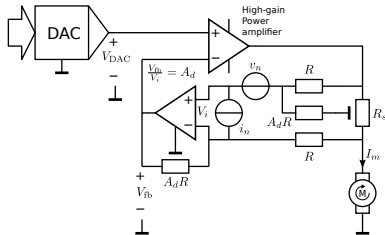


A signal (voltage) source with a source impedance R_s is connected to an amplifier. A low-noise regulator consists of a low noise voltage reference and a power amplifier. The output voltage can be adjusted with a resistive divider. The noise performance of the power amplifier is modeled with its input-referred noise v_n (voltage noise) and i_n (current noise) with spectral densities $S_{v_n} \left[\frac{V^2}{Hz} \right]$ and $S_{i_n} \left[\frac{A^2}{Hz} \right]$, respectively.

- 1 Give an expression for the output voltage noise of this arrangement.
- 2 Verify your answer with SLiCAP.
- 3 Propose a method to obtain a low output noise, independent of the values of the feedback resistors.

Homework 3

Low-noise current driver



A current driver consists of a high-gain, low-noise, high-power voltage amplifier with output current feedback.

A current sense resistor with resistance R_s converts the output current into a voltage. This voltage is amplified by a differential voltage amplifier and compared with the setpoint provided by a DAC. The noise performance of the differential amplifier is modeled with its input-referred noise v_n (voltage noise) and i_n (current noise) with spectral densities $S_{v_n} \left[\frac{V^2}{Hz} \right]$ and $S_{i_n} \left[\frac{A^2}{Hz} \right]$, respectively.

- 1 Give an expression for motor current noise of this arrangement.
- 2 Verify your answer with SLiCAP.
- 3 Propose a method to reduce the contribution of the resistors of the differential amplifier to the motor noise current.

Next Week

Preliminary program

- ① Q & A (Homework)
- ② Poster session: Modeling and characterization of the ideal behavior of amplifiers and design of negative feedback amplifier configurations.
- ③ Modeling and characterization of port isolation errors
- ④ Modeling and characterization of inaccuracy and nonlinearity
- ⑤ Poster session: Modeling and characterization of dynamic behavior / Estimation of poles and zeros
- ⑥ Modeling and characterization of operational amplifiers
- ⑦ Guided Exercise: Modeling of individual performance aspects of OpAmps