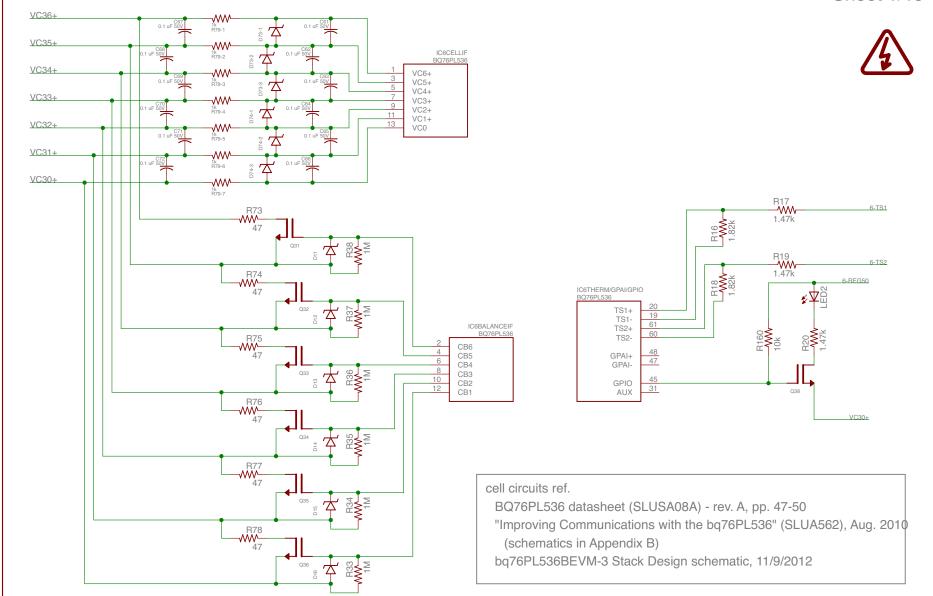
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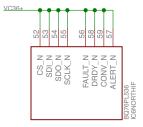


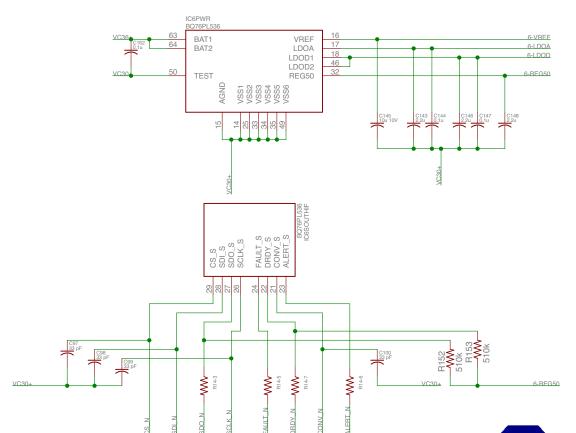
Device #6 cell interface, balancing, thermistors, GPIO RELEASE



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HSEL

CS_H
SDI_H
SDI_H
SDO_H
SCLK_H

FAULT_H
DRDY_H
CONV_H
ALERT_H
BG76PL536
IC6HOSTIF

43
42
41
42
41
40
39
37
36
38



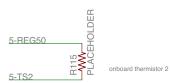
Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

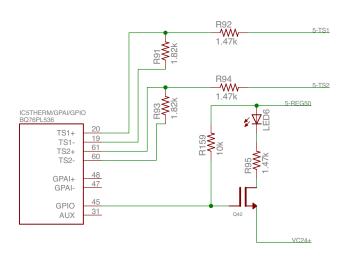
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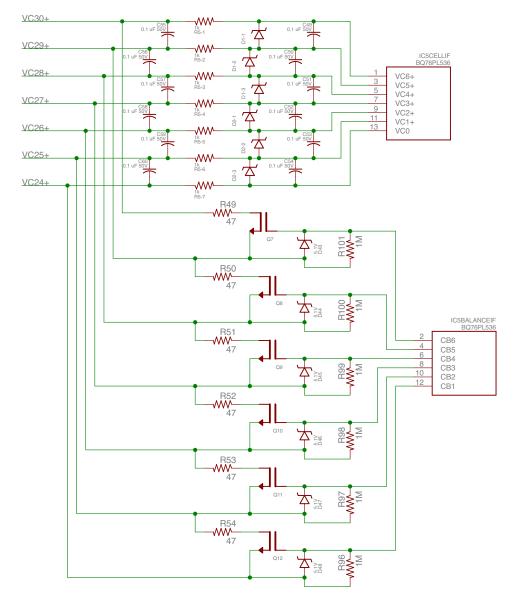
Device #6 stack comms + power RELEASE

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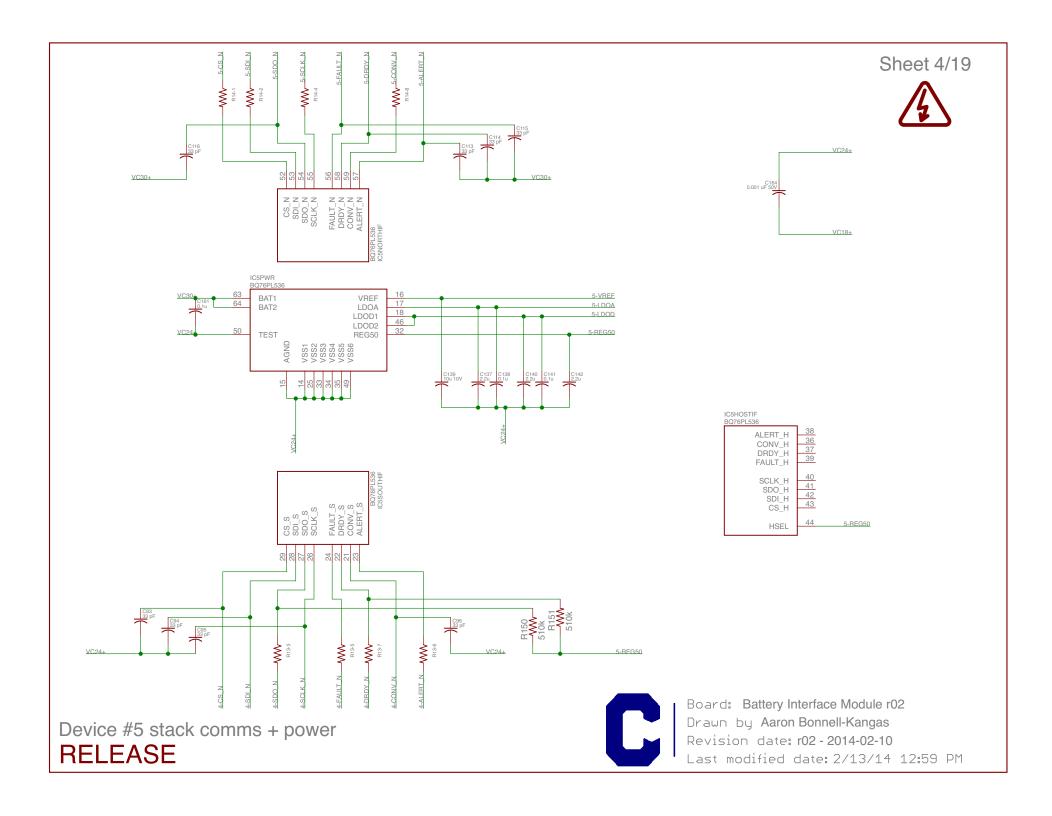






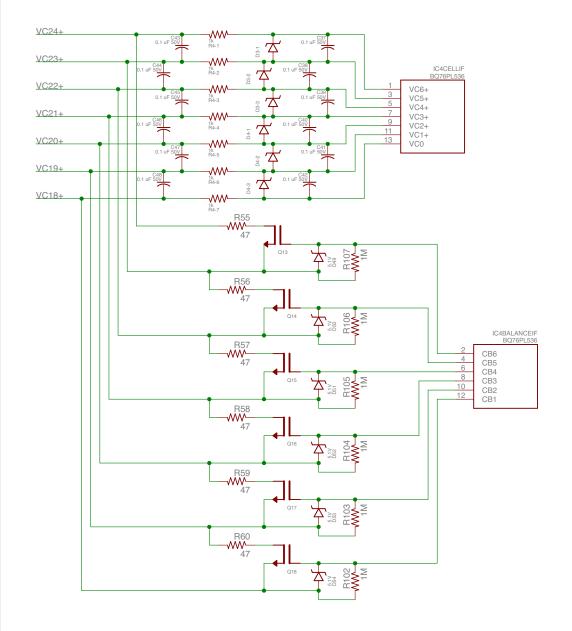
Device #5 cell interface, balancing, thermistors, GPIO RELEASE

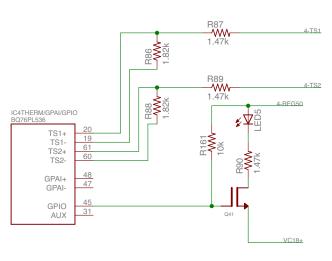




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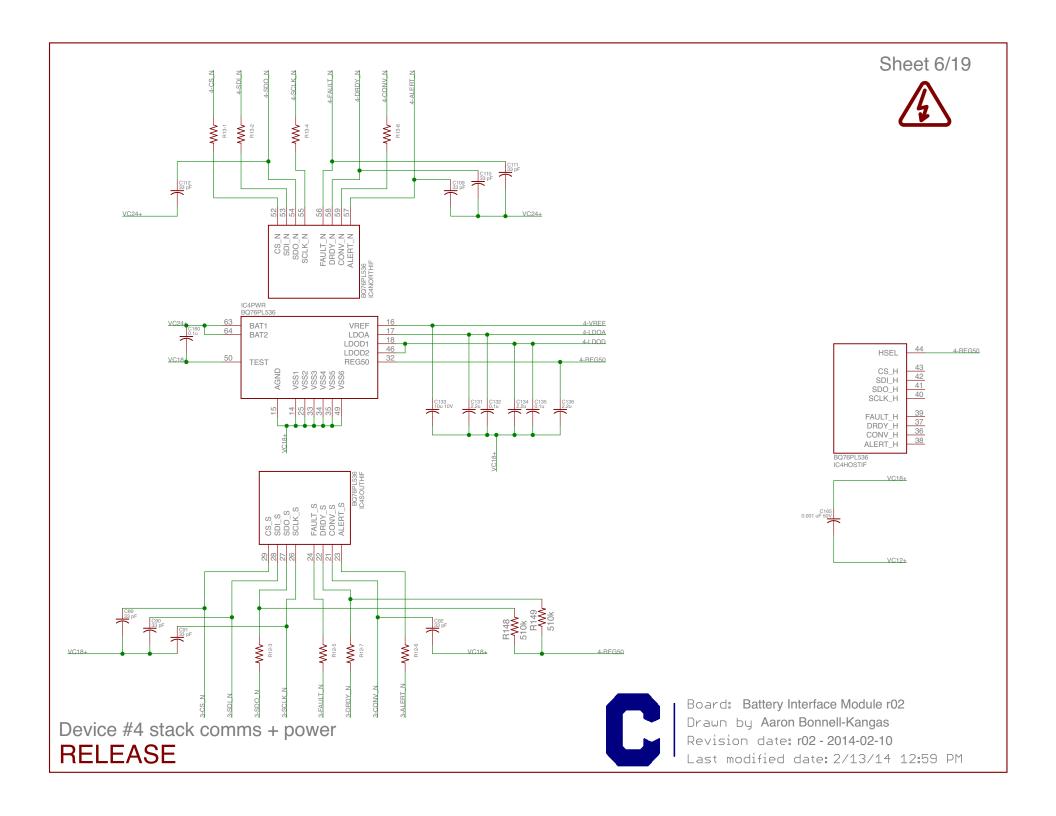






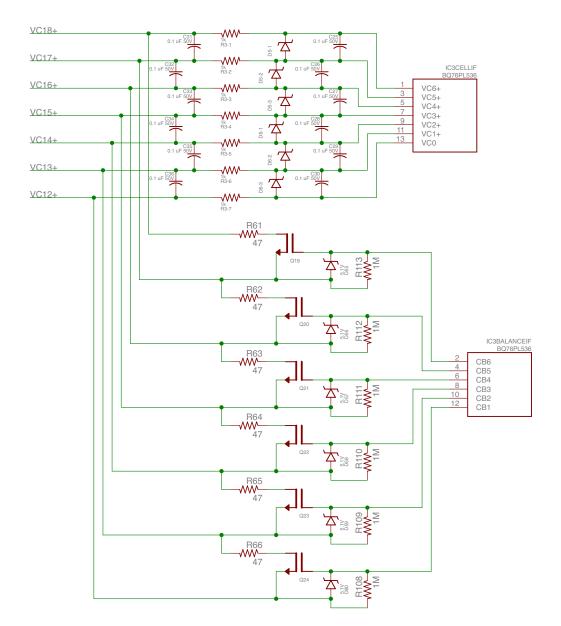
Device #4 cell interface, balancing, thermistors, GPIO RELEASE

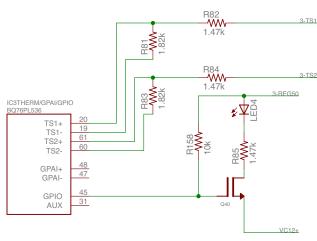




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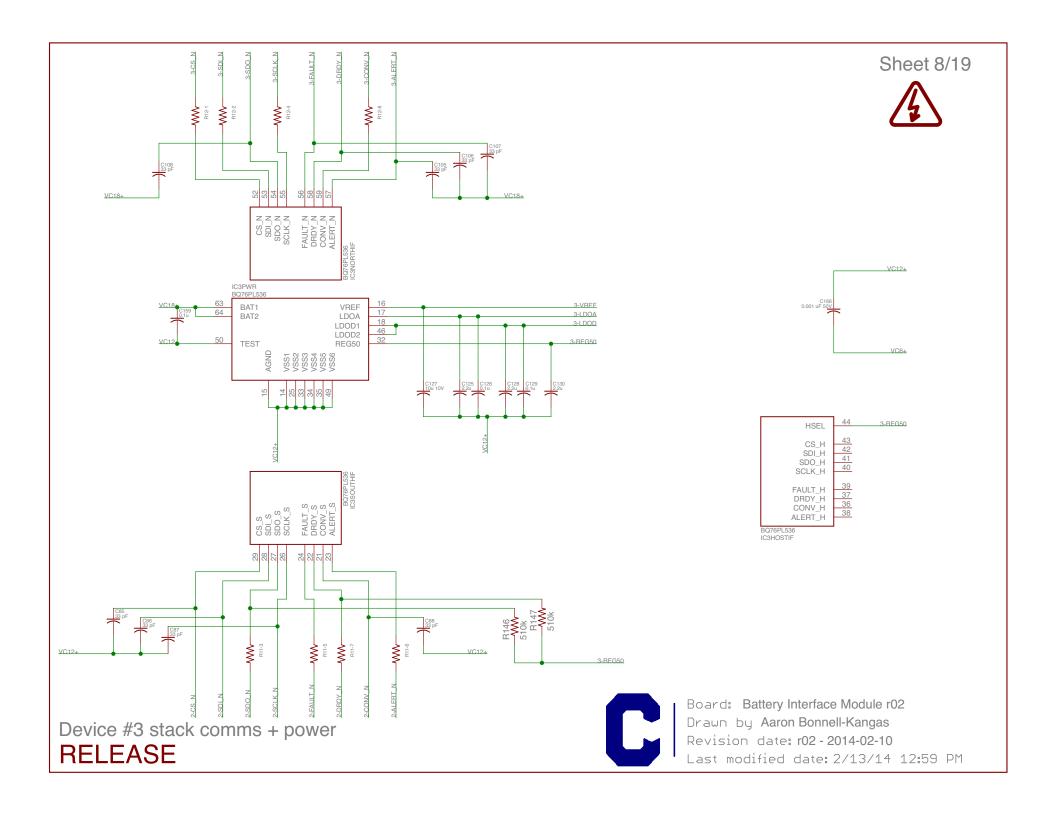






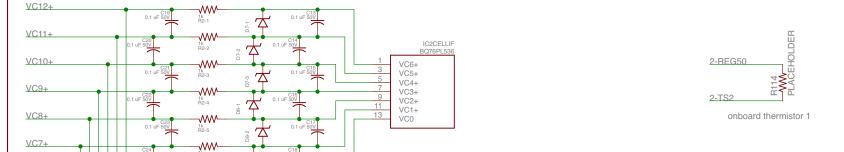
Device #3 cell interface, balancing, thermistors, GPIO RELEASE





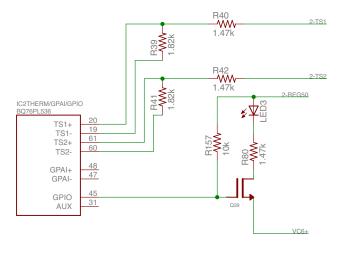
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IC2BALANCEIF BQ76PL536

CB5 CB4 CB3 CB2 CB1

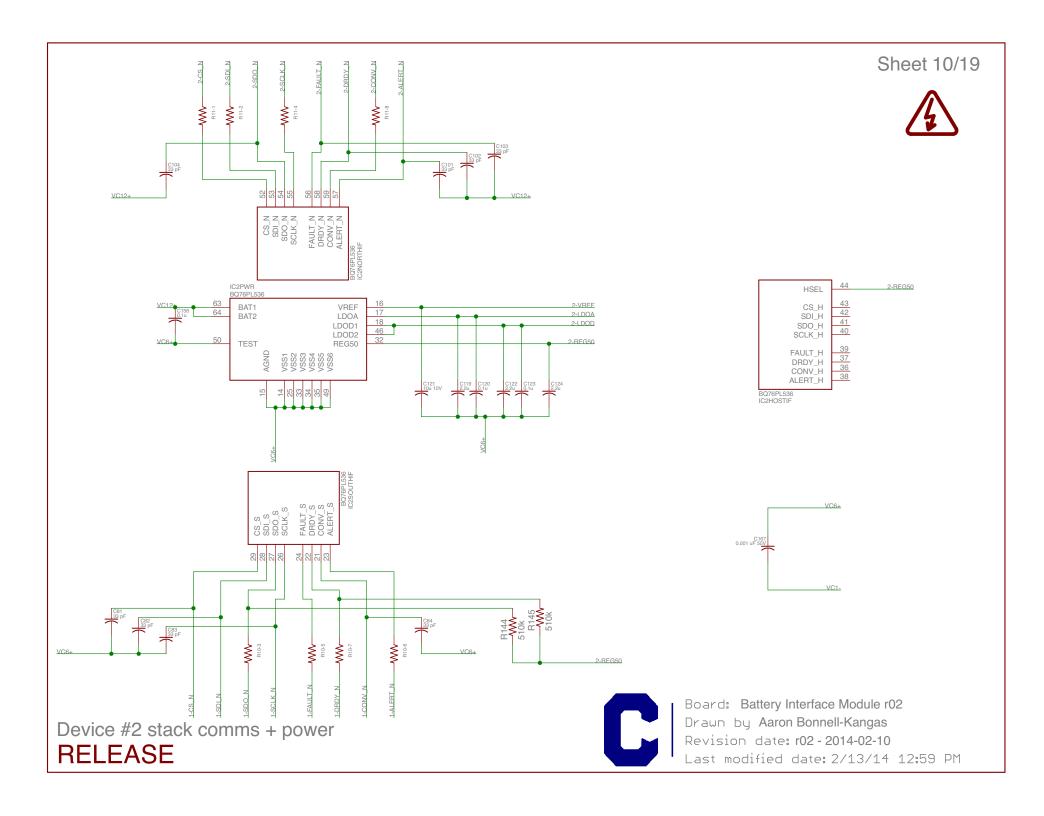


Device #2 cell interface, balancing, thermistors, GPIO RELEASE

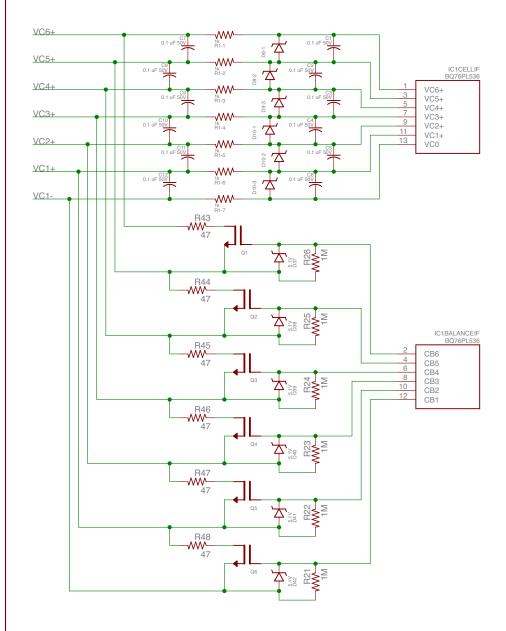
R71 ₩₩– 47

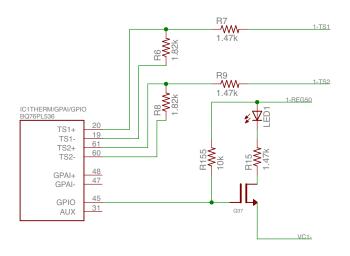
VC6+











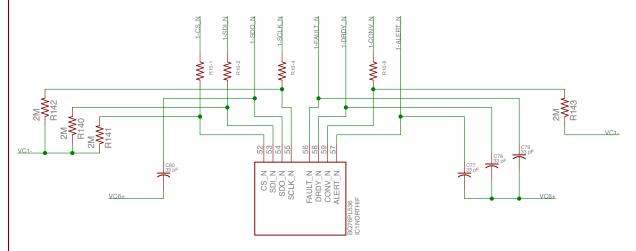
Device #1 cell interface, balancing, thermistors, GPIO RELEASE

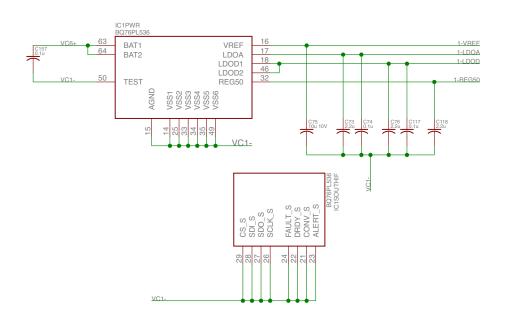


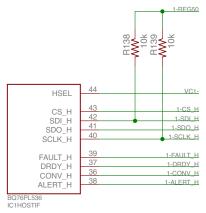
Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

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Device #1 stack comms + power

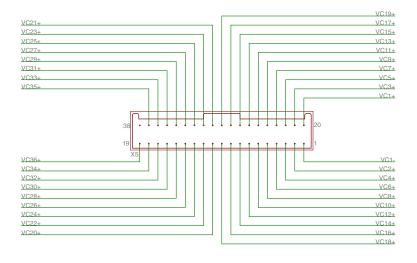
RELEASE



Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

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X5-CONN BOM-PART

X5-TERM BOM-PART

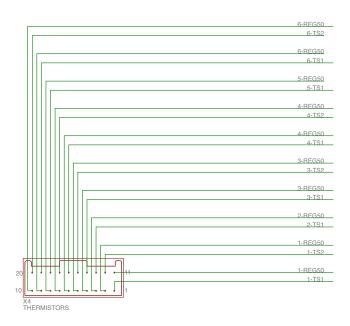
Cell connector RELEASE



Board: Battery Interface Module r02
Drawn by Aaron Bonnell-Kangas
Revision date: r02 - 2014-02-10

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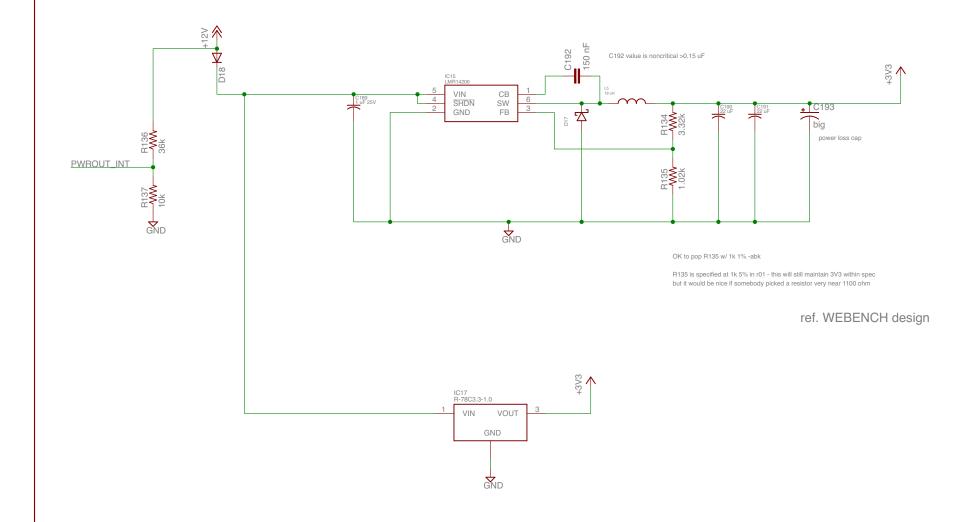




X4-CONN BOM-PART X4-TERM BOM-PART



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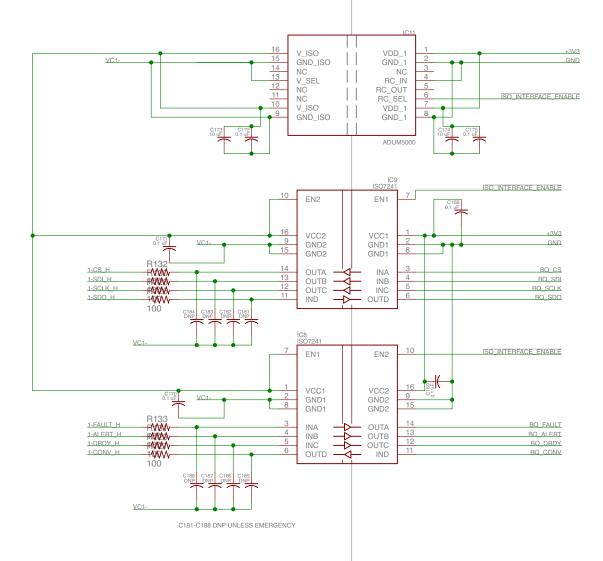
12V - 3V3 power **RELEASE**



Battery side Ho

Host side

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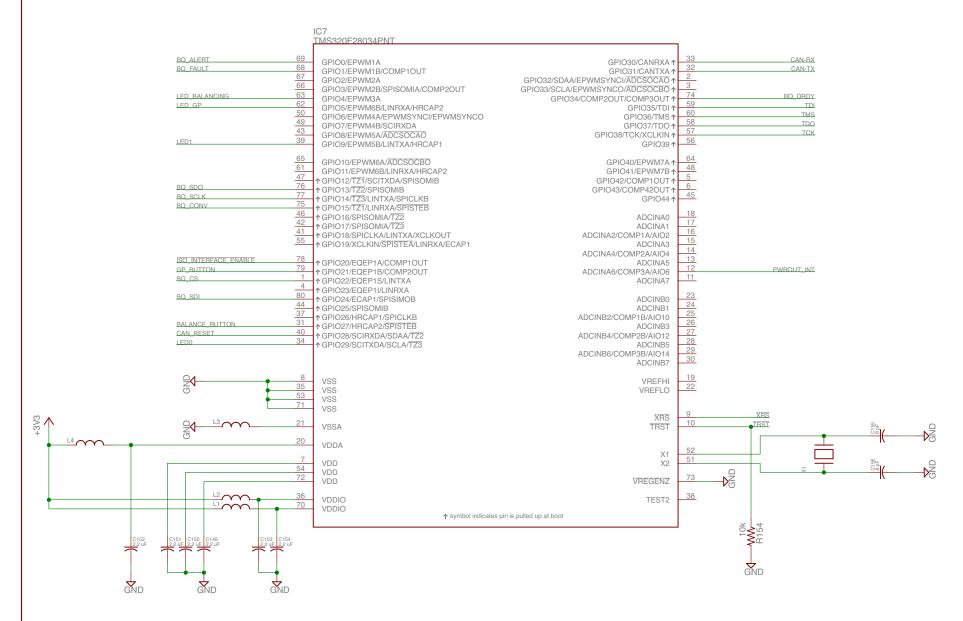
Isolated comms interface

RELEASE



Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

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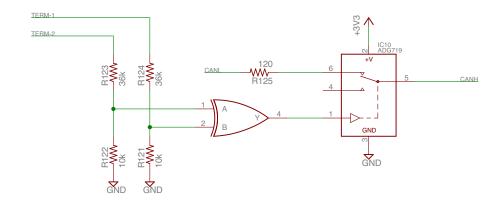
F28035 connections

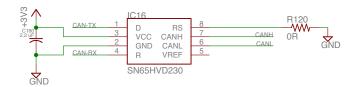
RELEASE

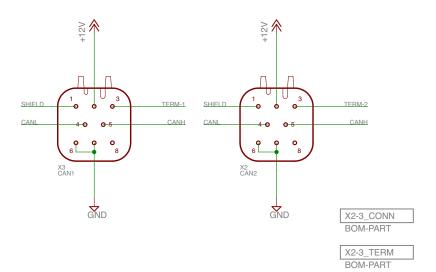


Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

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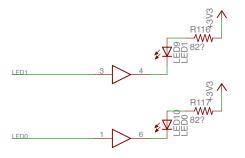
Board: Battery Interface Module r02 Drawn by Aaron Bonnell-Kangas Revision date: r02 - 2014-02-10

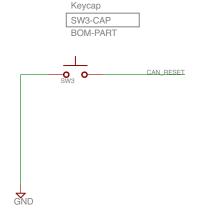
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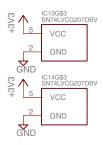
CAN interface and termination, JTAG

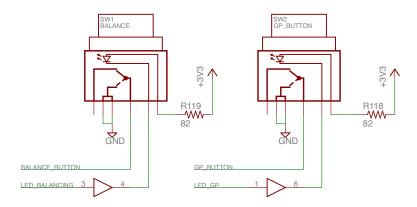
RELEASE

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Buttons and LEDs RELEASE



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