

PROFESSIONAL SUMMARY	Computer Engineer seeking a full time position in a fast-paced environment in which skills in digital logic design and verification can be applied. Qualified by logic design and development experience in previous work positions as well as a strong academic background.	
WORK EXPERIENCE	<p>Northrop Grumman Corporation - Baltimore, MD March 2018 - Present http://northropgrumman.com/ Electronics Engineer I Implemented the whirligig function in VHDL for an Electronically Steering Antenna. Used HDL Designer to aid in the implementation of state machines and used ModelSim to simulate and debug the behavior. Documentation was also written to describe the functionality of the whirligig component.</p> <p>Debugged a SPI bus implemented in VHDL through simulations in ModelSim and in hardware with the use of an oscilloscope. Required closely following documentation of certain components and protocols in order to find any timing violations or larger issues in functionality of the design.</p> <p>Ran fit tests for a board migration from UltraScale to UltraScale+. Required resource analysis after many place and routes in Xilinx Vivado.</p> <p>Xelic, Inc. - Pittsford, NY Summer 2017 http://xelic.com/ Product Development Engineering Intern Worked on the verification for multiple functions of an existing networking IP core, written in VHDL. Collaborated on the design of blocks for a networking IP core in Verilog HDL, relating to Optical Transport Network (OTN)/packet processing.</p> <p>Critical Link LLC - Syracuse, NY Summer-Fall 2016 http://criticallink.com/ Engineering Intern Aided in the research for application of Hall Effect sensors for proximity sensing. Research includes possible concerns and risks of the application, and design, implementation, and analysis of test circuits. Strict deadlines were assigned for different stages of the project.</p> <p>Aided in the development of the Android application for a bacteria-scanning device. Mainly focused on the development of the user interface and analysis of data readings. Specific customer specifications were given for the project.</p> <p>Parsons Government Services - Centreville, VA Summer 2015 http://parsons.com/ Personal Computer Support Tech Intern Worked on Java back end development in an Eclipse environment with a focus on fixing existing issues in a networking security application. Collaborated on the documentation of the installation of a service for the project on a clean virtual machine running Ubuntu. Tested and verified different components of the application and submitted defect tickets through JIRA. Collaborated in code reviews using Review Board.</p>	
EDUCATION	<p>Rochester Institute of Technology - Rochester, NY Graduation: December 2017 Major: Computer Engineering (BS) GPA: 3.47 Minor: Mathematics</p>	
TECHNICAL SKILLS & CERTIFICATIONS	<p>Software VHDL, Verilog, L^AT_EX, Java, C</p> <p>Hardware FPGAs, Microcontrollers, Digital Circuit Design</p> <p>Tools Xilinx- Vivado & ISE, HDL Designer, ModelSim, Git</p>	
PROJECTS	<p>http://github.com/VictoriaWeaver/</p> <p>Smart Security System: Senior Design Team Project- An electronic door lock system with facial recognition capabilities and Android mobile application for system management and remote operation. Primary lead on development of Android mobile application in Java with Github used for version control.</p> <p>Sobel Image Filter: Designed, synthesized, and verified a Sobel edge-detection IP core in VHDL, capable of converting grayscale images into black and white images.</p> <p>MIPS-VHDL: Implemented a portion of the MIPS assembly instruction set architecture in VHDL.</p> <p>LED Table: Constructed a programmable matrix of LEDs in a custom wooden case with a diffused Plexiglass cover to make various patterns and animations.</p>	