Victoria Weaver

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Objective

To obtain a cooperative education position in the field of Computer Engineering for the Summer of 2017.

Work Experience

Critical Link LLC - Syracuse, NY — May 2016 - Present
Engineering Intern — http://criticallink.com/
Currently aiding in the development of the Android Application for a bacteria scanning device. Android Studio is used to develop the application for Android 4.2. Components of the application contributed to include analysis of the test results of scanning the bacteria through the use of plots as well as the grouping of locations to be tested in order to improve organization and increase efficiency of testing.

Parsons Government Services - Centreville, VA — Summer 2015
Personal Computer Support Tech Intern — http://parsons.com/
Worked on Java back end development in an Eclipse environment with a focus on fixing existing issues in a networking security application. Collaborated on the documentation of the installation of a service for the project on a clean virtual machine running Ubuntu. Tested and verified different components of the application and submitted defect tickets through JIRA. Collaborated in code reviews using Review Board.

Rochester Institute of Technology - Rochester, NY — Fall 2014
Student Lab Instructor — http://rit.edu/
Lab instructor, individual tutor, and grader of assignments for students enrolled in the introductory computer science course sequence.

Rochester Institute of Technology - Rochester, NY — Summer 2014
Student Developer — http://rit.edu/
Worked on the development of a keyboard and screen reader compatible "drag and drop" programming environment based off of the Massachusetts Institute of Technology's "Scratch" program. Aided in the development of Inclusive Exploring Computer Science curriculum for visually impaired students.

Education

Rochester Institute of Technology - Rochester, NY — August 2013 - Present
Major: Computer Engineering, Minor: Mathematics — GPA: 3.337
Graduation: May 2016

Technical Skills & Certifications

Software VHDL, LaTeX, Java, C, ARM and MIPS Assembly, Python
Hardware Soldering, Power Supplies, Multimeters, Oscilloscopes
Tools Git, Xilinx, ModelSim, Android Studio, Altera Quartus II
Certifications Electrostatic Discharge Certification, 2016

Personal Projects

MIPS-VHDL: Implemented a portion of the MIPS assembly instruction set architecture in VHDL.

Infrared Proximity Sensor: Employed the use of infrared LEDs for proximity sensing in conjunction with seven segment displays and a buzzer for visual and auditory feedback.

LED Table: Constructed a programmable matrix of LEDs in a custom wooden case with a diffused Plexiglas cover to make various patterns and animations.

Encryption Algorithm Speeds: Implemented RSA and a symmetric key algorithm in Java to compare the execution time of the algorithms.

Interests & Activities

Computer Science House (CSH) Member: A Special Interest House at RIT that provides a unique living and learning environment with access to facilities and other resources to promote hands-on learning while still maintaining a social atmosphere.

CSH Director of Evaluations (Spring 2016): Responsible for membership evaluations, requirements, and recruitment as well as serving as a liaison between CSH and RIT Residence Life.