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Reset, oscillators, JTAG
I2C-E2PROM, SPI-Flash

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SODIMM edge connector interface

UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic.
However, always check jumper positions on actual boards
since there is no guarantee that all jumpers are in default place.

Rev 1.4

Added U20 (alternative pad). Added R100-R105.
Added fix for LPC3250 DDR.2 errata.

Rev 1.3

Added U19 (74LVC1G125) and C114 (100nF), and
changed R17 to 33K (from UL), see schematic page 2.

Rev 1.2

Added R95, R96 pulldowns. Clarified U10 part number.
Added R97-R99, C112, C113 to prepare for LPC3250
DDR errata, but not needed. R17 not mounted.

Rev 1.1

Changed control pins for LED3-5 to P2.10-12.
Added voltage test points.

Rev 1.0

Original revision



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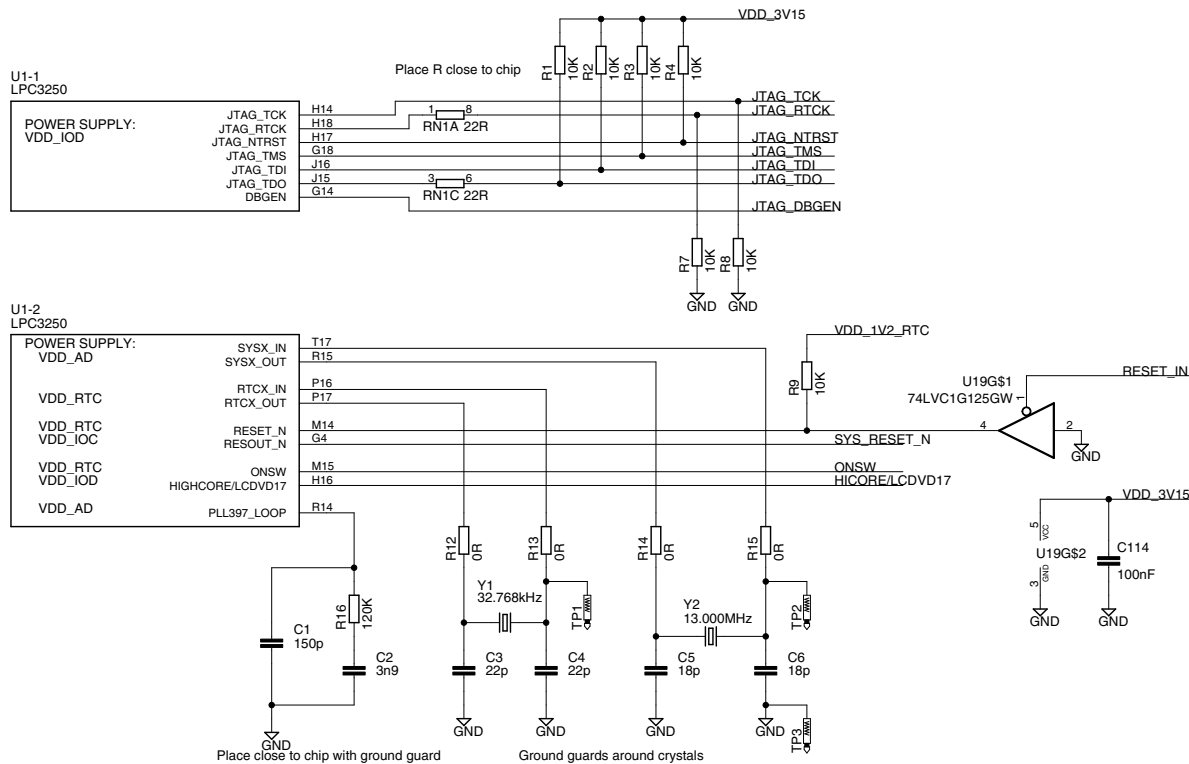
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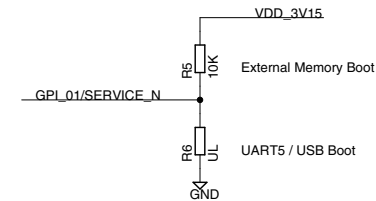
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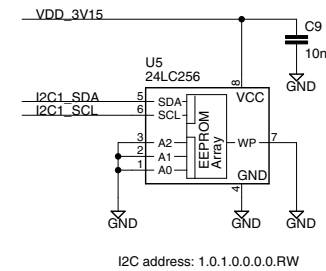
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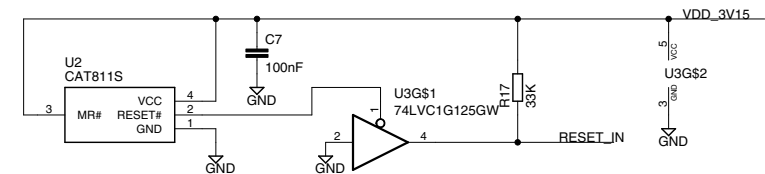
Boot Source Selection



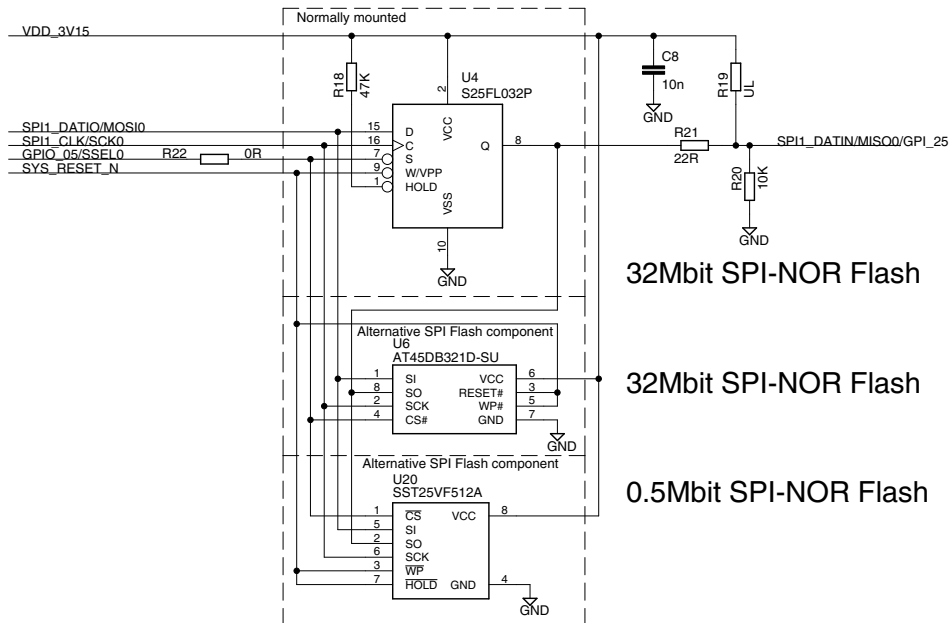
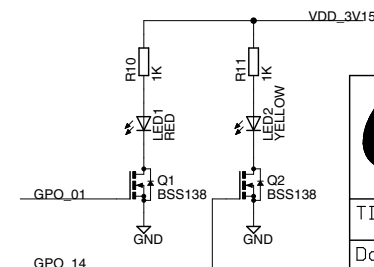
256kbit EEPROM



Reset generation



LEDs



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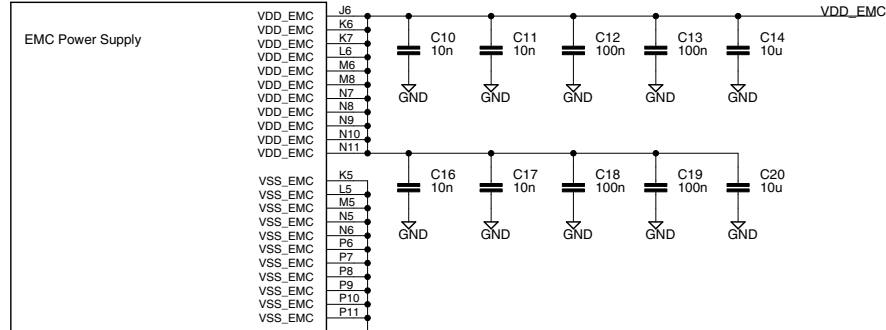
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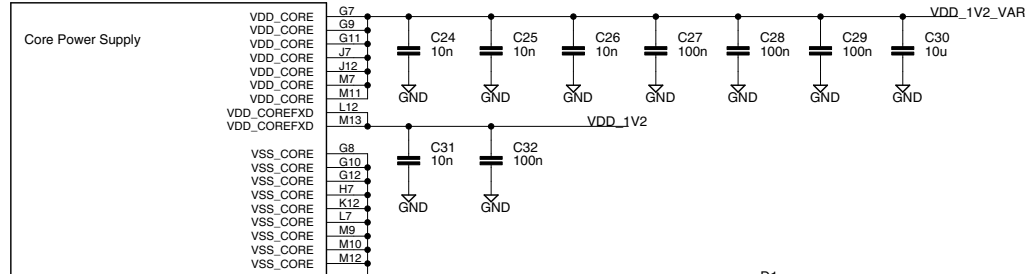
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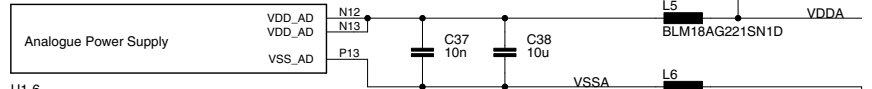
U1-3
LPC3250



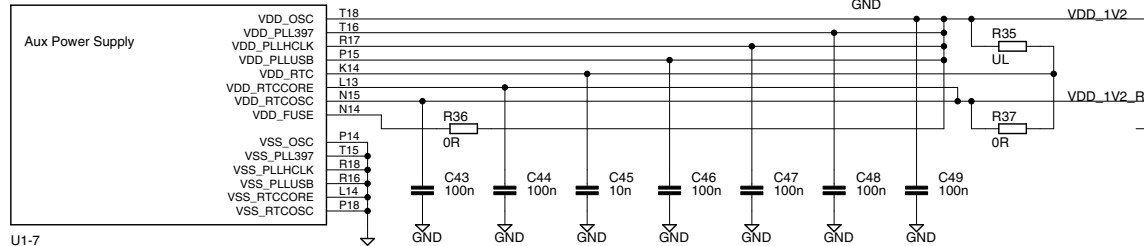
U1-4
LPC3250



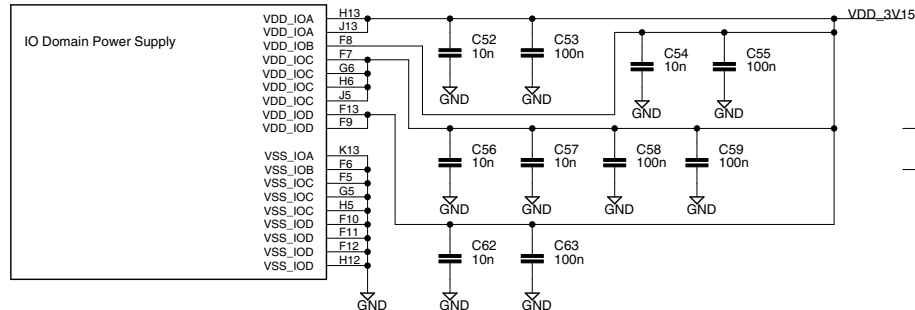
U1-5
LPC3250



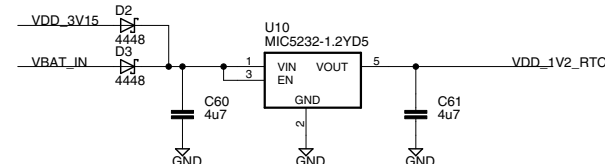
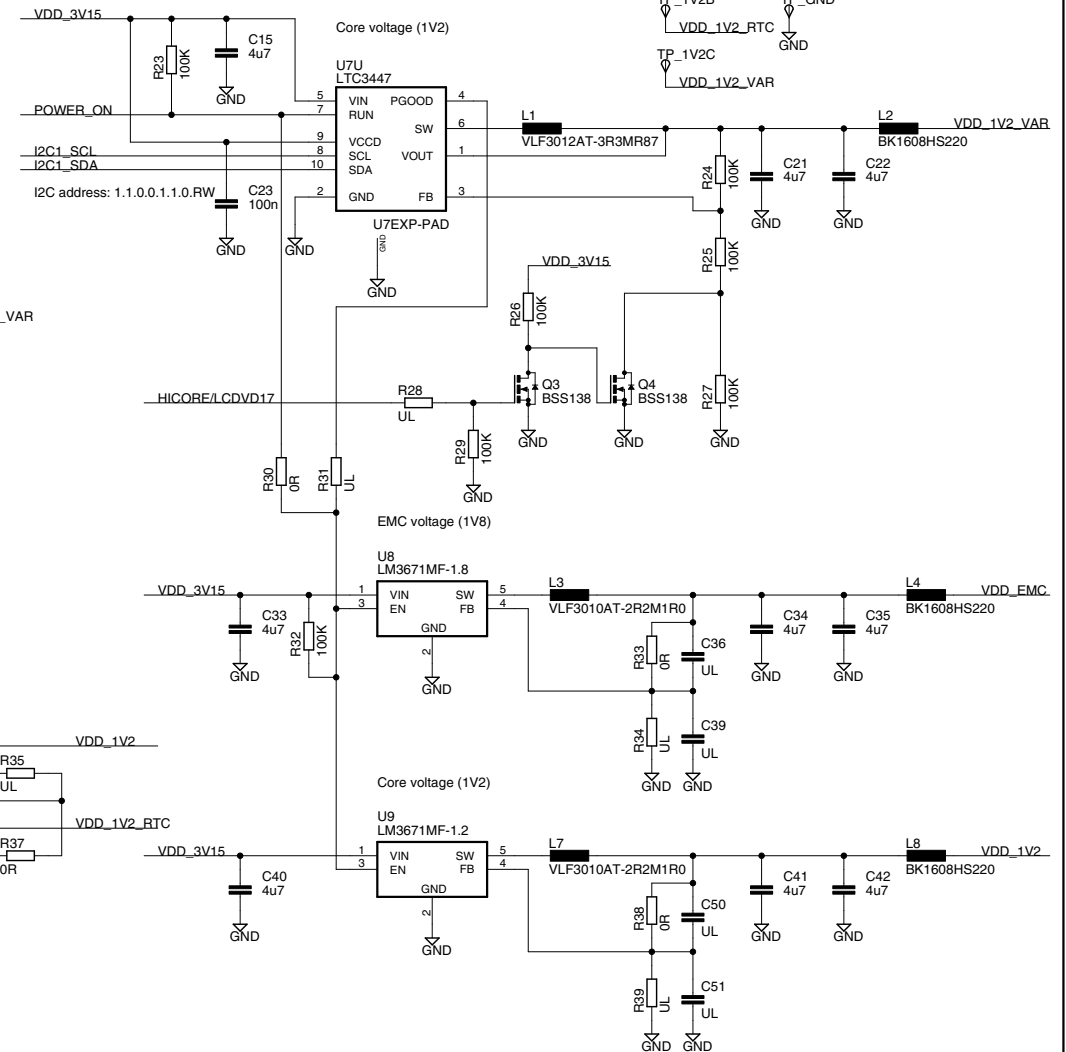
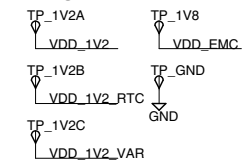
U1-6
LPC3250



U1-7
LPC3250



Voltage Test Points



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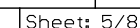
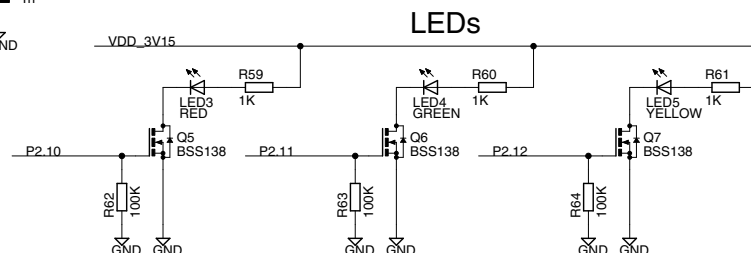
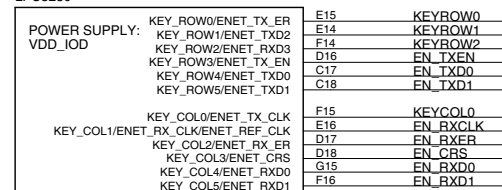
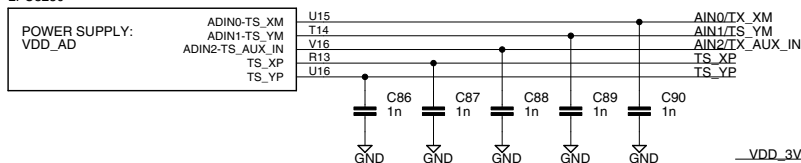
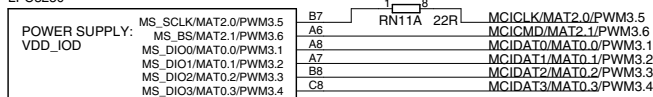
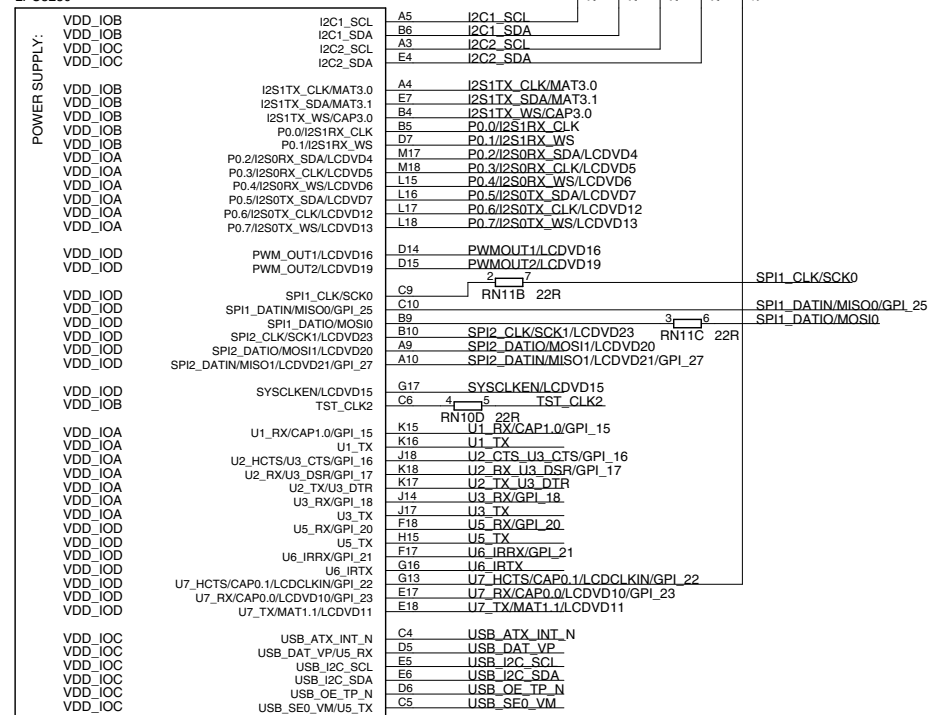
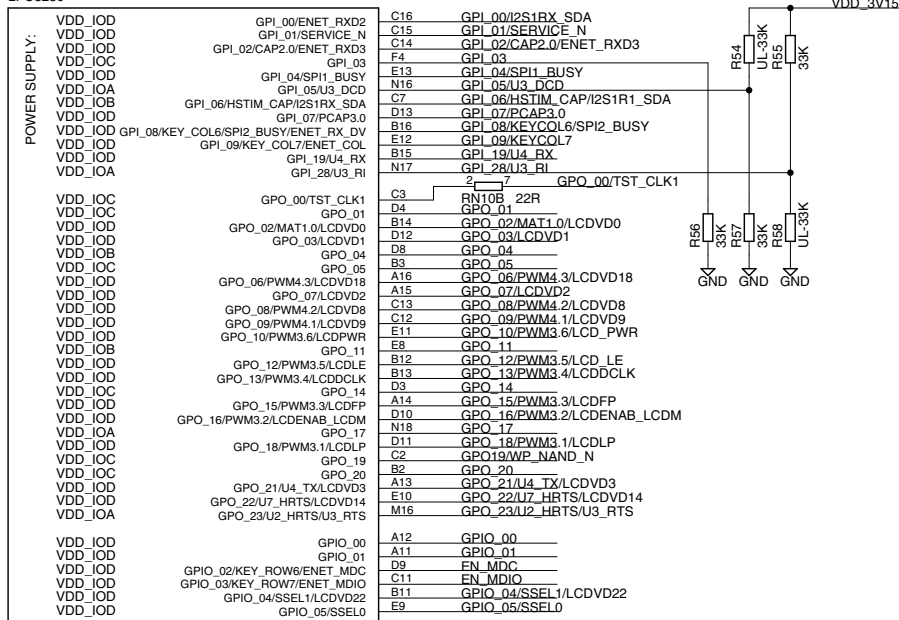
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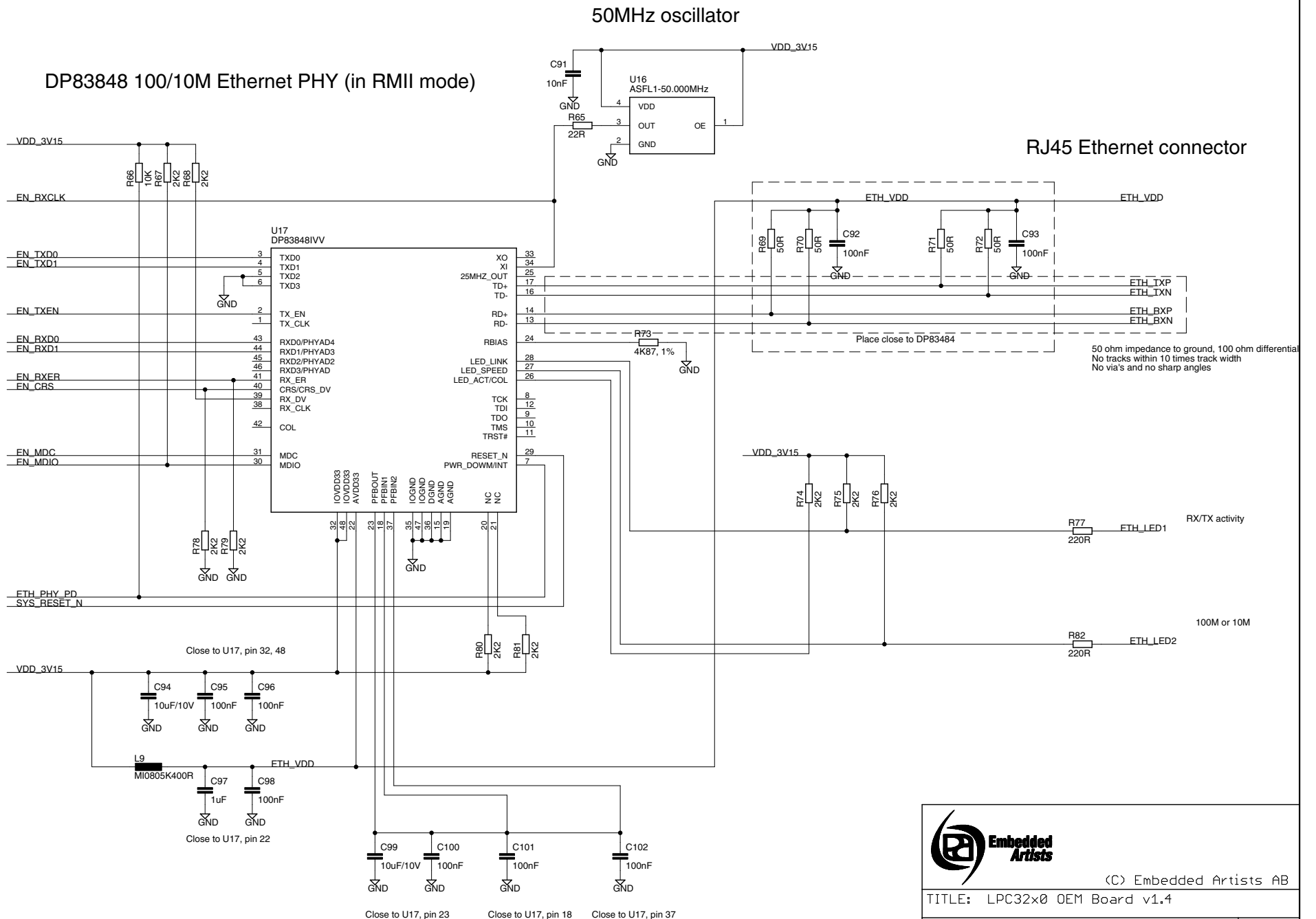
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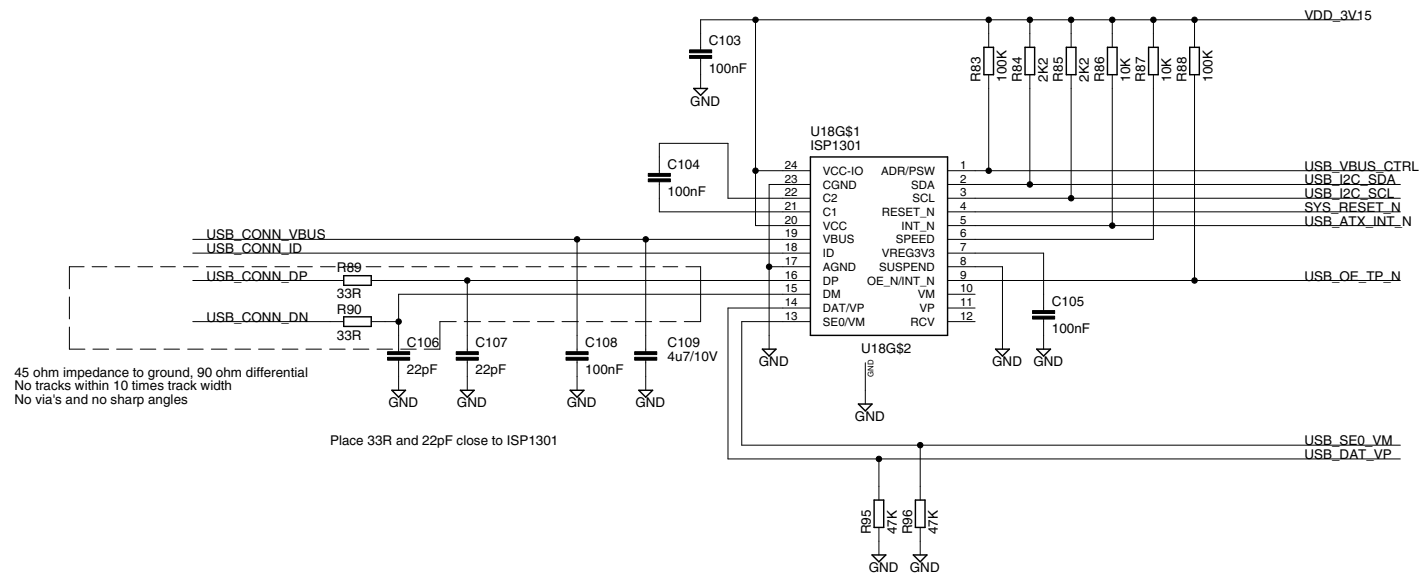
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OTG USB Channel



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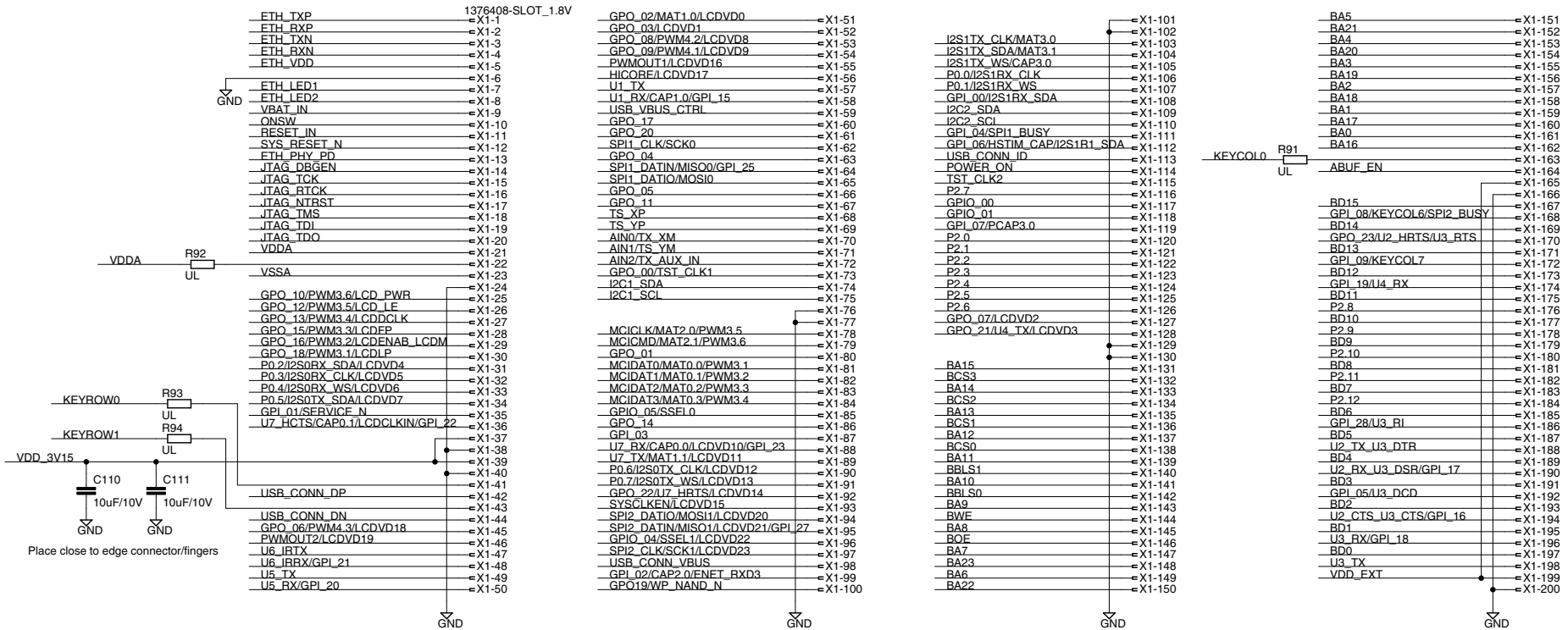
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SODIMM-200 Edge Connector



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