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SODIMM edge connector interface

UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic.
However, always check jumper positions on actual boards
since there is no guarantee that all jumpers are in default place.

Rev 1.2

Added R95, R96 pulldowns. Clarified U10 part number.
Added R97-R99, C112, C113 to prepare for LPC3250
DDR errata, but not needed. R17 not mounted.

Rev 1.1

Changed control pins for LED3-5 to P2.10-12.
Added voltage test points.

Rev 1.0

Original revision



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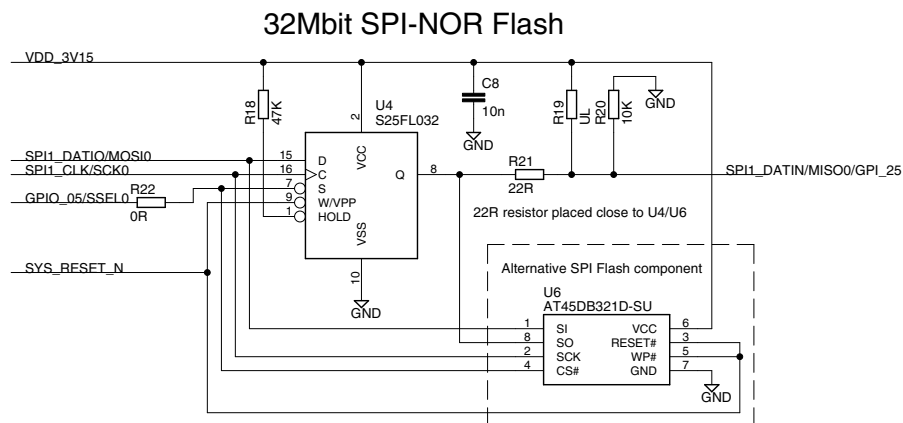
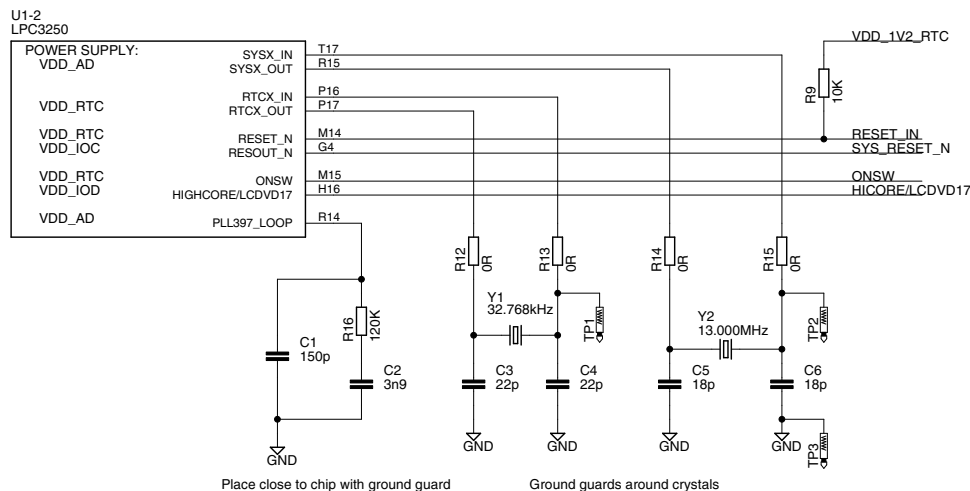
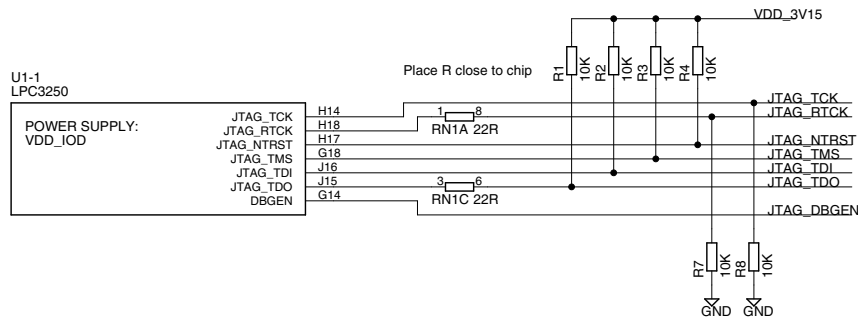
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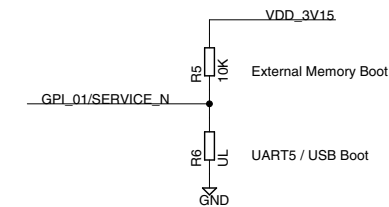
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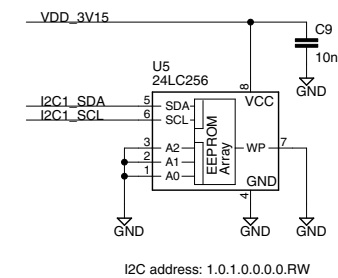
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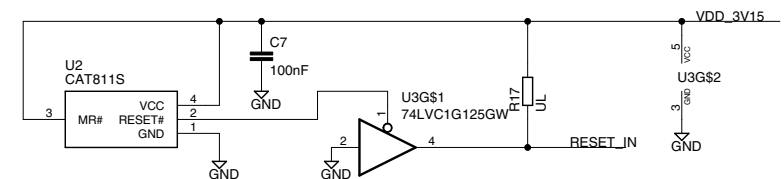
Boot Source Selection



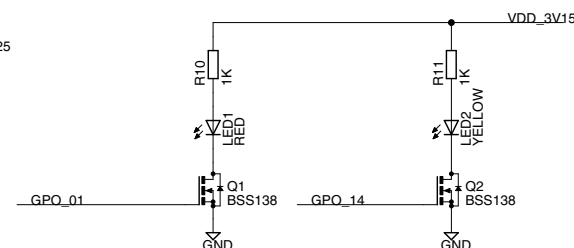
256kbit EEPROM



Reset generation



LEDs



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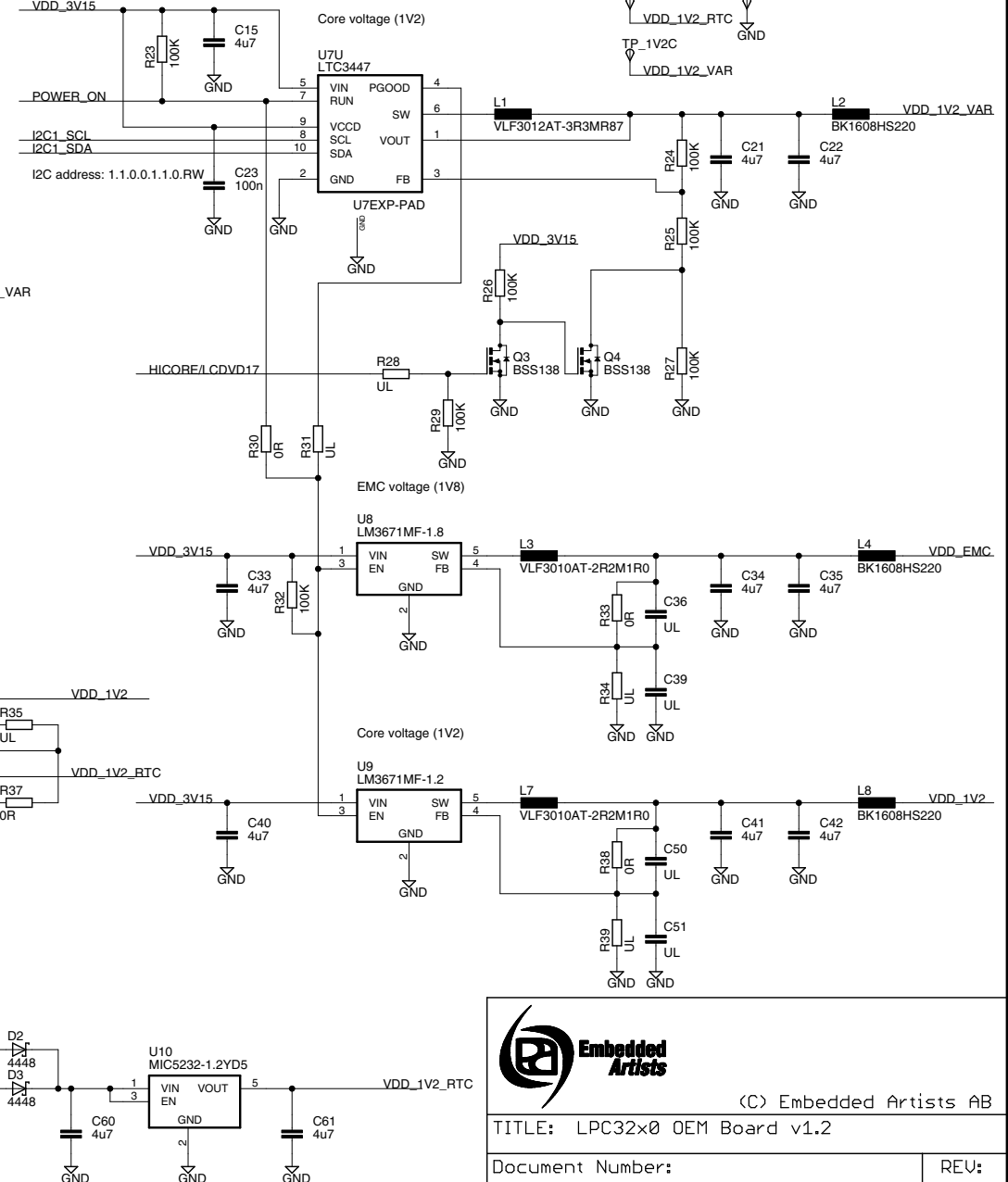
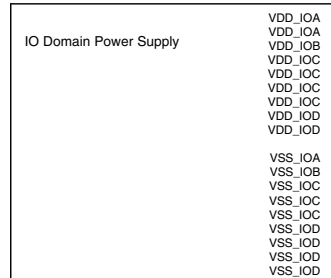
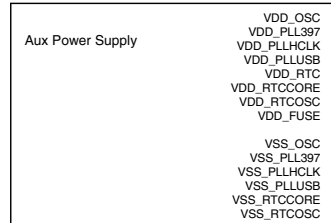
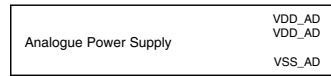
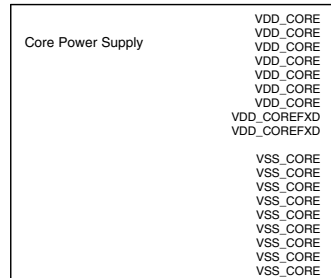
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EMC Power Supply	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VDD_EMC
	VSS_EMC
	VSS_EMC
	VSS_EMC
	VSS_EMC
	VSS_EMC



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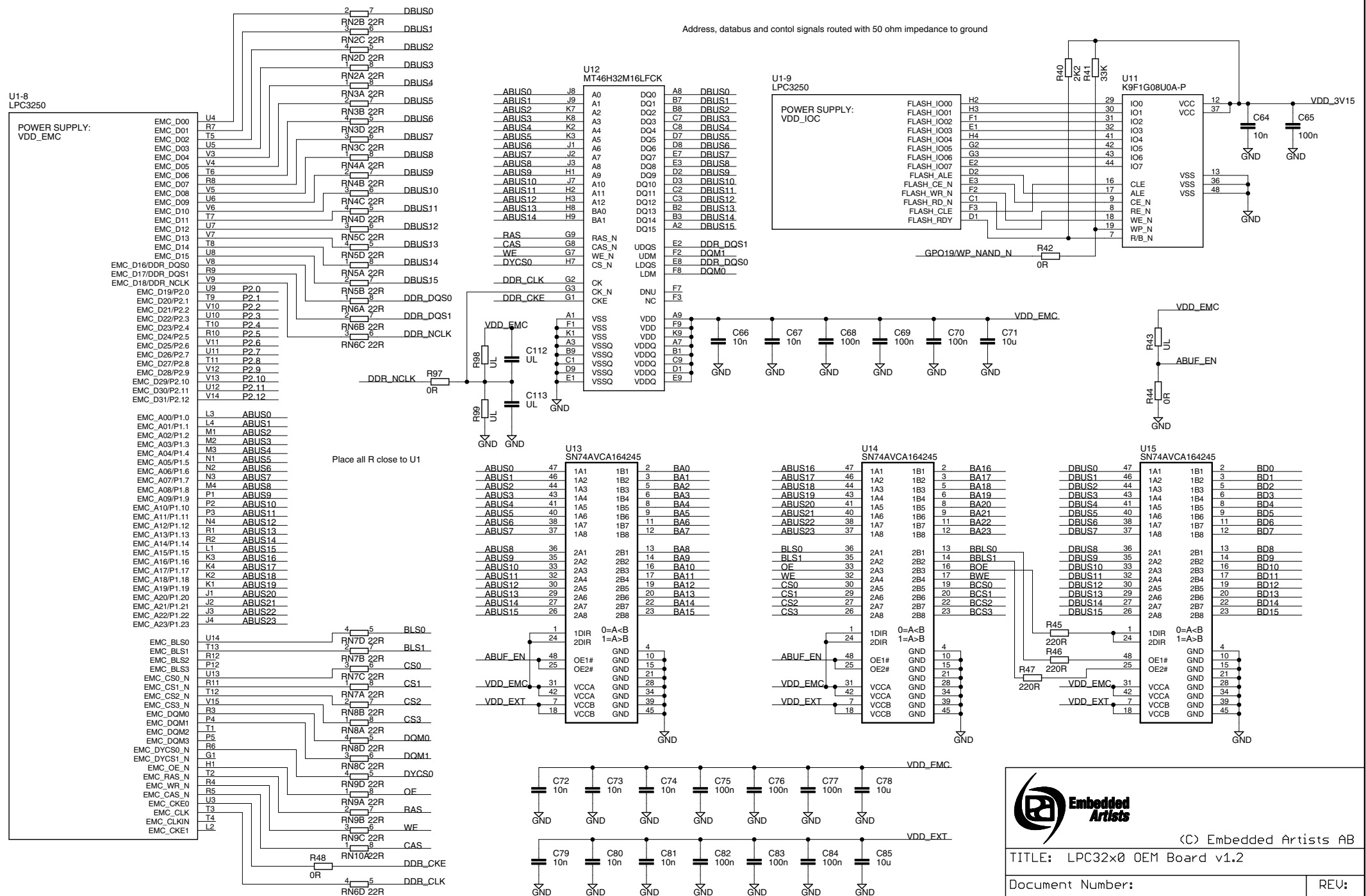
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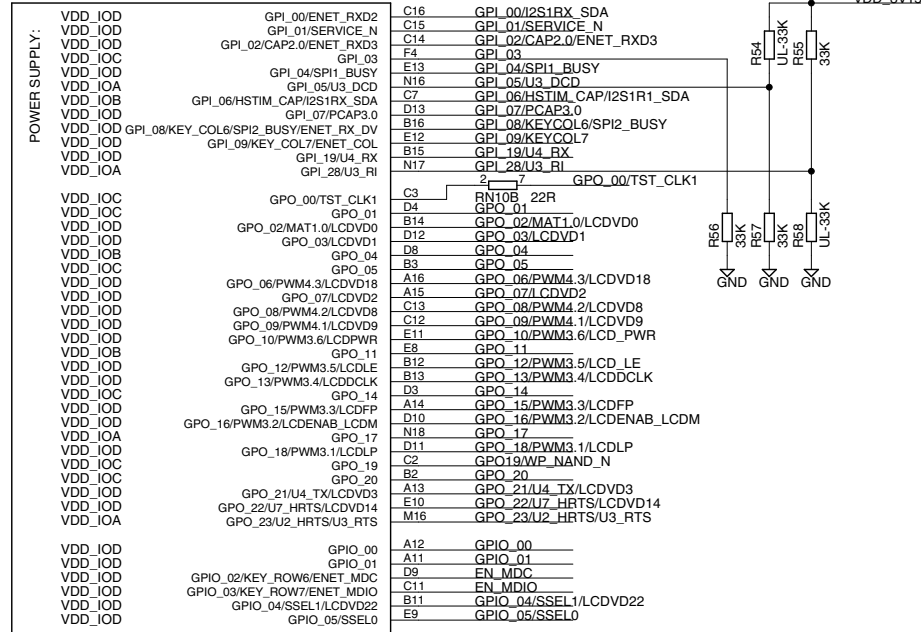
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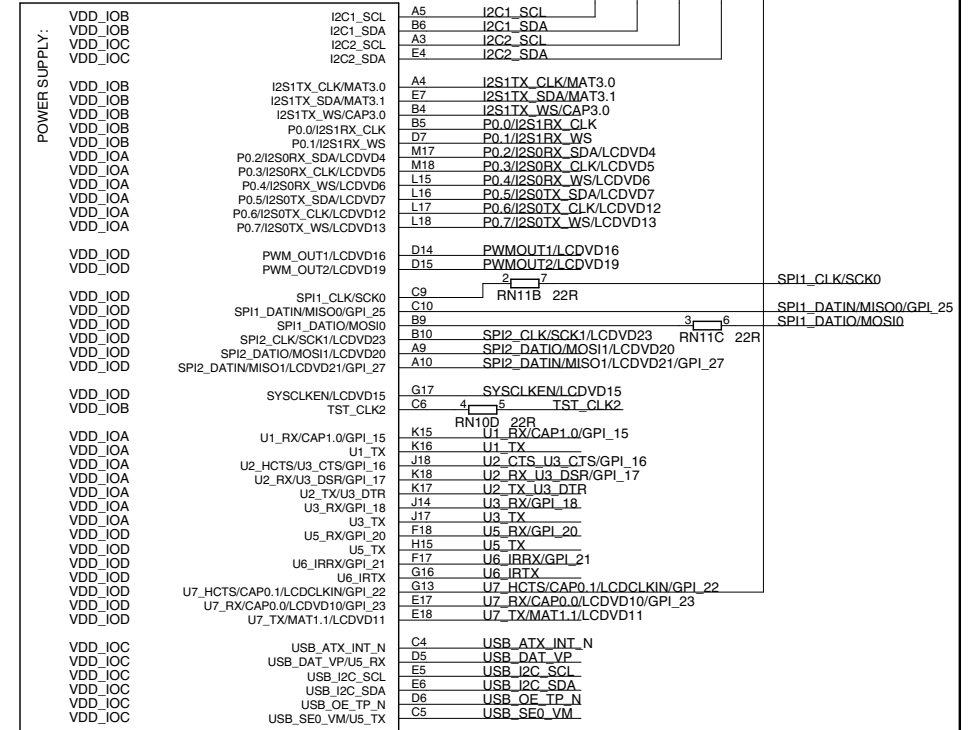
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U1-10
LPC3250



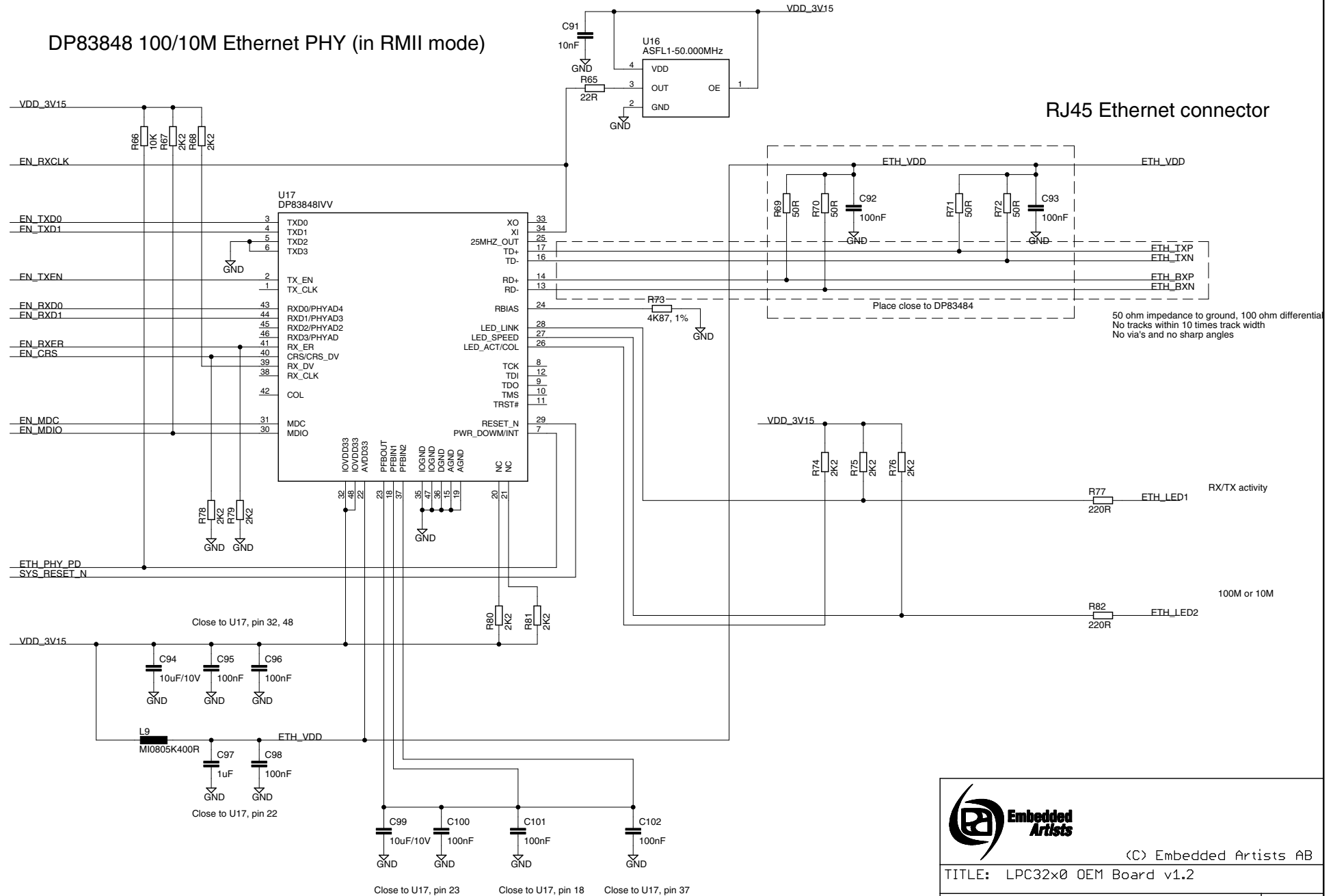
U1-11
LPC3250



DP83848 100/10M Ethernet PHY (in RMI mode)

50MHz oscillator

RJ45 Ethernet connector



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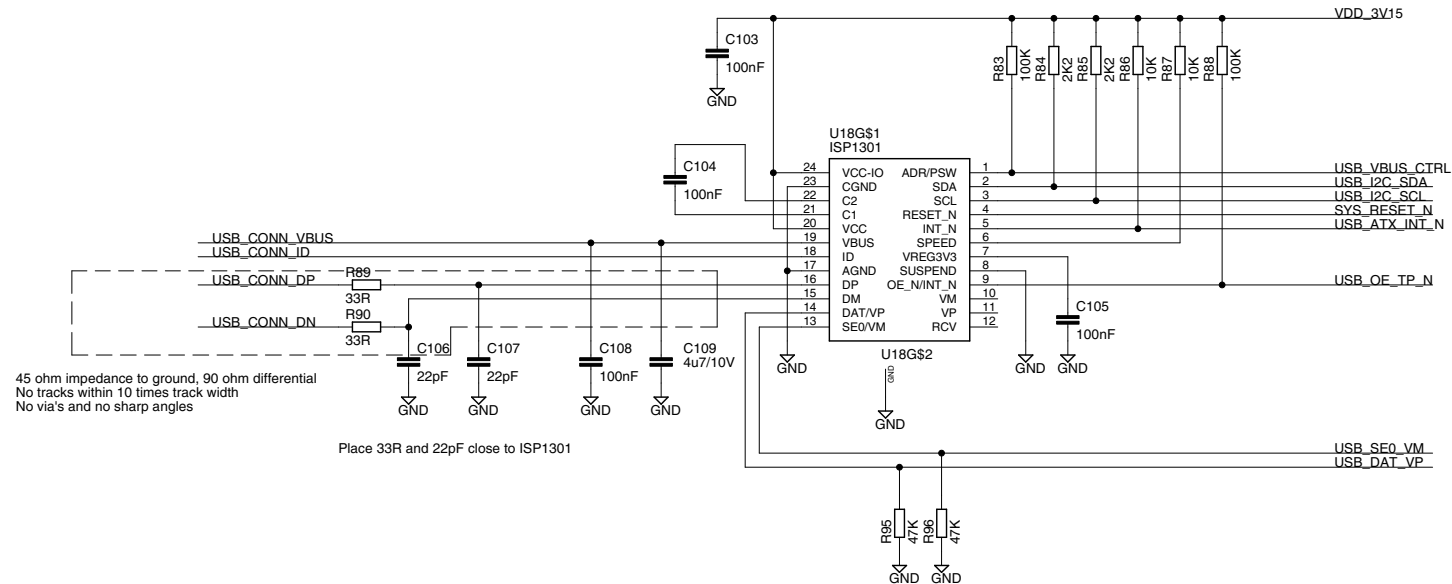
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OTG USB Channel



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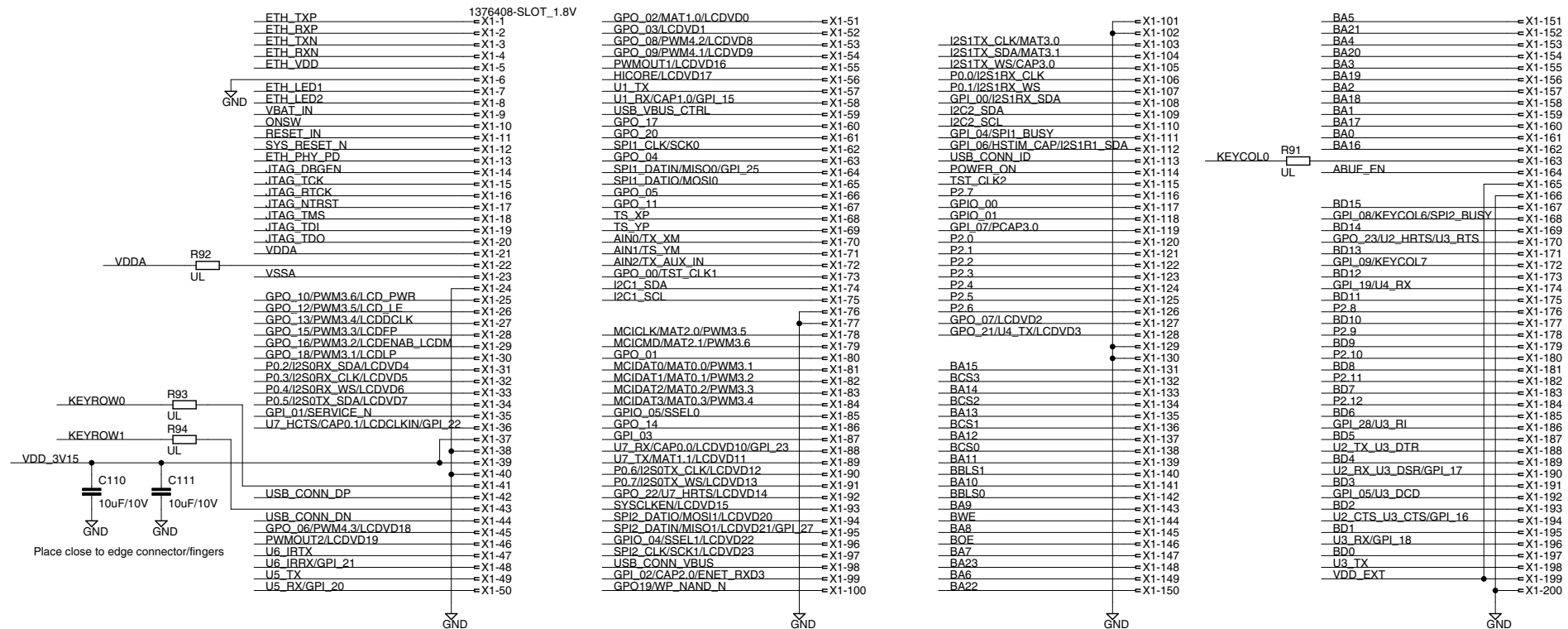
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SODIMM-200 Edge Connector



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