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Reset, oscillators, JTAG I2C-E2PROM, SPI-Flash

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DDR RAM, NAND FLASH, buffers

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Peripheral interface

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Ethernet interface

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SODIMM edge connector interface

UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic. However, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place.

Rev 1.1

Changed control pins for LED3-5 to P2.10-12. Added voltage test points.

Rev 1.0

Original revision



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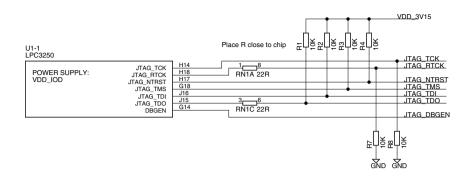
TITLE: LPC32x0 OEM Board v1.1

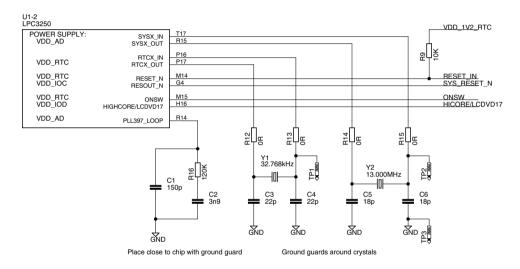
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Date: 2009-09-06 20:42:49

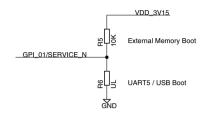
Sheet: 1/8

REV:

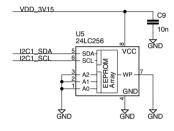




Boot Source Selection

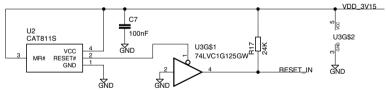


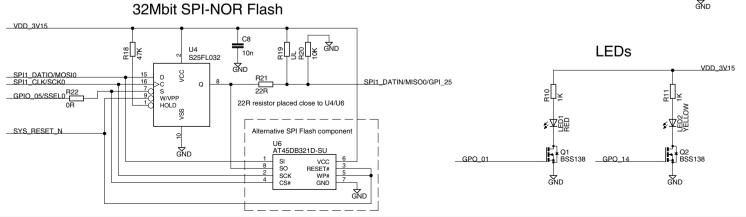
256kbit EEPROM

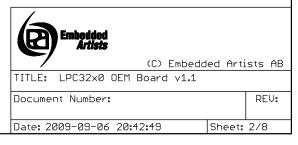


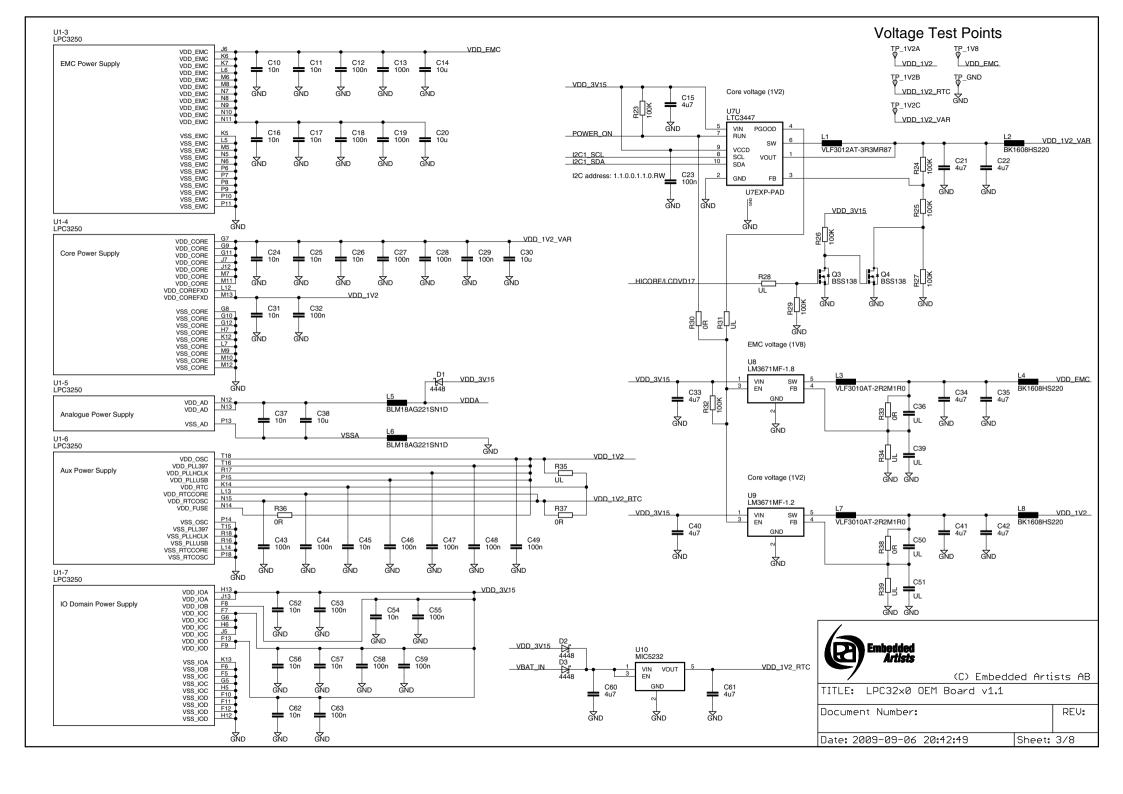
I2C address: 1.0.1.0.0.0.0.RW

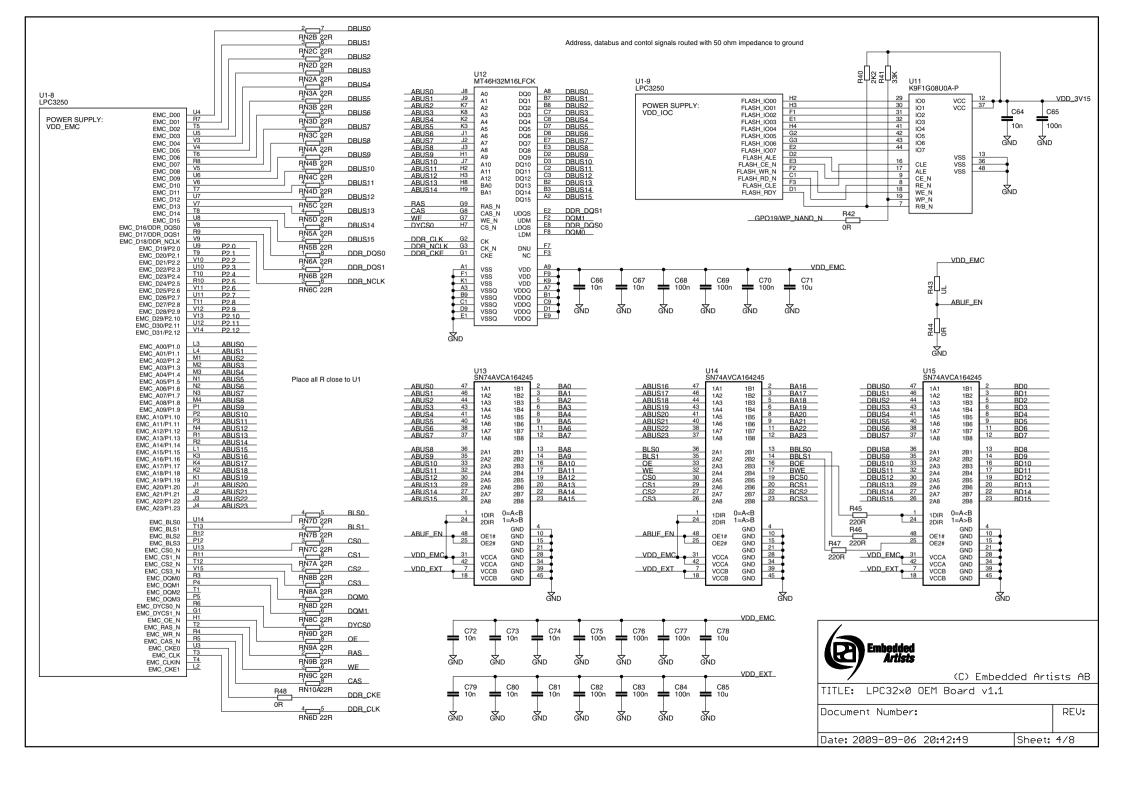
Reset generation

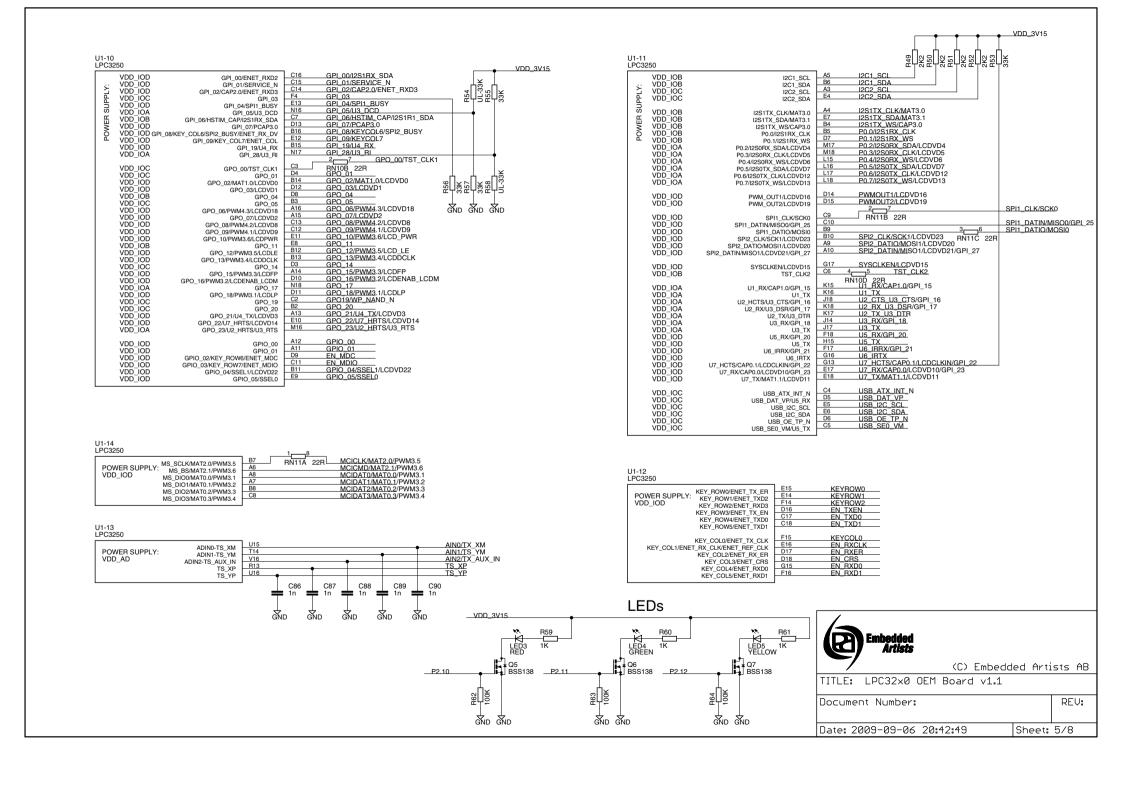


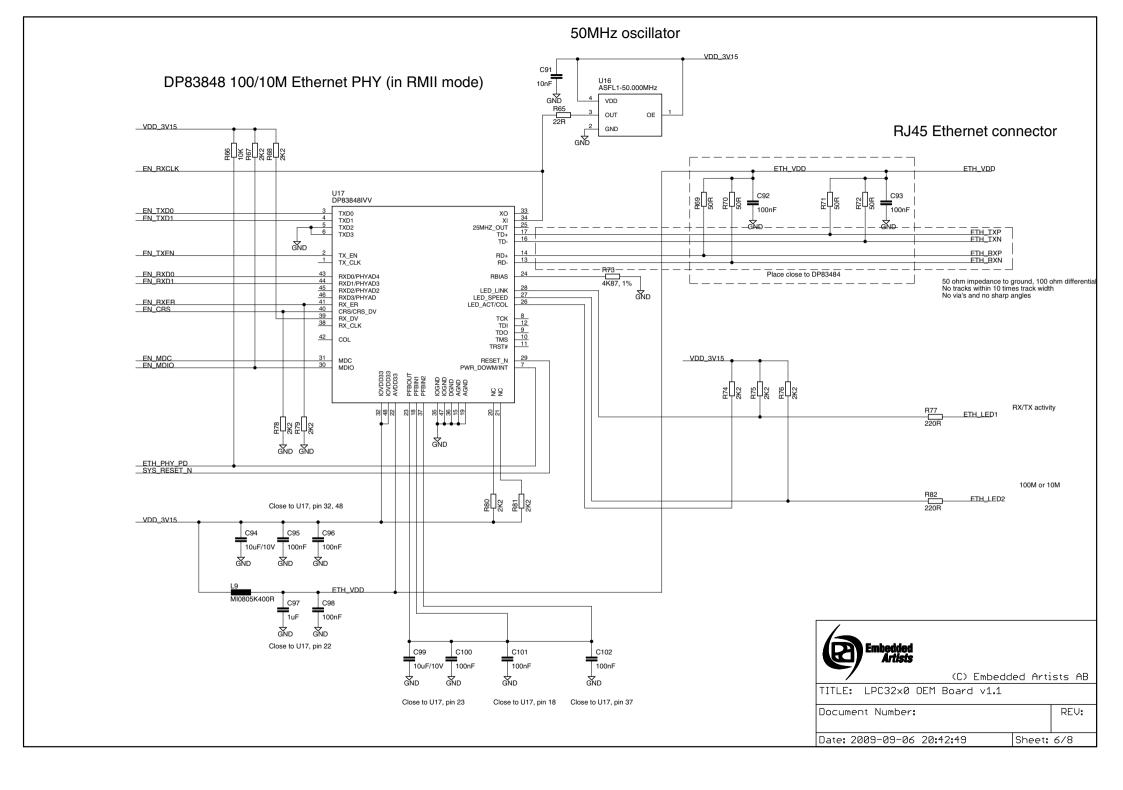




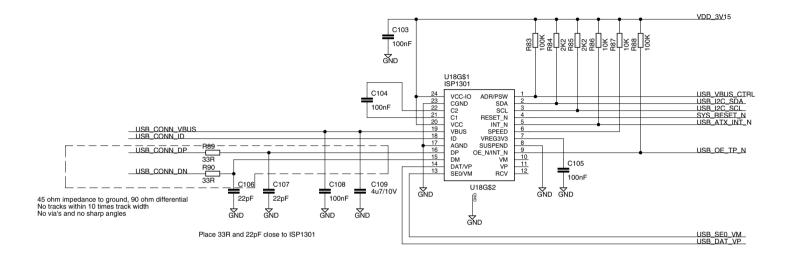








OTG USB Channel





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SODIMM-200 Edge Connector

