

# Vidushi Gupta

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## EDUCATION

### IIT (ISM) DHANBAD

#### MTECH IN POWER SYSTEMS

July 2019 | Dhanbad, India

## ACHIEVEMENTS

- Maximum star gold badge achieved in **Problem Solving** and **SQL** on **HackerRank** ([profile link](#))
- 230+ problems solved on **LeetCode** ([profile link](#)) hosted **here** on **GitHub**
- **All India Rank - 2443** out of **1,17,443** in GATE 2017 Electrical with percentile of **97.91**
- Qualified GATE Electrical in **2016, 2017, 2018** and **2019**
- **Director** of Club services ROTARACT UIET '14
- **Ranked 2nd** in Senior (WOMEN) Chandigarh Handball Championship in March 2012
- Participated in 40th Senior (WOMEN) **National** Handball Championship Delhi in 2012

## SKILLS

### LANGUAGES

C • C++ • Python • SQL

### SOFTWARE

MATLAB • Comsol • KEIL • Git

### HARDWARE

8085 Microprocessor • 8051 Microcontroller

### FAMILIAR

DS/Algo • DBMS

OS • LLD • CN

## LINKS

Github: [VidushiGupta80](#)

LinkedIn: [vidushi-gupta-80384183](#)

Hackerrank: [guptavidushi80\\_1](#)

LeetCode: [guptavidushi](#)

## MINOR PROJECTS

- EMI and Counter Measures for Pilot Display System of Fight Aircrafts
- Wireless Microphone System

## WORK EXPERIENCE AND INTERNSHIPS

### RAYAT GROUP OF INSTITUTIONS | ASSISTANT PROFESSOR

- Taught **Power System Analysis** (*Sem 7*), **Power Electronics** (*Sem 5*), **Electrical Circuit Analysis** (*Sem 3*), **Basics of Electrical Engineering** (*Sem 1*)
- **SPOC** for **NPTEL Swayam** and class in-charge for *Sem 3*.

### INTERNSHIPS

- 1 year Research internship at **CSIR-CSIO, Chandigarh** (2018-2019)
- 6 months Research internship at **CSIR-CSIO, Chandigarh** (2016)
- 6 weeks Industrial training at **Bharat Electronics Limited, Panchkula** (2015)

## CERTIFICATIONS

- Passing Certificate with **96%** in Problem solving through **programming in C** ([certificate link](#)) conducted by **IIT - Kharagpur, NPTEL**
- Passing Certificate with **96%** in An introduction to **programming through C++** ([certificate link](#)) conducted by **IIT - Bombay, NPTEL**
- Elite and Faculty Development Program Certificate with **71%** in **Introduction to Smart Grid** ([certificate link](#)) conducted by **IIT - Roorkee, NPTEL**
- Elite and Faculty Development Program Certificate with **68%** in **DC Microgrid** ([certificate link](#)) conducted by **IIT - Roorkee, NPTEL**

## RESEARCH

### CSIO-CSIR & IIT (ISM), DHANBAD | RESEARCH

Jul 2018 – Jul 2019 | Chandigarh & Dhanbad

- Worked under **Principal Scientist Vipin Kumar (CSIO-CSIR)** and **Dr. Gauri Shankar (IIT Dhanbad)**. Simulated the effect of various coolants when passed through a power card emitting high heat energy. Fluids considered were air, water and ferrofluid. Designed fins for convective cooling. Analyzed EMI caused due to permanent magnet and ferrofluid. Worked on EMC to reduce EMI to work in safe operating area.

## PROJECTS

- **Parking Lot Management System** ([Repo Link](#))  
**Backend:** Complete with **RESTful APIs** written in **Flask (Python3)** tested using Postman, **SQLite** as DB. Supports different user logins with **Flask blueprints**.  
**Frontend:** Working towards developing with **HTML/CSS/JS**.
- **Room Automation System**  
Lead efforts towards making an automatic room light control system using lasers (LDR pair), 8051 microprocessor and programming in C. Acts according to how many people are in the room currently. Implemented in one of the lecture halls of UIET, PU, Chandigarh.

## PUBLICATIONS

### INTERNATIONAL CONFERENCE

- Vipin Kumar, Vidushi Gupta, Gauri Shankar, Harry Garg - Simulation of efficient thermal management techniques for miniaturized circuits and systems, IEEE conference (Madras section), ViTECoN, March 2019

### NATIONAL CONFERENCE

- Kamalpreet Kaur, Tanvi Thakur, Vidushi Gupta, Dipanshu Sehgal, Vipin Kumar - Electromagnetic Interference and its counter measures for Pilot Display System (PDS): A case study, IETE, Sector 30 Chandigarh, April 2016