

Testbench Synchronous FIFO behav.wcf



Name	Value
clk	1
rst	1
wr_en	0
rd_en	0
> din[7:0]	00000000
> dout[7:0]	00000000
full	0
empty	1
> i[31:0]	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
> DATA_...	00000000000000000000000000001000
> DEPTH[...	000000000000000000000000000010000
> ADDR_...	0000000000000000000000000000100

