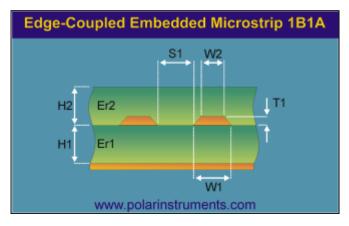
Polar Si9000 PCB Transmission Line Field Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	6.0000	+/-	0.0000	6.0000	6.0000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Substrate 2 Height	H2	2.4000	+/-	0.0000	2.4000	2.4000
Substrate 2 Dielectric	Er2	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	4.0000	+/-	0.0000	4.0000	4.0000
Upper Trace Width	W2	3.0000	+/-	0.0000	3.0000	3.0000
Trace Separation	S1	4.2000	+/-	0.0000	4.2000	4.2000
Trace Thickness	T1	1.2000	+/-	0.0000	1.2000	1.2000
Differential Impedance	Zdiff	100.62			100.62	100.62
Delay (Odd Mode) (ps/in)	D	158.572			158.572	158.572
Odd Mode Impedance	Zodd	50.31			50.31	50.31
Even Mode Impedance	Zeven	91.45			91.45	91.45
Common Mode Impedance	Zcommon	45.73			45.73	45.73

Notes

Add your comments here

