Polar Si9000 PCB Transmission Line Field Solver

Edge-Coupled Coated Microstrip 1B

www.polarinstruments.com

				Tolerance	Minimum	Maximum
Substrate 1 Height	H1	4.0000	+/-	0.0000	4.0000	4.0000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	4.0000	+/-	0.0000	4.0000	4.0000
Upper Trace Width	W2	3.0000	+/-	0.0000	3.0000	3.0000
Trace Separation	S1	4.0000	+/-	0.0000	4.0000	4.0000
Trace Thickness	T1	1.2000	+/-	0.0000	1.2000	1.2000
Coating Above Substrate	C1	1.0000	+/-	0.0000	1.0000	1.0000
Coating Above Trace	C2	0.5000	+/-	0.0000	0.5000	0.5000
Coating Between Traces	C3	1.0000	+/-	0.0000	1.0000	1.0000
Coating Dielectric	CEr	3.4000	+/-	0.0000	3.4000	3.4000

Differential Impedance	Zdiff	100.35	------		100.35	100.35
Delay (Odd Mode) (ps/in)	D	146.043	------		146.043	146.043
Odd Mode Impedance	Zodd	50.17	------		50.17	50.17
Even Mode Impedance	Zeven	74.86	------		74.86	74.86
Common Mode Impedance	Zcommon	37.43	------		37.43	37.43

Notes

Add your comments here