Polar Si9000 PCB Transmission Line Field Solver

			Tolerance		Minimum	Maximum
Substrate 1 Height	H1	6.0000	+/-	0.0000	6.0000	6.0000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Substrate 2 Height	H2	2.4000	+/-	0.0000	2.4000	2.4000
Substrate 2 Dielectric	Er2	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	5.5000	+/-	0.0000	5.5000	5.5000
Upper Trace Width	W2	4.5000	+/-	0.0000	4.5000	4.5000
Trace Separation	S1	4.0000	+/-	0.0000	4.0000	4.0000
Trace Thickness	T1	1.2000	+/-	0.0000	1.2000	1.2000

Differential Impedance	Zdiff	90.28	------		90.28	90.28
Delay (Odd Mode) (ps/in)	D	157.624	------		157.624	157.624
Odd Mode Impedance	Zodd	45.14	------		45.14	45.14
Even Mode Impedance	Zeven	80.73	------		80.73	80.73
Common Mode Impedance	Zcommon	40.37	------		40.37	40.37

Notes

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