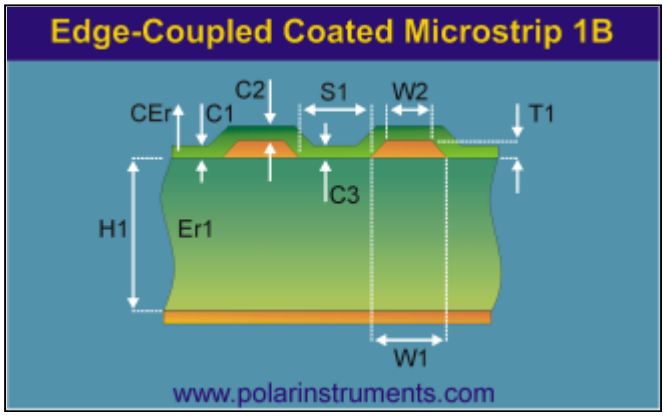
Polar Si9000 PCB Transmission Line Field Solver



				Tolerance	Minimum	Maximum
Substrate 1 Height	H1	4.0000	+/-	0.0000	4.0000	4.0000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	5.3000	+/-	0.0000	5.3000	5.3000
Upper Trace Width	W2	4.3000	+/-	0.0000	4.3000	4.3000
Trace Separation	S1	4.0000	+/-	0.0000	4.0000	4.0000
Trace Thickness	T1	1.2000	+/-	0.0000	1.2000	1.2000
Coating Above Substrate	C1	1.0000	+/-	0.0000	1.0000	1.0000
Coating Above Trace	C2	0.5000	+/-	0.0000	0.5000	0.5000
Coating Between Traces	C3	1.0000	+/-	0.0000	1.0000	1.0000
Coating Dielectric	CEr	3.4000	+/-	0.0000	3.4000	3.4000

Differential Impedance	Zdiff	90.05	------	90.05	90.05
Delay (Odd Mode) (ps/in)	D	146.613	------	146.613	146.613
Odd Mode Impedance	Zodd	45.03	------	45.03	45.03
Even Mode Impedance	Zeven	65.42	------	65.42	65.42
Common Mode Impedance	Zcommon	32.71	------	32.71	32.71

Notes

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