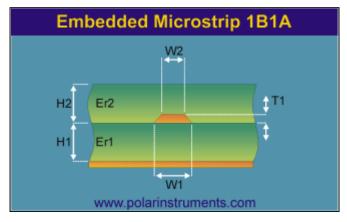
Polar Si9000 PCB Transmission Line Field Solver



			<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>	
Substrate 1 Height	H1	6.0000 +	-/- 0.0000	6.0000	6.0000	
Substrate 1 Dielectric	Er1	4.2000 +	-/- 0.0000	4.2000	4.2000	
Substrate 2 Height	H2	2.4000 +	-/- 0.0000	2.4000	2.4000	
Substrate 2 Dielectric	Er2	4.2000 +	-/- 0.0000	4.2000	4.2000	
Lower Trace Width	W1	9.5000 +	-/- 0.0000	9.5000	9.5000	
Upper Trace Width	W2	8.5000 +	-/- 0.0000	8.5000	8.5000	
Trace Thickness	T1	1.2000 +	-/- 0.0000	1.2000	1.2000	
Impedance	Zo	50.53		50.53	50.53	
Delay (ps/in)	D	159.063		159.063	159.063	
Inductance (nH/in)	L	8.037		8.037	8.037	
Capacitance (pF/in)	С	3.148		3.148	3.148	

Notes

Add your comments here

