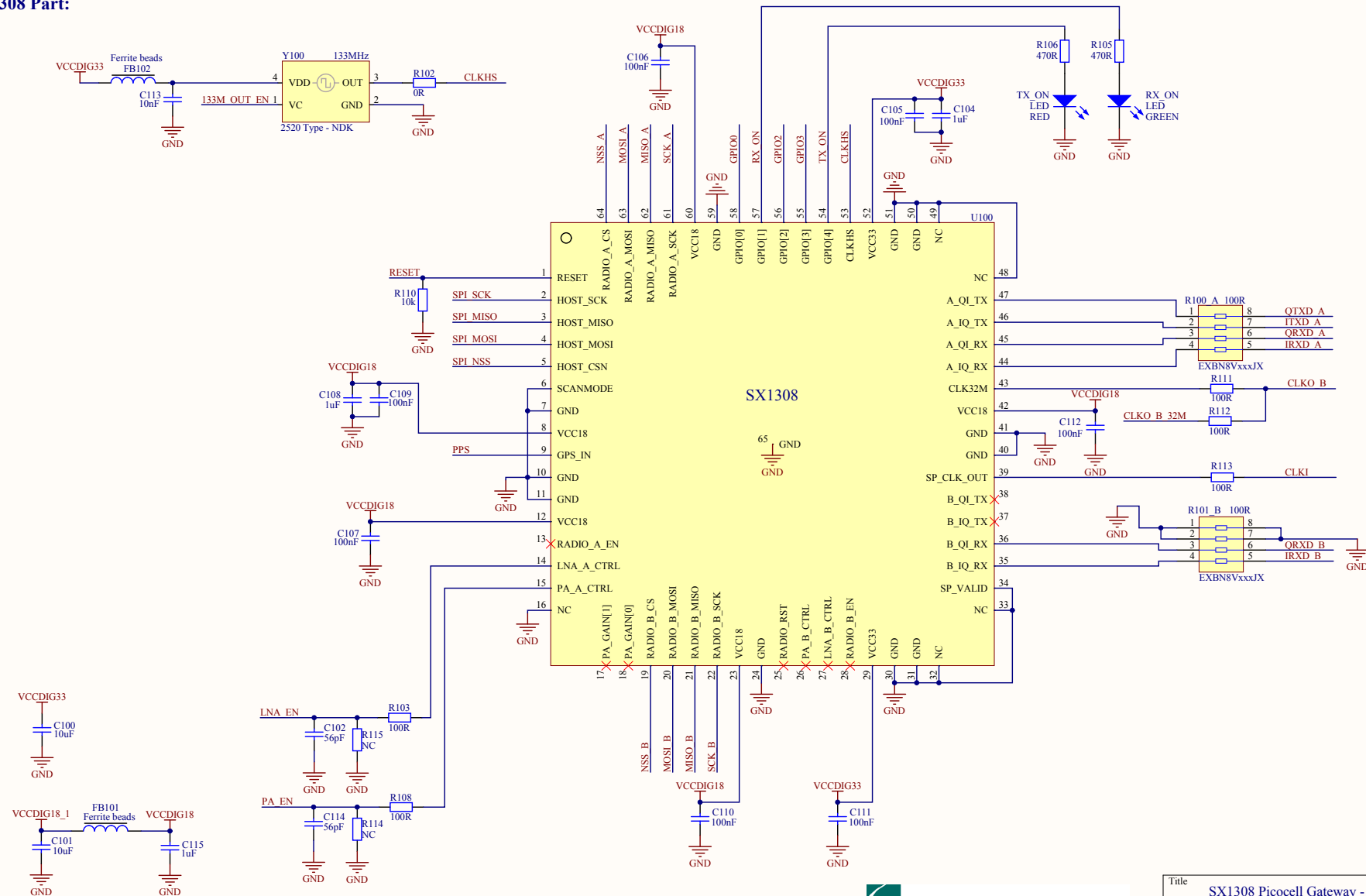


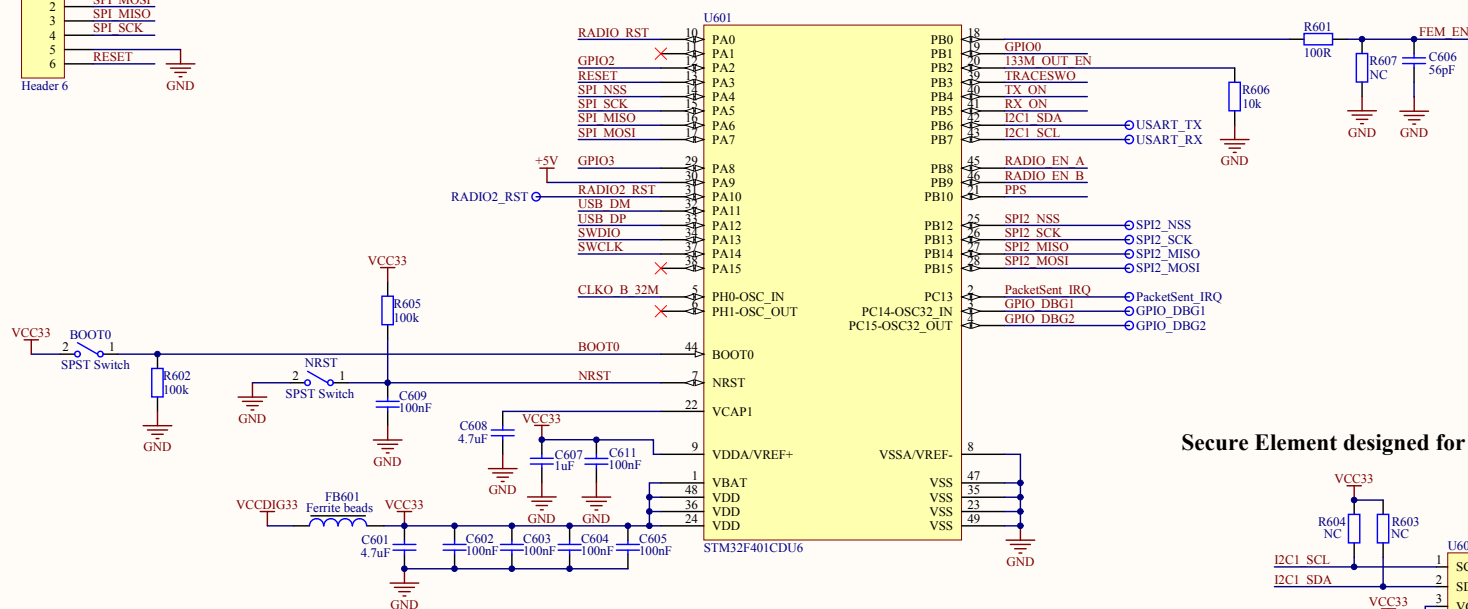
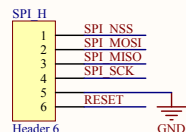
SX1308 Part:

Title SX1308 Picocell Gateway - SX1308 part			
Size B	Number PCB_E381V02C		Revision V1a
Date:	20.12.2017		Sheet of
File:	\\...\SX1308 GTW_e381v02c_SX1308 part\NewDocx		

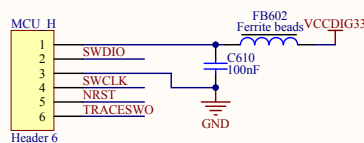
The image displays a detailed PCB layout for a LoRa module, featuring two identical channels labeled A and B. Each channel is centered around an SX1257 transceiver IC (U800 for channel A, U900 for channel B). The layout includes various passive components such as capacitors (C801, C802, C803, C804, C805, C806, C807, C808, C809, C810, C811, C812, C813, C814, C815, C816, C817, C818, C819, C820, C821, C822, C823, C824, C825, C826, C827, C828, C829, C830, C831, C832, C833, C834, C835, C836, C837, C838, C839, C840, C841, C842, C843, C844, C845, C846, C847, C848, C849, C850, C851, C852, C853, C854, C855, C856, C857, C858, C859, C860, C861, C862, C863, C864, C865, C866, C867, C868, C869, C870, C871, C872, C873, C874, C875, C876, C877, C878, C879, C880, C881, C882, C883, C884, C885, C886, C887, C888, C889, C890, C891, C892, C893, C894, C895, C896, C897, C898, C899, C900, C901, C902, C903, C904, C905, C906, C907, C908, C909, C910, C911, C912, C913, C914, C915, C916, C917, C918, C919, C920, C921, C922, C923, C924, C925, C926, C927, C928, C929, C930, C931, C932, C933, C934, C935, C936, C937, C938, C939, C940, C941, C942, C943, C944, C945, C946, C947, C948, C949, C950, C951, C952, C953, C954, C955, C956, C957, C958, C959, C960, C961, C962, C963, C964, C965, C966, C967, C968, C969, C970, C971, C972, C973, C974, C975, C976, C977, C978, C979, C980, C981, C982, C983, C984, C985, C986, C987, C988, C989, C990, C991, C992, C993, C994, C995, C996, C997, C998, C999, C1000) and resistors (R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R837, R838, R839, R840, R841, R842, R843, R844, R845, R846, R847, R848, R849, R850, R851, R852, R853, R854, R855, R856, R857, R858, R859, R860, R861, R862, R863, R864, R865, R866, R867, R868, R869, R870, R871, R872, R873, R874, R875, R876, R877, R878, R879, R880, R881, R882, R883, R884, R885, R886, R887, R888, R889, R890, R891, R892, R893, R894, R895, R896, R897, R898, R899, R900, R901, R902, R903, R904, R905, R906, R907, R908, R909, R910, R911, R912, R913, R914, R915, R916, R917, R918, R919, R920, R921, R922, R923, R924, R925, R926, R927, R928, R929, R930, R931, R932, R933, R934, R935, R936, R937, R938, R939, R940, R941, R942, R943, R944, R945, R946, R947, R948, R949, R950, R951, R952, R953, R954, R955, R956, R957, R958, R959, R960, R961, R962, R963, R964, R965, R966, R967, R968, R969, R970, R971, R972, R973, R974, R975, R976, R977, R978, R979, R980, R981, R982, R983, R984, R985, R986, R987, R988, R989, R990, R991, R992, R993, R994, R995, R996, R997, R998, R999, R1000). The layout also shows the placement of the SX1257 IC, the SX1257 module, and the SX1257 module. The layout is divided into two main sections, A and B, each with its own set of components and connections. The layout is designed to be a single-layer PCB, with all components placed on one side of the board. The layout is a detailed representation of the physical design of the module, showing the placement of every component and the connections between them. The layout is a key part of the design process, as it allows the designer to visualize the physical layout of the module and to identify any potential issues or optimizations. The layout is a complex and detailed design, but it is also a very clear and easy-to-understand representation of the module's physical design. The layout is a valuable tool for the designer, as it allows them to see the physical layout of the module and to make any necessary adjustments or optimizations. The layout is a key part of the design process, and it is a very important part of the overall design of the module.

STM32 MCU Part :

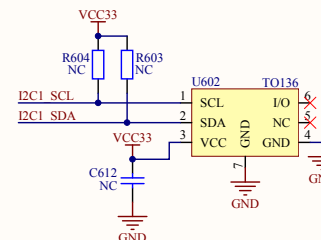
SPI_Header for debug



MCU Prog Header :



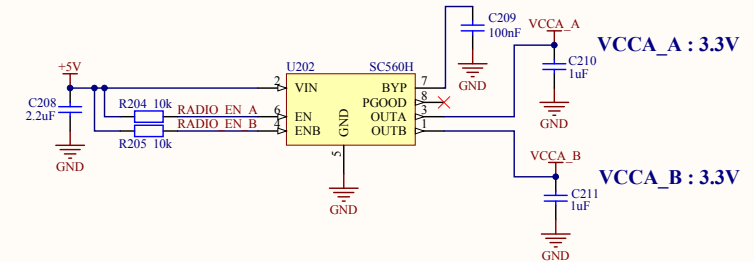
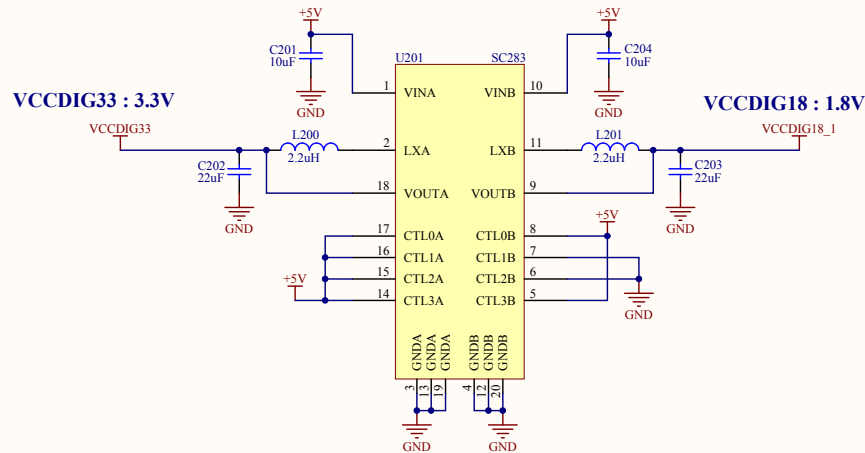
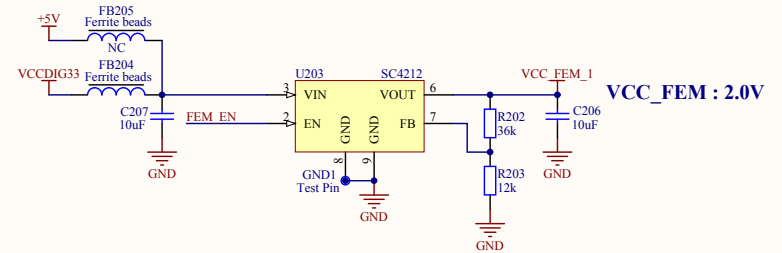
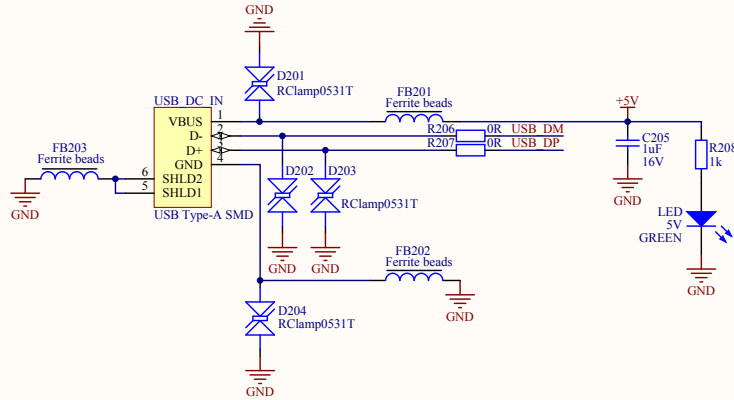
Secure Element designed for IoT and M2M apps (optional)



Title SX1308 Picocell Gateway - STM32 MCU part		
Size B	Number PCB_E381V02C	Revision V1a
Date: 20.12.2017	Sheet 3 of 5	
File: \\SX1308_GTW_e381v02c_STM32_MCU_SchDoc	Erik Fountain	

DC Power Management for Picocell Gateway:

Main +5V USB power Supply



Title SX1308 Picocell Gateway - DC Power Management		
Size B	Number PCB_E381V02C	Revision V1a
Date: 20.12.2017	Sheet 4 of 5	Drawn By: Erik Fountain
File: \\.\SX1308 GTW_e381v02c Power.Sch		

SX1308 Picocell Gateway @868MHz - Reference Design :

TOP Schematic:

Designator
SX1308_GTW_e381v02c_Power.SchDoc



Designator
SX1308_GTW_e381v02c_RF_Part.SchDoc



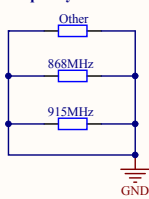
Designator
SX1308_GTW_e381v02c_SX1308_part.SchDoc



Designator
SX1308_GTW_e381v02c_STM32_MCU.SchDoc



Frequency reference:



Semtech logos + Fiducial_Marks:

- .0 Logo Recycling2
- .0 Logo ESD 2
- .0 Logo LoRa 1
- .0 Logo LoRa 2
- .0 Fiducial Mark1
- .0 Logo Semtech 1
- .0 Logo Semtech 2



Title			
SX1308 Picocell Gateway - TOP schematic			
Size	Number		Revision
B	PCB_E381V02C		V1a
Date:	20.12.2017	Sheet of	
File:	\\.\SX1308_GTW_e381v02c_Top.SchDoc		
	Drawn By:		