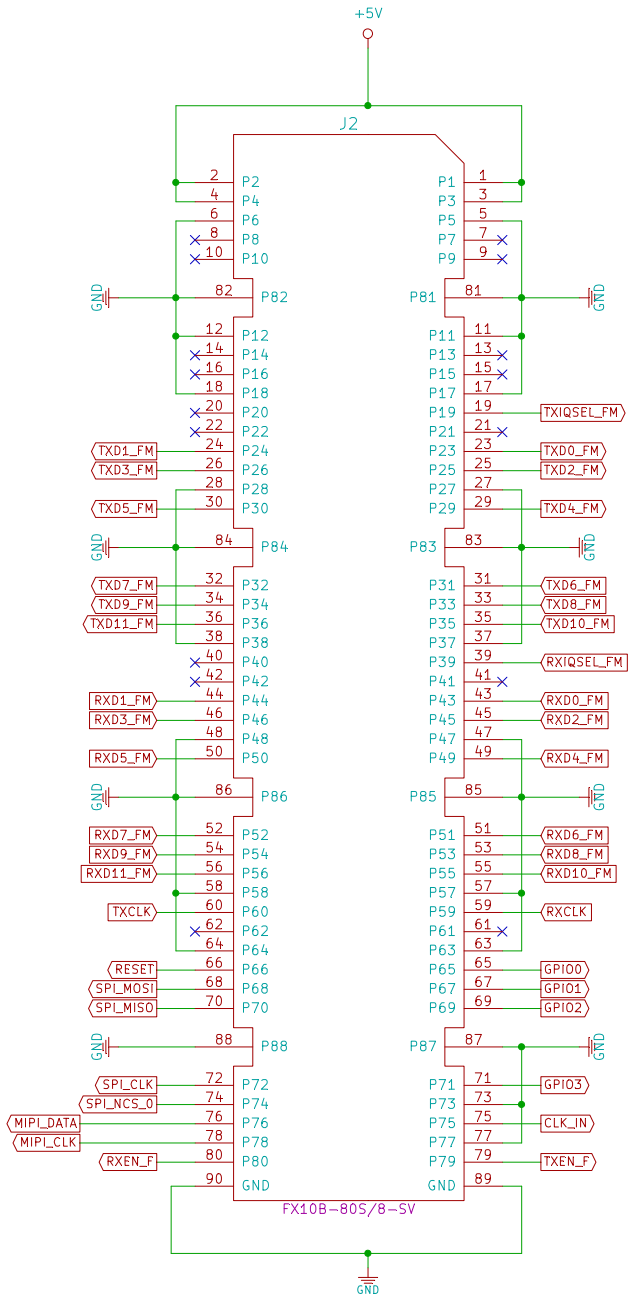
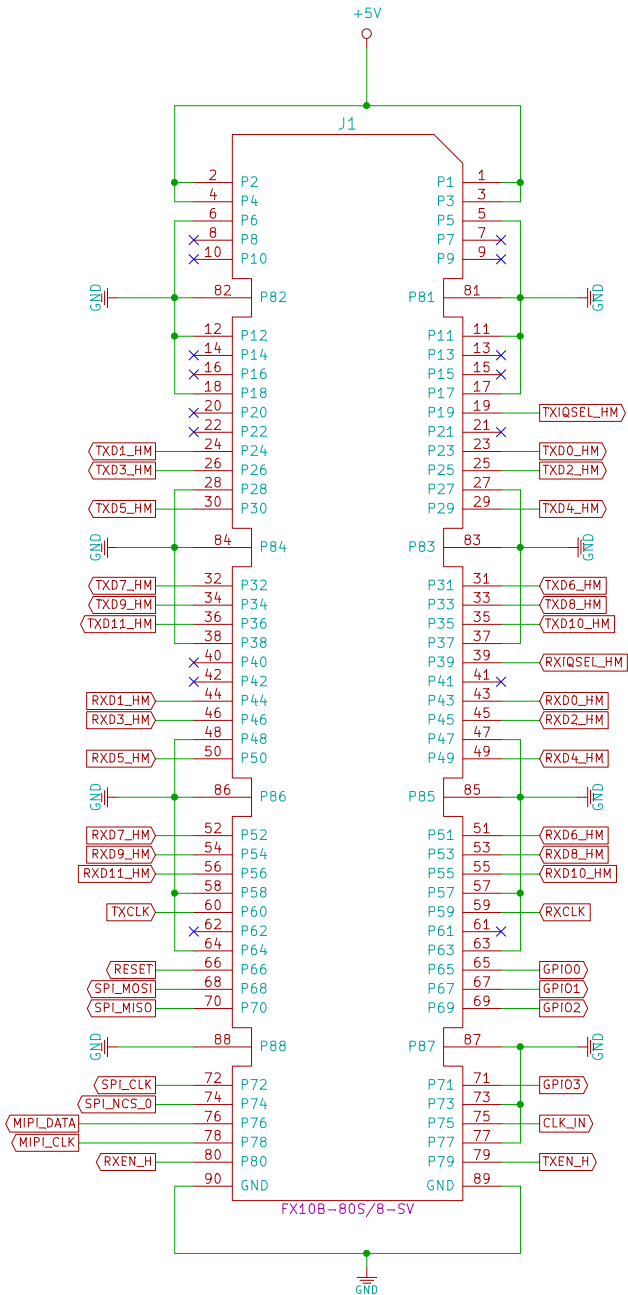


	1	2	3
A			
B	<div>Sheet: Data Interface Connector</div> <div>File: Data Interface Connector.sch</div>	<div>Sheet: HSMC &amp; Pin Header Connectors</div> <div>File: HSMC &amp; Pin Header Connectors.sch</div>	<div>Sheet: FMC Connector</div> <div>File: FMC Connector.sch</div>
C	<div>Sheet: Clock Generator</div> <div>File: Clock Generator.sch</div>	<div>Sheet: Power Supply</div> <div>File: Power Supply.sch</div>	<div>Sheet: Buffers</div> <div>File: Buffers.sch</div>
D	<div>Sheet: Microcontroller</div> <div>File: Microcontroller.sch</div>	<div>Sheet: ADF4002</div> <div>File: ADF4002.sch</div>	
E			<div>Copyright 2013 AZIO Electronics Co., Ltd. THIS WORK IS COVERED UNDER A CREATIVE COMMONS LICENSE (CC BY 3.0)</div> <div>File: Zipper.sch Sheet: /</div> <div>Title: Zipper v.2</div> <div>Size: User      Date: 7 jan 2014</div> <div>KiCad E.D.A.      eeschema (2013-07-07 BZR 4022)-stable</div> <div>Rev: 2</div> <div>Id: 1/9</div>
	1	2	3

# Myriad RF – HSMC / PinHeader Connector

# Myriad RF – FMC Connector



Copyright 2013 AZIO Electronics Co., Ltd.

THIS WORK IS COVERED UNDER A CREATIVE COMMONS LICENSE (CC BY 3.0)

File: Data Interface Connector.sch

Sheet: /Data Interface Connector/

Title: Zipper v.2

Size: A4

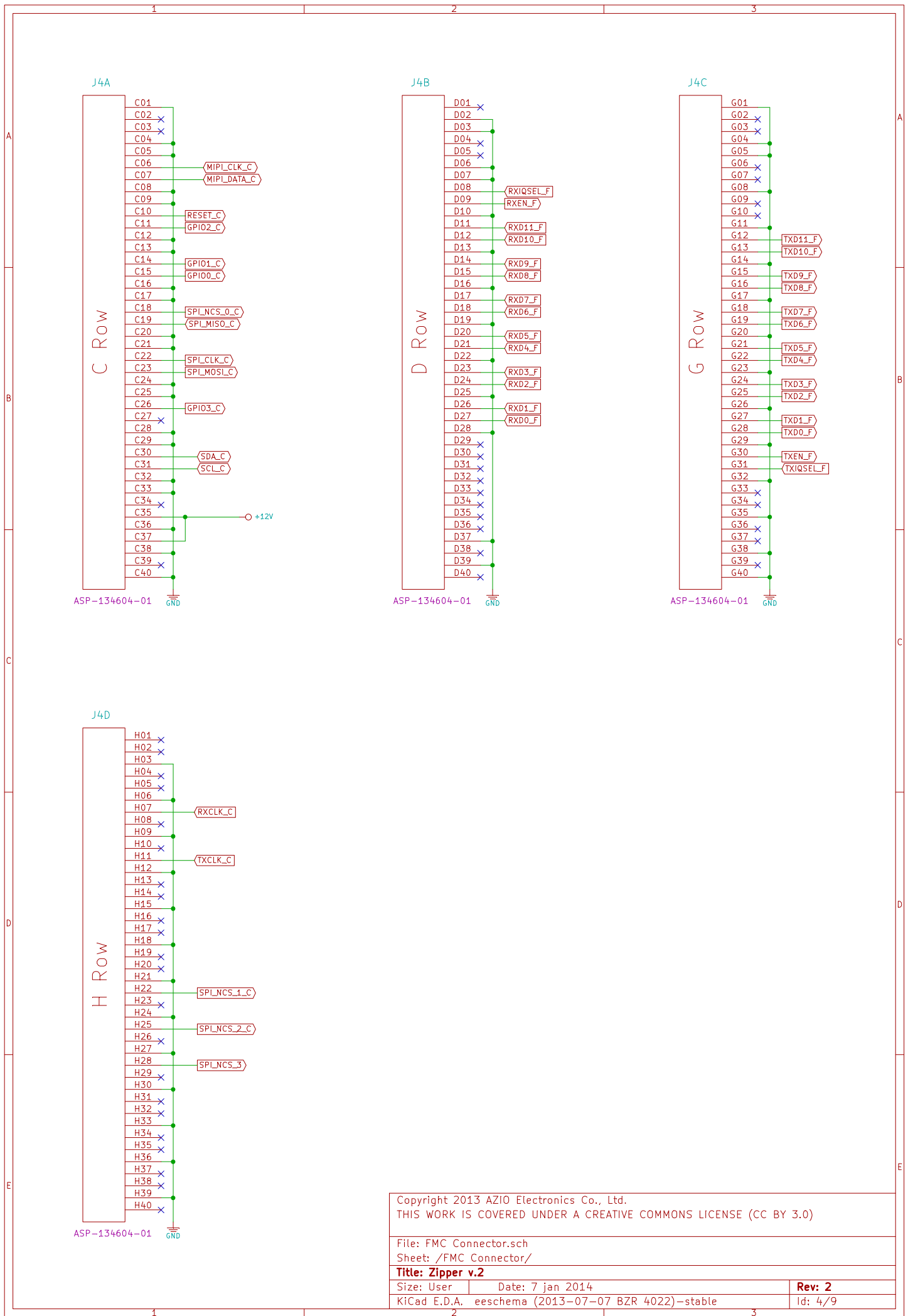
Date: 7 jan 2014

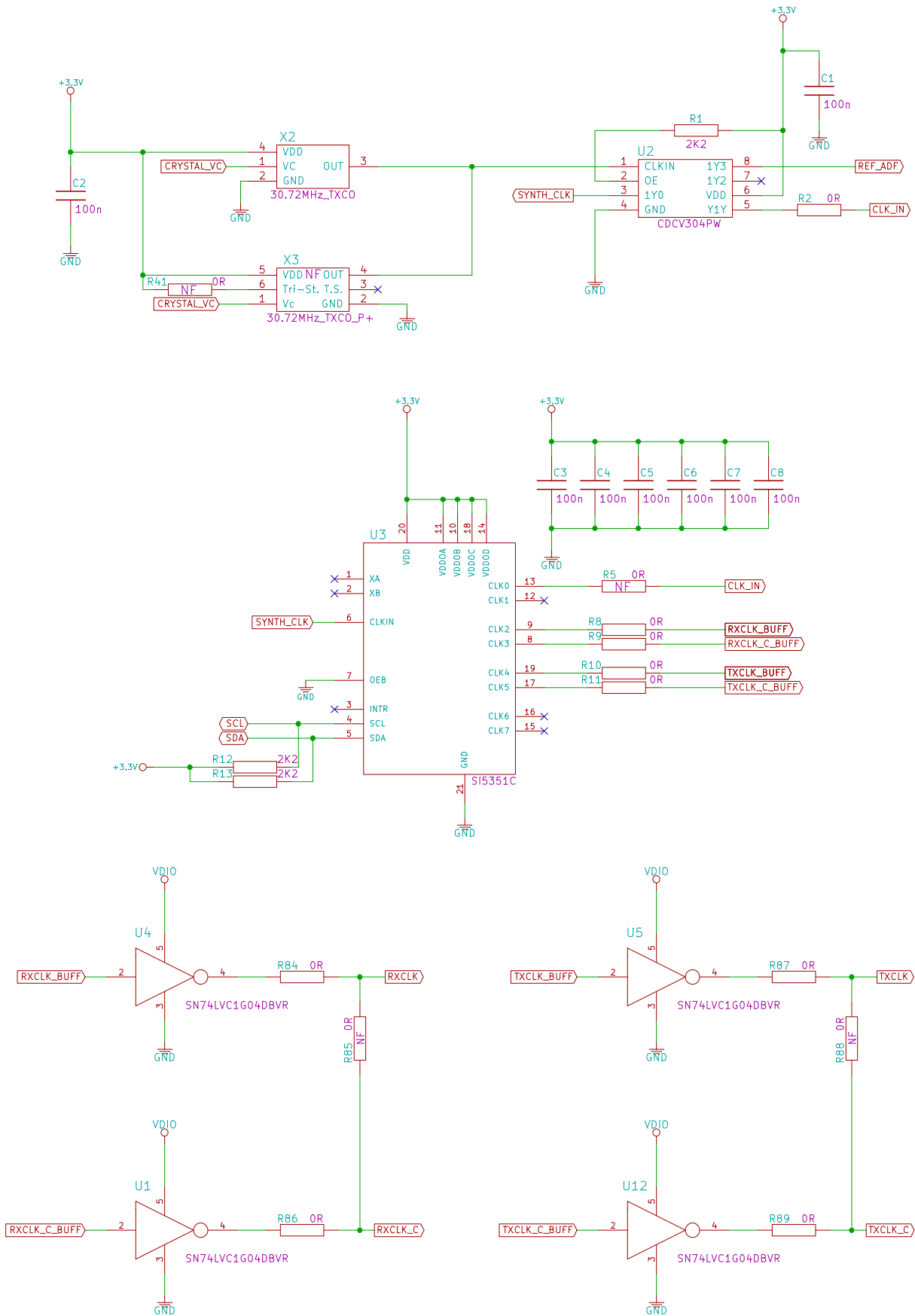
Rev: 2

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

Id: 2/9







Copyright 2013 AZIO Electronics Co., Ltd.  
THIS WORK IS COVERED UNDER A CREATIVE COMMONS LICENSE (CC BY 3.0)

File: Clock Generator.sch  
Sheet: /Clock Generator/

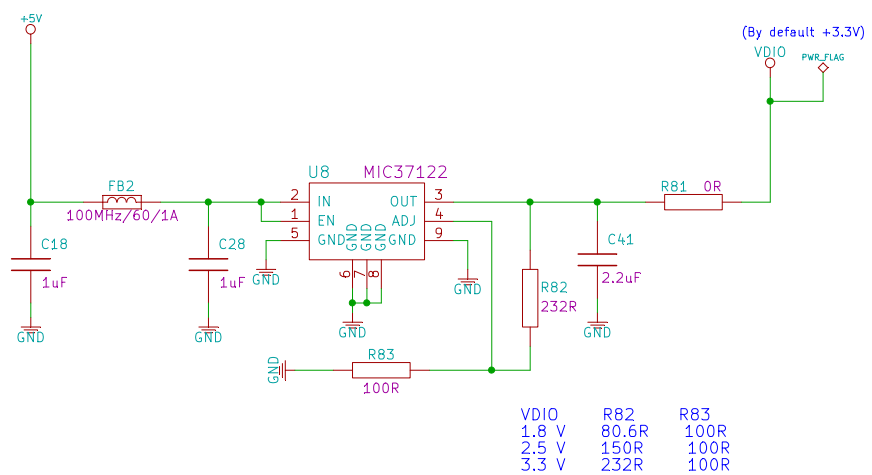
**Title: Zipper v.2**

Size: User Date: 7 jan 2014

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

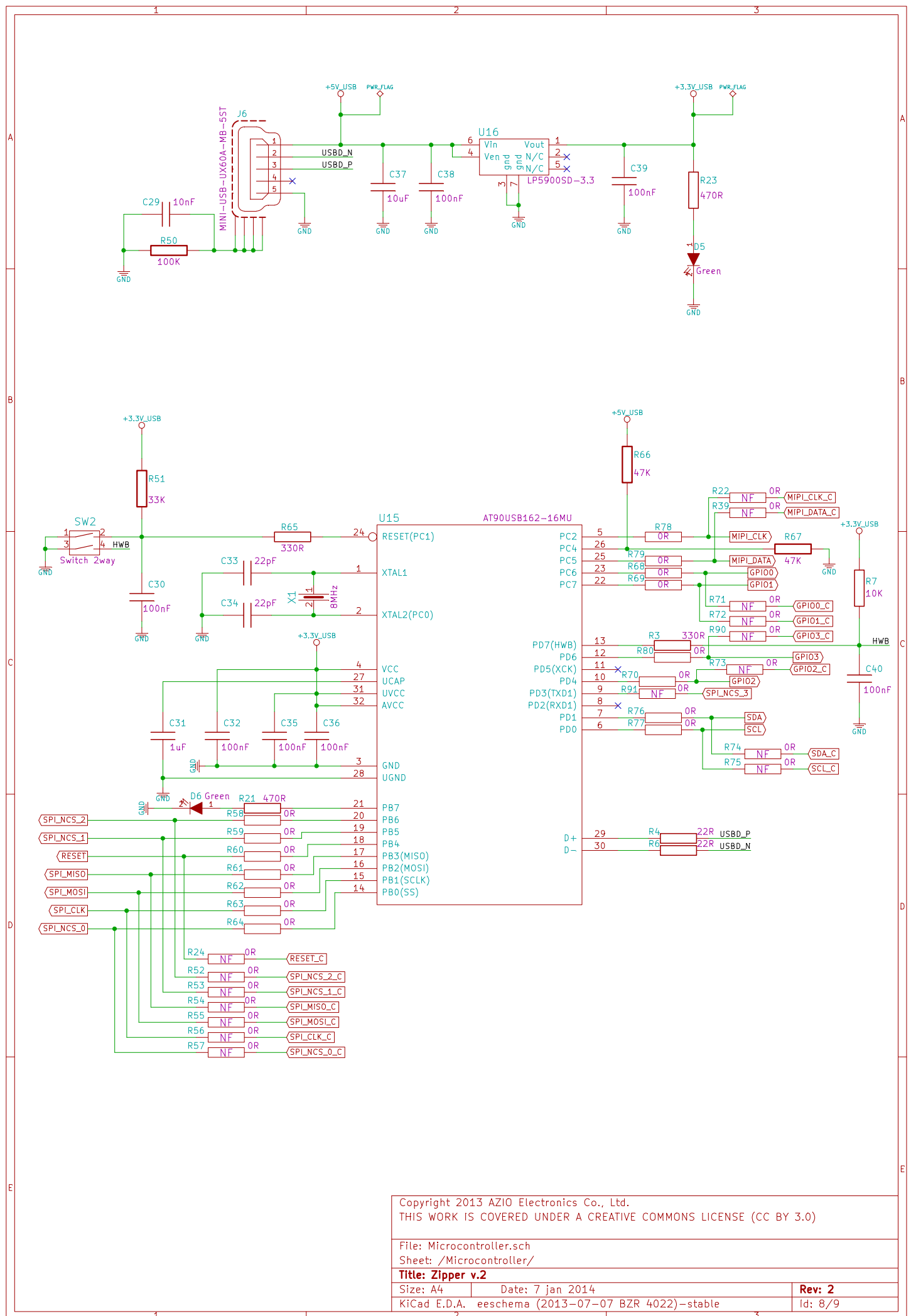
**Rev: 2**

Id: 5/9

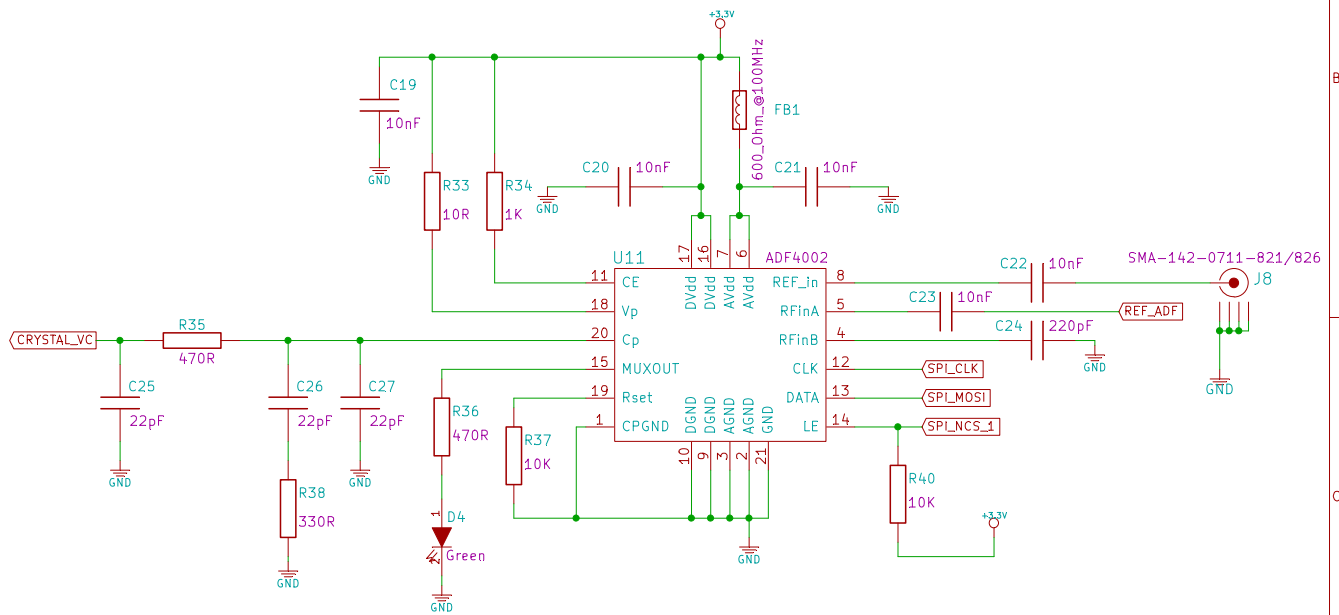


Id: 6/9









Copyright 2013 AZIO Electronics Co., Ltd.  
THIS WORK IS COVERED UNDER A CREATIVE COMMONS LICENSE (CC BY 3.0)

File: ADF4002.sch  
Sheet: /ADF4002/

**Title: Zipper v.2**

Size: A4 Date: 7 jan 2014

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

**Rev: 2**

Id: 9/9