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Operating Systems
Assignment 1

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1 Introduction

2 Annotated Bibliography

2.1 Virtual memory managers

Blanchet, G., & Bertrand, D. (2012). **Computer Architecture**. Hoboken, NJ: Wiley.

- Blanchet and Bertrand's (2012) book is aimed at readers wishing to learn about the essential architecture of a computer as a whole. However, chapter 9 (p. 175 - 204) provides insight into the functionality of virtual memory management.
- The authors introduce virtual management with a brief history, and explain the purpose and the trade-off when using virtual memory.
 - Blanchet and Bertrand (2012) assert that delays may occur while data is transferred between physical memory and mass storage.
- They explain how physical memory and a mass storage device interact to provide virtual memory through the use of paging, and briefly cover fetch and page fault algorithms. Page size considerations are also listed, and referred to as "conflicting criteria". The chapter also introduces the concept of multi-level paging.
- Blanchet and Bertrand (2012) then provide a detailed and technical step-by-step view of virtual memory management at work, using a program execution as an example.

3 Conclusion

References

Blanchet, G., & Bertrand, D. (2012). *Computer Architecture*. Hoboken, NJ: Wiley.