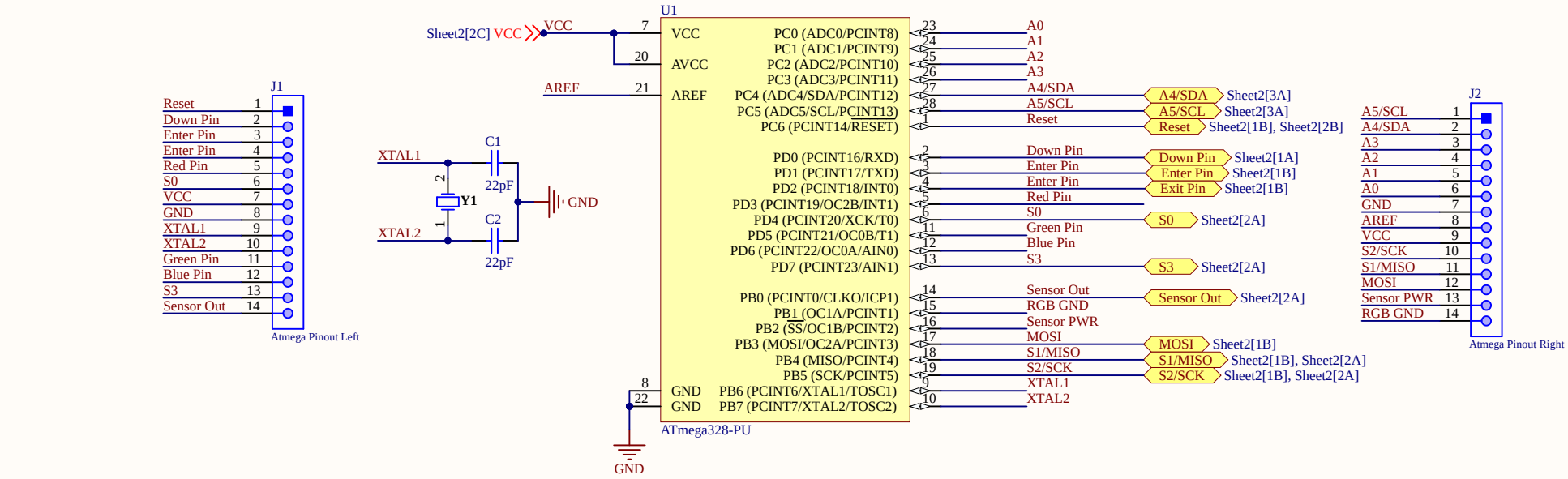
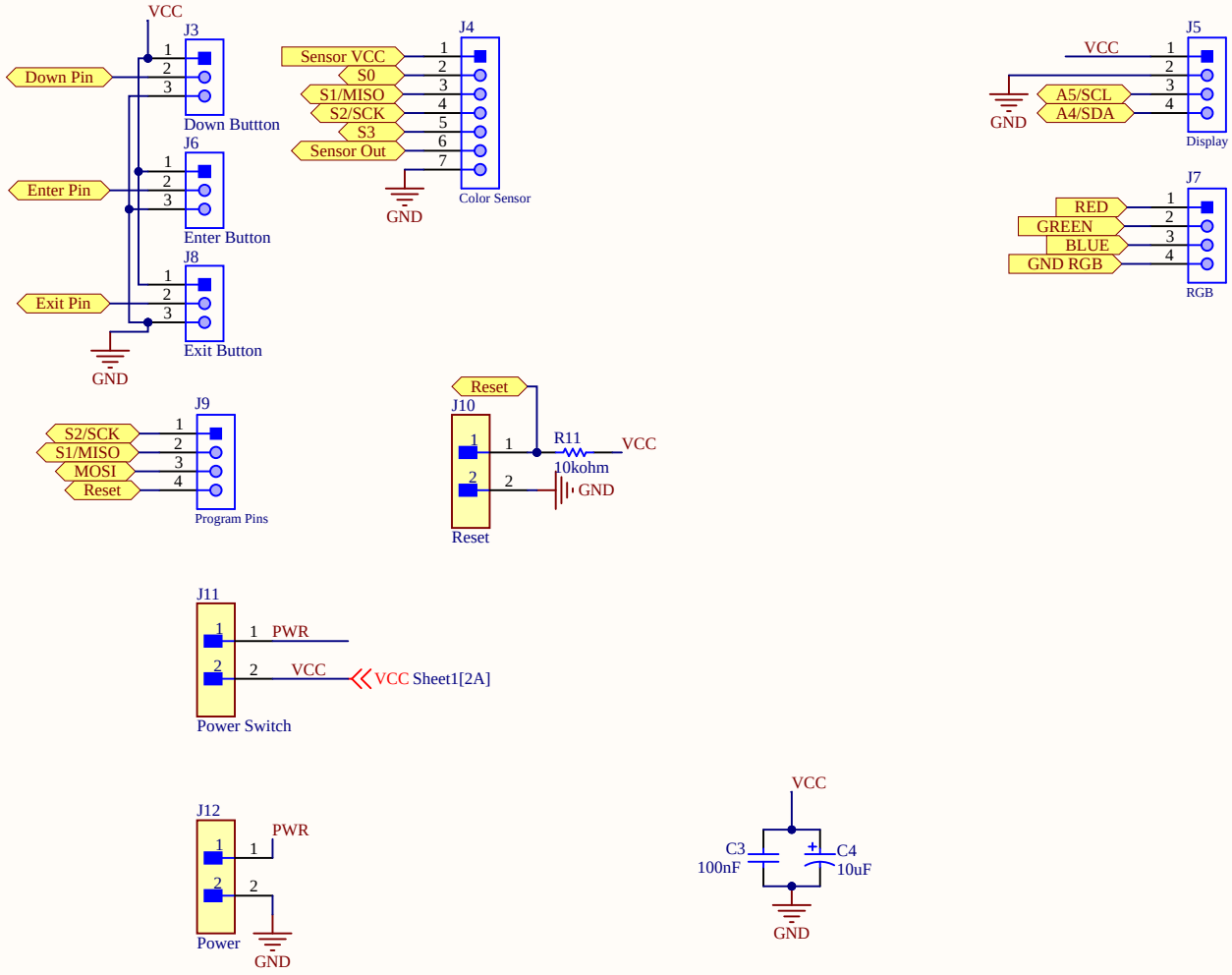
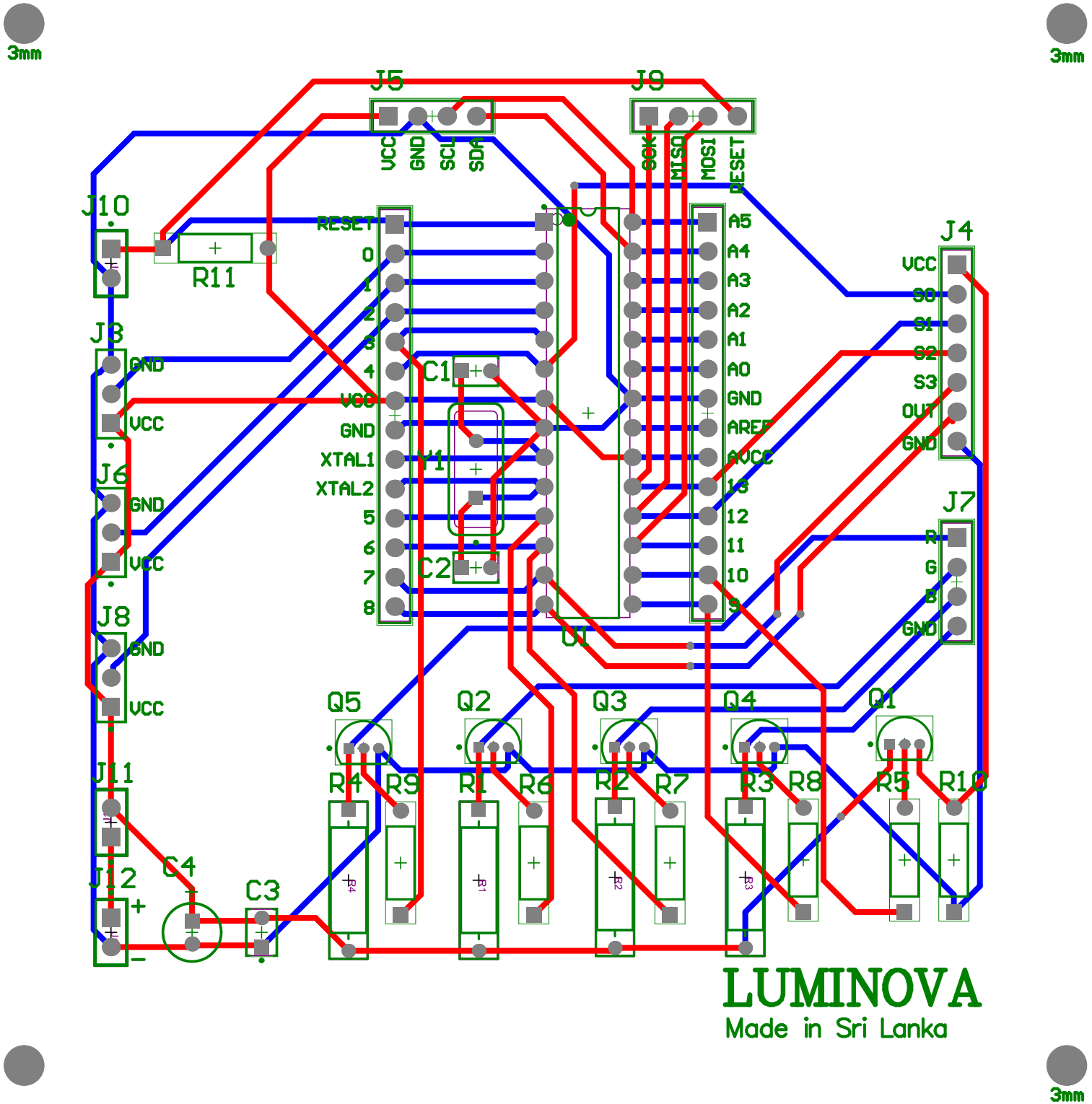


Title	Lamp Schematic - Microcontroller, Switching Transistors	
Size A4	Number 1	Revision
Date: 5/10/2023		Sheet 1 of 2
File: C:\Users\..\Sheet1.SchDoc		Drawn By: V Kurrshanth 200331T



Title	Lamp Schematic - I/O & Power	
Size: A4	Number: 2	Revision:
Date: 5/10/2023		Sheet 2 of 2
File: C:\Users\..\Sheet2.SchDoc		Drawn By: V Kurrshanth 200331T


3mm
3mm
3mm
J5
VCC
GND
SCL
SDA
J9
SCK
MISO
MOSI
RESET
J10
R11
J3
GND
VCC
J6
GND
VCC
J8
GND
VCC
J11
J12
C4
C3
RESET
0
1
2
3
4
VCC
GND
XTAL1
XTAL2
5
6
7
8
C1
Y1
C2
U1
A5
A4
A3
A2
A1
A0
GND
AREF
AVCC
13
12
11
10
9
J4
VCC
S0
S1
S2
S3
OUT
GND
J7
R
G
B
GND
Q5
Q2
Q3
Q4
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R4
R9
R1
R6
R2
R7
R3
R8
R5
R10
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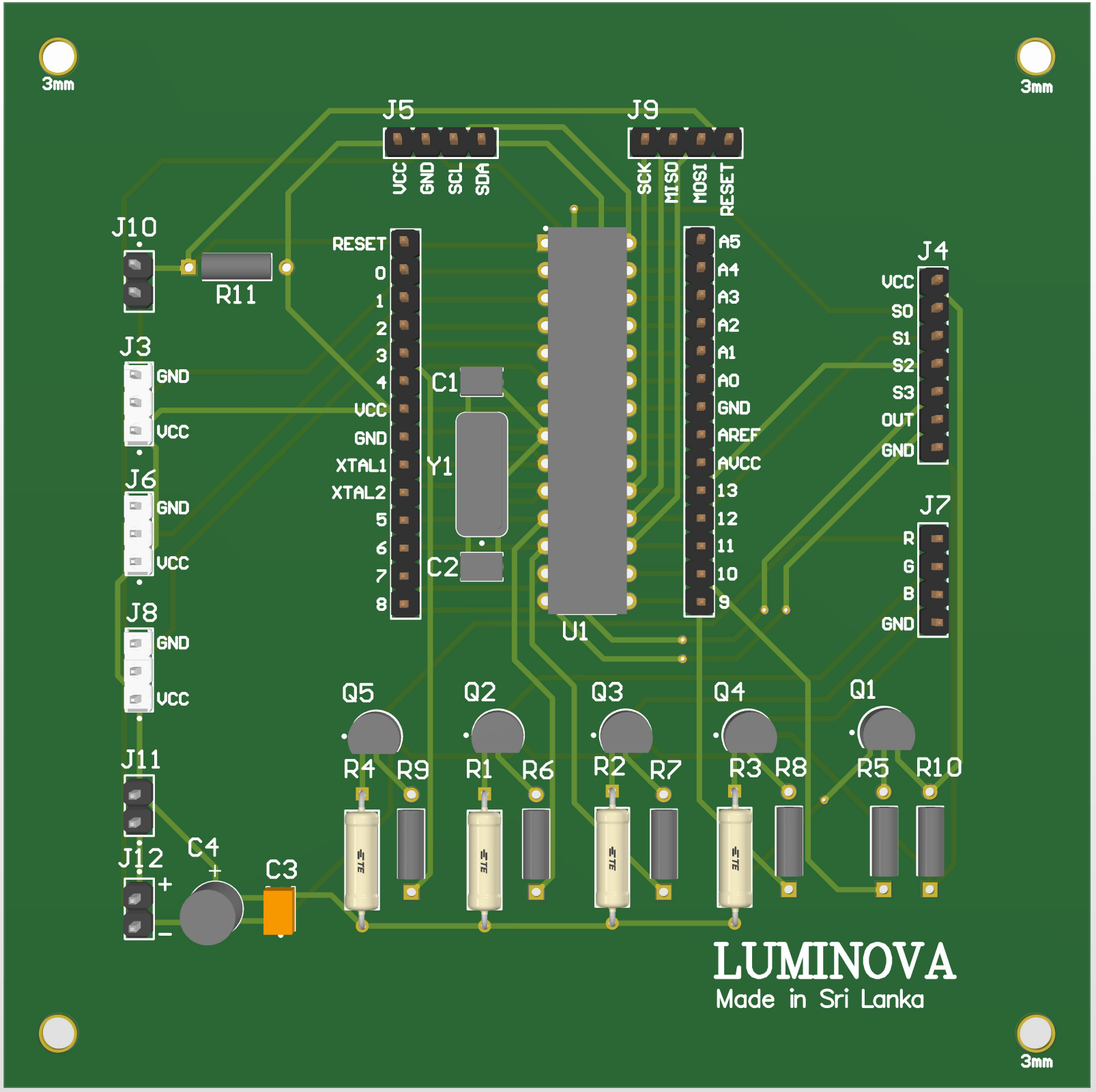
3mm
3mm
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J5
VCC
GND
SCL
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SCK
MISO
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RESET
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Y1
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VCC
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S1
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OUT
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GND
Q5
Q2
Q3
Q4
Q1
R4
R9
R1
R6
R2
R7
R3
R8
R5
R10
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