

Amrita Vishwa Vidyapeetham
B.Tech. Degree CSE (AI) First Semester
Computational Engineering and Networking

19AIE101-Elements of Computing Systems-1 : Written Assignment 4 (Group-wise)

Max marks: 100 (Including viva)

This evaluation is related to the following course outcome

CO2 Implement different combinational and sequential digital logic systems

CO3 Design the hardware hierarchy of general-purpose computing systems

**Qn
No**

Question

- 1 Design the program counter (PC) chip. Implement the chip logic designed in HDL and test the chip using a hardware simulator (Use script-based simulation).
- 2 Design a digital system which checks if two 2-bit inputs are equal, and one of the input is greater than other input and vice versa

Instructions: Design means you should draw the block schematic and explain the working. The answer can be uploaded as word file or PDF or PPT (Don't upload HDL file). Show the HDL code and output combination as screenshot inside the word or PDF or PPT .You can upload the file one time only. So kindly ensure that you are uploading the correct file or not. The first page of the file should contain your identity information. Plagiarism will not be entertained and if found, the answer will not be evaluated.

Link to upload word/PDF/PPT (One member in the team can upload. Do not upload zip folder)

https://amritavishwavidyapeetham-my.sharepoint.com/personal/g_jyothishlal_cb_amrita_edu/_layouts/15/onedrive.aspx?p=26&s=aHR0cHM6Ly9hYXJpdGF2aXNod2F2aWR5YXBlZXRoYW0tbXkuc2hhcmVwb2ludC5jb20vOmY6L2cvcGVyc29uYWwvZ19geW90aGlzaGxhbF9jY9hbXJpdGFfZWRR1L0VqMTQxRUUVwM1ExT2tMc0tsMTNoUXRvQlk0cjdQeXRyTldCNkhPQjNpYkFFMIE