| | Mentor and TA | Mentor and TA | Mentor and TA | Mentor and TA | Mentor and TA |
|---------------------|---|--|--|---|---|
| Name | Mr Varma | Mr Dhayal | Ms Skanda | Ms Sameera | Mr Zubair |
| | coe16d001@iiitdm.ac.in | edm17d002@iiitdm.ac.in | edm17d001@iiitdm.ac.in | coe18d002@iiitdm.ac.in | coe20d003@iiitdm.ac.in |
| DESIGN ACTIVITY1 | Write a perl or python or any scripting language based CMOS Compound Gate Design. Input to your code is compound gate based expresssion and output is compound gate stucture. | Write a perl or python or any scripting language based CMOS Compound Gate Design. Input to your code is compound gate based expresssion and output is compound gate stucture. | Write a perl or python or any scripting language based CMOS Compound Gate Design. Input to your code is compound gate based expresssion and output is compound gate stucture. | Write a perl or python or any scripting language based CMOS Compound Gate Design. Input to your code is compound gate based expresssion and output is compound gate stucture. | Write a perl or python or any scripting language based CMOS Compound Gate Design. Input to your code is compound gate based expresssion and output is compound gate stucture. |