Name	**Mentor and TA**	**Mentor and TA**	**Mentor and TA**	**Mentor and TA**	**Mentor and TA**
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DESIGN ACTIVITY2	16-bit Kogge-stone Adder design using verilog HDL	16-bit Brent-Kung Adder design using verilog HDL	16-bit Lander-Fisher Adder design using verilog HDL	16-bit Han-Carlson Adder design using verilog HDL	16-bit Slansky Adder design using verilog HDL