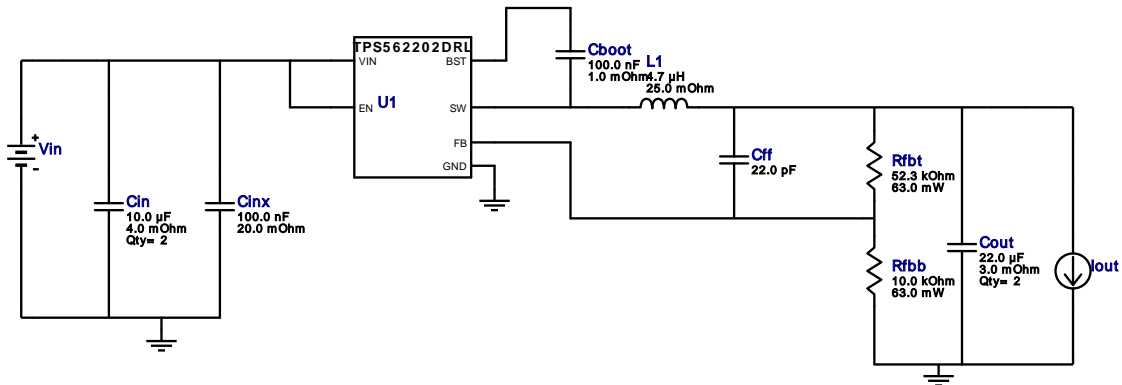


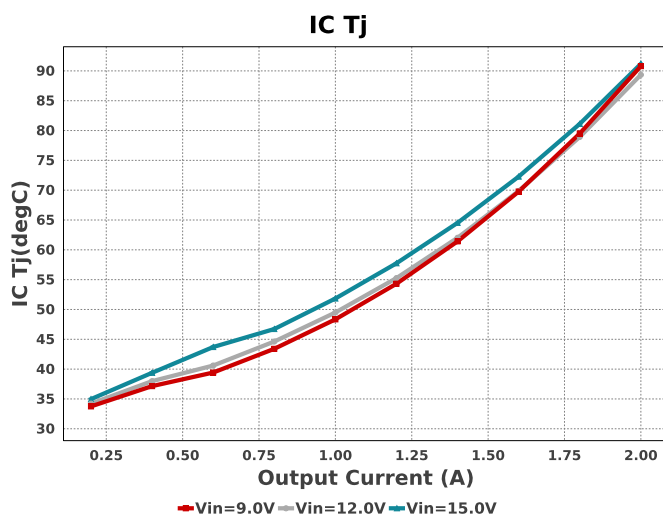
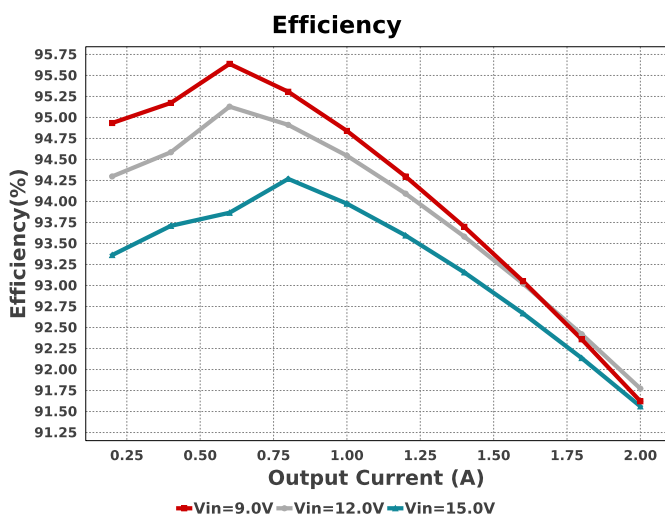
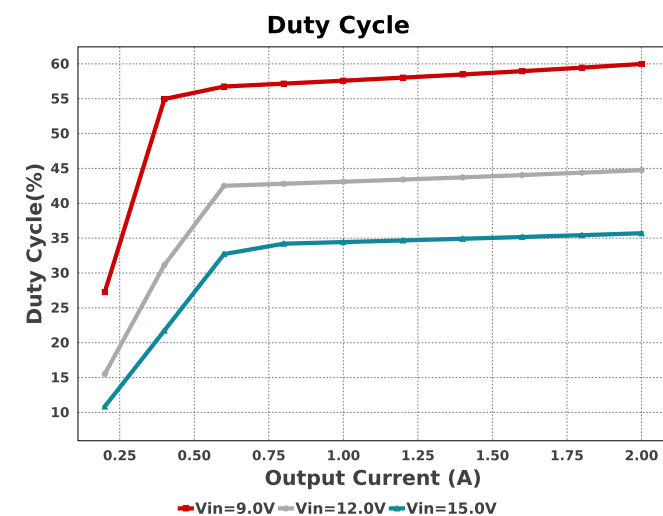
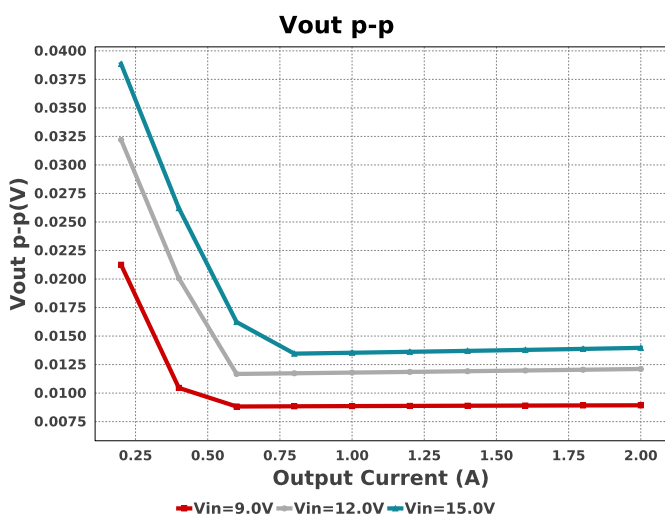
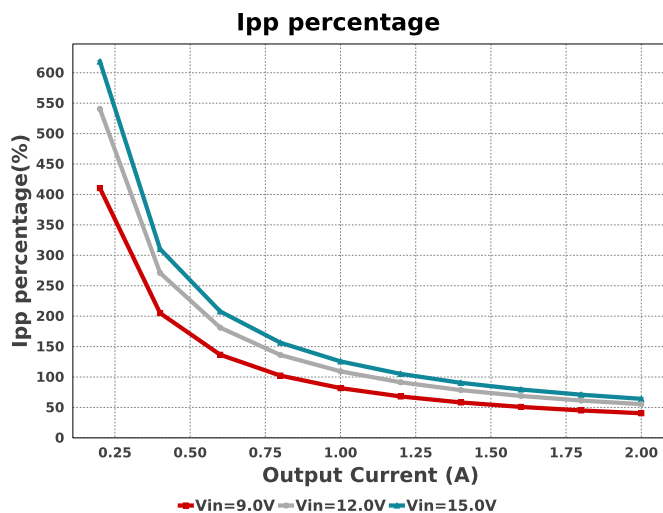
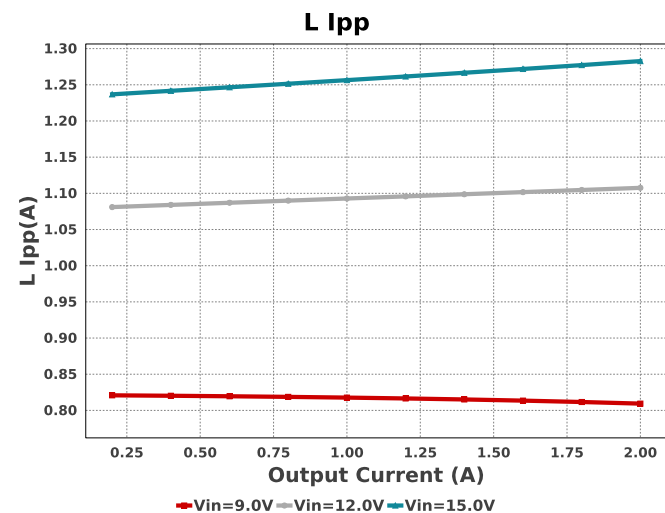
WEBENCH® Design Report

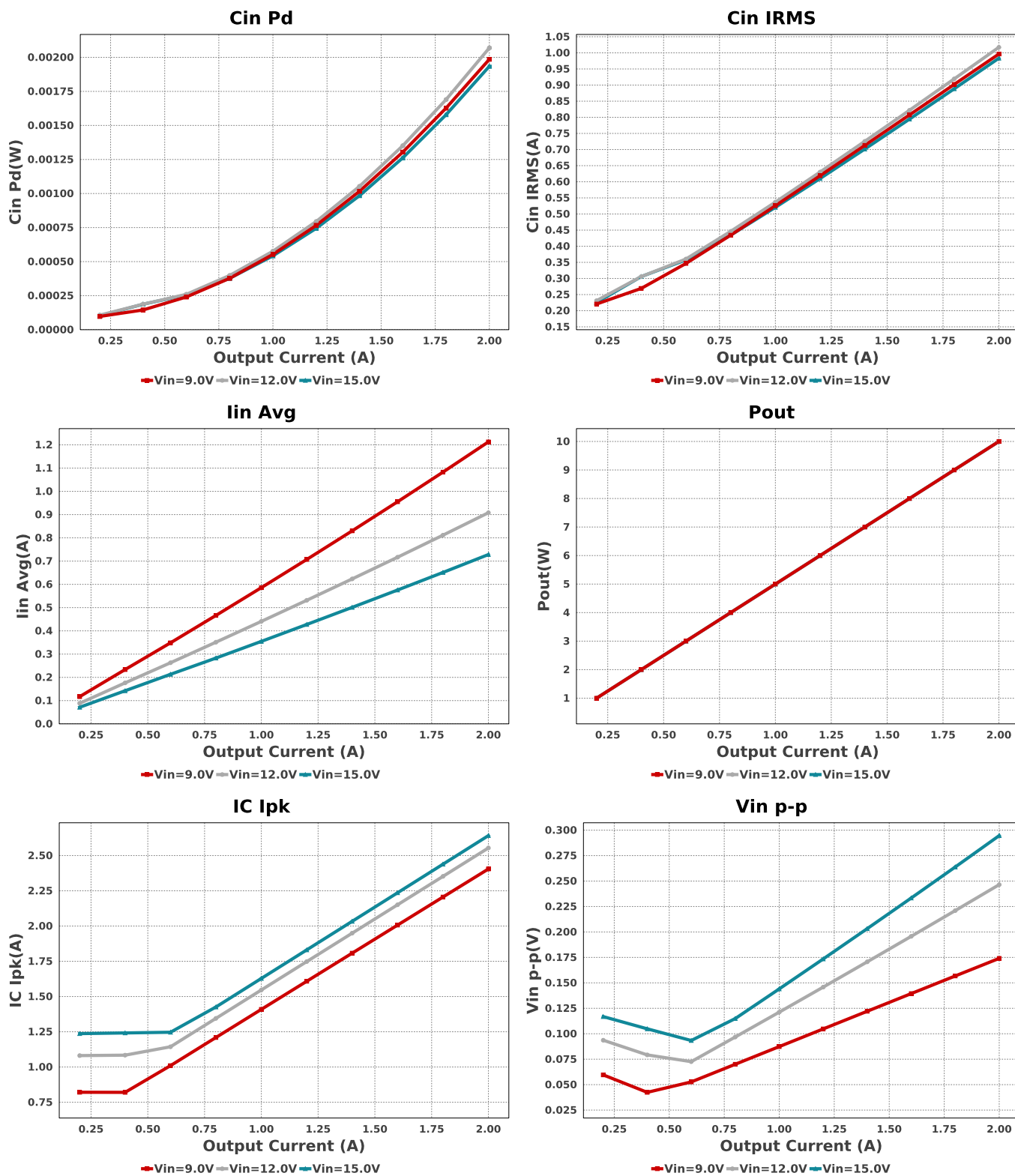
Design : 28 TPS562202DRLR
TPS562202DRLR 9V-15V to 5.00V @ 2A

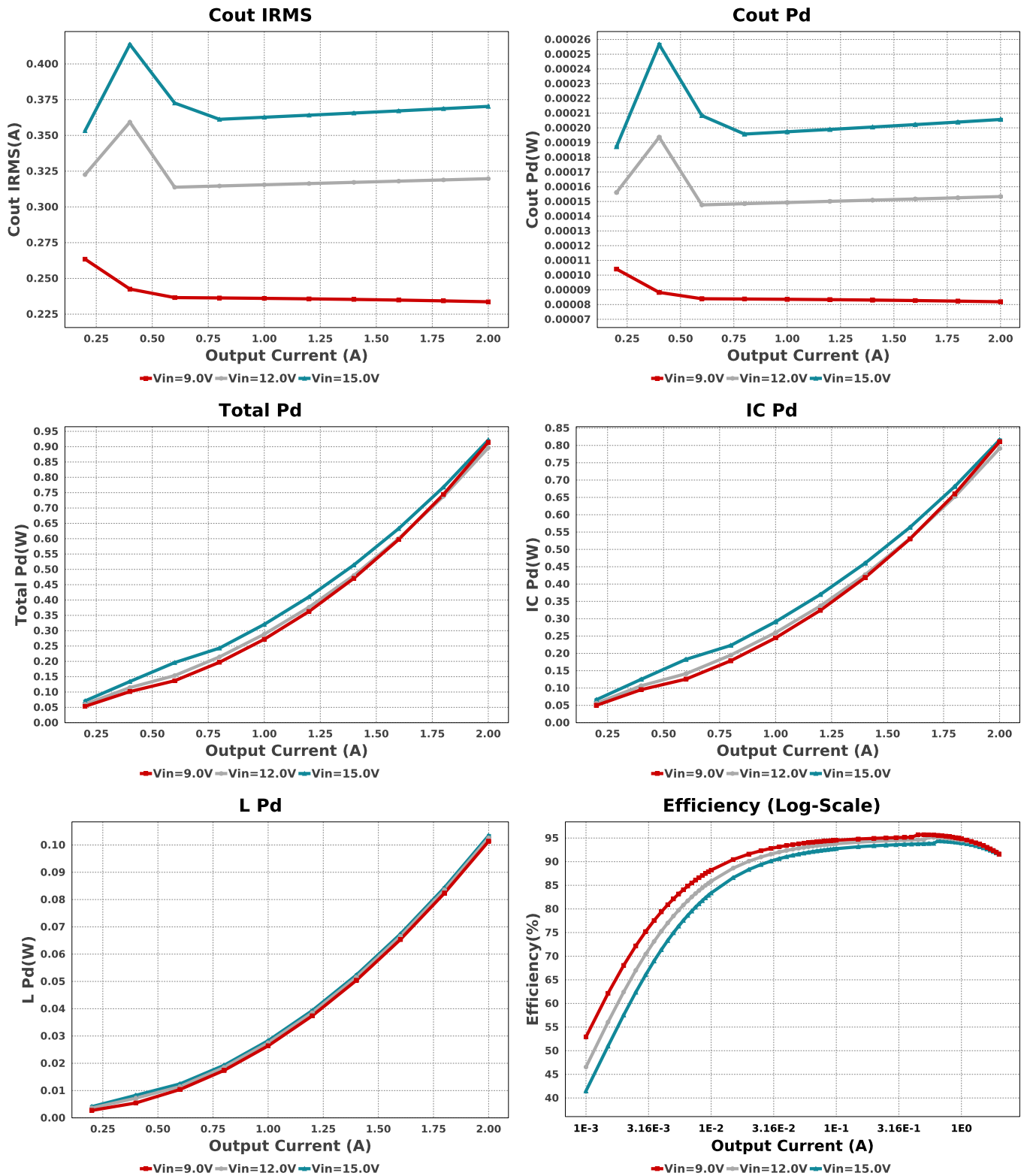


Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cff	MuRata	GRM0225C1C220JD05L Series= C0G/NP0	Cap= 22.0 pF VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	01005 2 mm ²
Cin	MuRata	GRM21BR61E106MA73L Series= X5R	Cap= 10.0 uF ESR= 4.0 mOhm VDC= 25.0 V IRMS= 2.8 A	2	\$0.04	0805 7 mm ²
Cinx	MuRata	GRM188R71H104KA93D Series= X7R	Cap= 100.0 nF ESR= 20.0 mOhm VDC= 50.0 V IRMS= 3.8 A	1	\$0.02	0603 5 mm ²
Cout	MuRata	GRM21BR61A226ME44L Series= X5R	Cap= 22.0 uF ESR= 3.0 mOhm VDC= 10.0 V IRMS= 3.84 A	2	\$0.09	0805 7 mm ²
L1	TDK	VLP8040T-4R7M	L= 4.7 uH 25.0 mOhm	1	\$0.22	VLP8040 113 mm ²
Rfbb	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rfht	Vishay-Dale	CRCW040252K3FKED Series= CRCW..e3	Res= 52.3 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
U1	Texas Instruments	TPS562202DRLR	Switcher	1	\$0.07	DRL0006A 7 mm ²







Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	983.459 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	1.934 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	370.297 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	205.68 μ W	Capacitor	Output capacitor power dissipation
5.	IC Ipk	2.641 A	IC	Peak switch current in IC
6.	IC Pd	815.69 mW	IC	IC power dissipation
7.	IC Tj	91.177 degC	IC	IC junction temperature
8.	IC Tolerance	16.0 mV	IC	IC Feedback Tolerance
9.	ICThetaJA Effective	75.0 degC/W	IC	IC junction-to-ambient thermal resistance with TI EVM
10.	Iin Avg	728.11 mA	IC	Average input current

#	Name	Value	Category	Description
11.	Ipp percentage	64.137 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
12.	L Ipp	1.283 A	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	103.43 mW	Inductor	Inductor power dissipation
14.	Cin Pd	1.934 mW	Power	Input capacitor power dissipation
15.	Cout Pd	205.68 μ W	Power	Output capacitor power dissipation
16.	IC Pd	815.69 mW	Power	IC power dissipation
17.	L Pd	103.43 mW	Power	Inductor power dissipation
18.	Total Pd	921.678 mW	Power	Total Power Dissipation
19.	BOM Count	11	System	Total Design BOM count
20.	Duty Cycle	35.703 %	System	Duty cycle
21.	Efficiency	91.561 %	System	Steady state efficiency
22.	FootPrint	163.0 mm ²	System	Total Foot Print Area of BOM components
23.	Frequency	564.358 kHz	System	Switching frequency
24.	Iout	2.0 A	System	Iout operating point
25.	Mode	CCM	System	Conduction Mode
26.	Pout	10.0 W	System	Total output power
27.	Total BOM	\$0.61	System	Total BOM Cost
28.	Vin	15.0 V	System	Vin operating point
29.	Vin p-p	294.645 mV	System	Peak-to-peak input voltage
30.	Vout	5.0 V	System	Operational Output Voltage
31.	Vout Actual	5.009 V	System	Vout Actual calculated based on selected voltage divider resistors
32.	Vout Tolerance	3.72 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
33.	Vout p-p	13.965 mV	System	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	2.0	Maximum Output Current
VinMax	15.0	Maximum input voltage
VinMin	9.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	TPS562202	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 9.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : A0BCBC578686B93F9BC7E92B45DAE2FB[v1]
2. **TPS562202** Product Folder : <http://www.ti.com/product/TPS562202> : contains the data sheet and other resources.

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