# Computational Science on Multi-Core Architectures: Exercise 2 (Tasks 1.acde and Task 2a):

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#### Task1: Basic Cuda

#### 1.a)

In Figure 1 one can see that cudaFree is definetely faster than cudaMalloc - allocation of memory seems to be more computationally expensive than freeing memory. The plot was produced with datapoints obtained from the program "basic\_cuda\_a" (see appendix). The time was averaged over 10 repeats. Note that the time was measured in each repeat instead of just measuring outside of the for-loop (no time to correct now that but at least I got smarter).

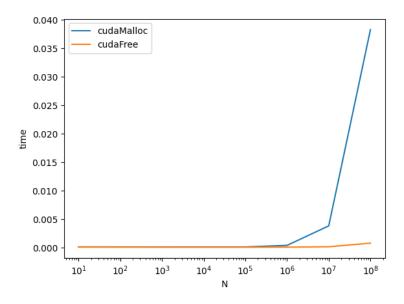


Figure 1: Time it takes to run cudaMalloc and cudaFree for different sizes of N

## 1.c) and d)

For these tasks a new program, "basic\_cuda\_cde", was created (similar to the one from 1.a)). The program loops over different values of N (here with 5 repeats) which can be seen in Figure 2. One can see that for N up to 10 to the power of 6 the curve stays almost constant and then

increases rapidly. This is due to the chosen numbers of blocks and threads per block which was  $256 \times 256$  which equals 65.536 threads in total. At roughly that number of N it starts to increase which means that at that point, some threads have to calculate more than one element in the vector.

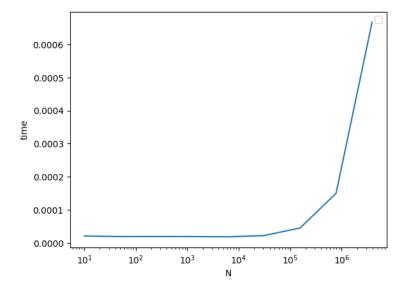


Figure 2: Time it takes to run the vector addition for different sizes of N

#### 1.e)

Here the same code as for c) and d) was used but with fixed N equal to 10 to the power of 7. In Figure 3 one can see that execution time for the vector addition decreases with a higher number of threads per block - as it should be. The number of blocks stayed the same (256) while the number of threads per block went from 16 to 1024. Even for 512x1024 and 1024x1024 (not documented in the Figure) the time did not decrease significantly anymore which means that the curve in Figure 3 would continue on a constant level.

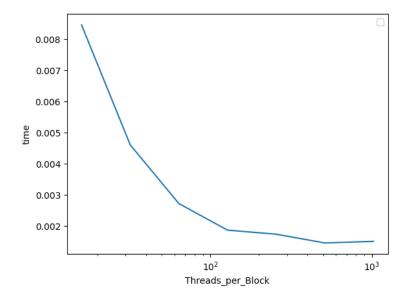


Figure 3: Time it takes to run the vector addition for different numbers of threads per block

## Task2: Dot Product

## 2.a)

One can see the plot in Figure 4 is (unsurprisingly) almost identical to the one of Figure 2. The dot product was calculated by two different kernels, one for multiplication (and summation) and one (only) for summation. In the first kernel the products of the single elements were stored into shared memories which were each shared by a whole block. These products were then summed up in each block. After that the second kernel (function dot\_sum) summed up each of the values of a whole block such that the finished dot product is received.

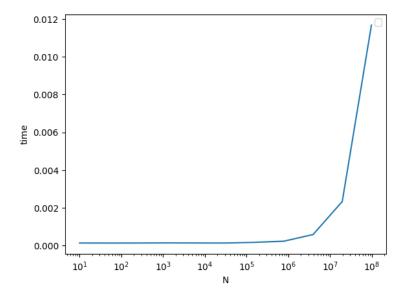


Figure 4: Time it takes to run the dot product (with two kernels) for different sizes of N

# **Appendix**

## basic cuda 1a.cu

```
#include <iostream>
   #include <vector>
   #include "timer.hpp"
   using Vector = std::vector<double>;
6
   void measure_time(int N_min, int N_max, int repeats){
     Vector time_vec_malloc;
9
     Vector time_vec_free;
10
11
     double time_malloc, time_free;
12
     double *d_x;
13
     Timer timer;
14
15
     for (int i = N_min; i <= N_max; i = i*10){
16
       std::cout << i << std::endl;
17
       time_malloc = 0;
18
       time_free = 0;
19
       for (int j = 0; j < repeats+1; j++){
20
           cudaDeviceSynchronize();
21
22
           timer.reset();
           cudaMalloc(&d_x, i*sizeof(float));
23
           time_malloc += timer.get();
           //printf("Elapsed: %g\n", time_malloc);
25
           cudaDeviceSynchronize();
26
27
           timer.reset();
           cudaFree(d_x);
28
           time_free += timer.get();
           //printf("Elapsed: %g\n", time);
30
31
32
       time_vec_malloc.push_back(time_malloc/repeats);
33
34
       time_vec_free.push_back(time_free/repeats);
35
37
     printf("malloc\n");
38
     for (const auto& value : time_vec_malloc){
39
       printf("%g\n", value);
40
41
     printf("\nfree\n");
42
     for (const auto& value : time_vec_free){
43
       printf("%g\n", value);
44
45
46
47
   int main(){
49
     measure_time(10, 100000000, 10);
50
51
52
     return EXIT_SUCCESS;
   }
53
```

## basic\_cuda\_1cde.cu

```
#include <iostream>
2
   #include <stdio.h>
3
   #include <vector>
4
   #include "timer.hpp"
   using Vector = std::vector<double>;
9
   __global__ void addition(int n, double *x, double *y, double *z) {
10
11
12
     int id = blockIdx.x*blockDim.x + threadIdx.x;
     for (size_t i = id; i < n; i += blockDim.x*gridDim.x){
13
        z[i] = x[i] + y[i];
14
15
16
17
   int main(void)
18
19
20
        double *x, *y, *z, *d_x, *d_y, *d_z;
21
22
        Vector time_vec;
23
        double time;
        Timer timer;
25
26
        int N_min = 10000000;
27
        int N_max = 10000000;
28
29
        int repeats = 5;
        for (int N = N_min; N <= N_max; N = N*5){</pre>
30
            std::cout << N << std::endl;</pre>
31
            time = 0;
32
33
            // REPEAT
34
            for (int j = 0; j < repeats; j++){
35
36
37
                // Allocate host memory and initialize
                x = (double*)malloc(N*sizeof(double));
38
39
                y = (double*)malloc(N*sizeof(double));
                z = (double*)malloc(N*sizeof(double));
40
41
                for (int i = 0; i < N; i++) {
42
43
                     x[i] = i;
                     y[i] = N-1-i;
44
                     z[i] = 0;
45
                }
46
47
48
                // Allocate device memory and copy host data over
                cudaMalloc(&d_x, N*sizeof(double));
49
                cudaMalloc(&d_y, N*sizeof(double));
50
51
                cudaMalloc(&d_z, N*sizeof(double));
52
                \verb"cudaMemcpy" (d_x, x, N*size of (double), cudaMemcpyHostToDevice)";
53
                \verb|cudaMemcpy|(d_y, y, N*sizeof(double), cudaMemcpyHostToDevice);|\\
54
55
                cudaMemcpy(d_z, z, N*sizeof(double), cudaMemcpyHostToDevice);
56
                cudaDeviceSynchronize();
57
58
                timer.reset();
59
60
                // MEASURING TIME FROM HERE
```

```
61
                     addition << <256, 1024>>>(N, d_x, d_y, d_z);
62
63
                     // TO HERE
64
                     cudaDeviceSynchronize();
                     time += timer.get();
66
                     //printf("Elapsed: %g\n", time);
67
68
                     cudaMemcpy(z, d_z, N*sizeof(double), cudaMemcpyDeviceToHost);
69
70
                     // CHECK IF VALUES OF z ARE EQUAL (fast check on first two values)
71
                     if (z[1] != z[2]){
72
                          \mathtt{std} :: \mathtt{cout} \; \mathrel{<<} \; \tt{"Values} \sqcup \mathtt{of} \sqcup \mathtt{z} \sqcup \mathtt{are} \sqcup \mathtt{not} \sqcup \mathtt{equal} \; ! \; ! \; ! \; \mathrel{<<} \; \mathtt{std} \; :: \; \mathtt{endl} \; ;
73
74
75
                     cudaFree(d_x);
76
                     cudaFree(d_y);
                     cudaFree(d_z);
78
                     free(x);
79
                     free(y);
80
                     free(z);
81
83
               time_vec.push_back(time/repeats);
84
85
86
87
88
          printf("Times:\n");
          for (const auto& value : time_vec){
90
               std::cout << value << std::endl;</pre>
91
92
93
95
96
          return EXIT_SUCCESS;
    }
97
```

#### dot product 2a.cu

```
#include <iostream>
   #include <vector>
2
   #include "timer.hpp"
   using Vector = std::vector<double>;
   const int threadsPerBlock = 256;
   // KERNEL 2
   __global__ void dot_partial(double *x, double *y, double *z_partial, int n)
10
11
            __shared__ double temp_array[threadsPerBlock]; // shared for both kernels
12
           double product = 0;
13
14
           int id = blockIdx.x * blockDim.x + threadIdx.x;
           for (int i = id; i < n; i += blockDim.x * gridDim.x)</pre>
15
           {
16
                    product += x[i] * y[i];
17
18
           // add results as long as i < size of vectors
           temp_array[threadIdx.x] = product;
20
```

```
21
            // reduce in each block
22
            for (int i = blockDim.x / 2; i > 0; i = i / 2)
23
24
                     __syncthreads(); // sync threads in block
26
                     // add elements
27
                     if (threadIdx.x < i)</pre>
28
                     {
29
                              temp_array[threadIdx.x] += temp_array[threadIdx.x + i];
30
                     }
31
32
            // let only thread 0 write to memory
33
            if (threadIdx.x == 0)
34
            {
35
                     z_partial[blockIdx.x] = temp_array[0];
36
            }
37
38
   // KERNEL 1
39
   __global__ void dot_sum(double *z_partial)
40
41
            for (int i = blockDim.x / 2; i > 0; i = i / 2)
42
            {
43
                     __syncthreads();
44
45
                     if (threadIdx.x < i)</pre>
46
                              z_partial[threadIdx.x] += z_partial[threadIdx.x + i];
47
            }
48
49
50
   int main(void)
51
52
53
            double *x, *y, *z, *d_x, *d_y, *d_z_partial;
            Vector time_vec;
55
56
            Timer timer;
57
            int N_{min} = 10;
58
            int N_max = 100000000;
59
            int repeats = 10;
60
61
            for (int N = N_{min}; N \le N_{max}; N = N * 5)
62
            {
63
                     std::cout << N << std::endl;</pre>
64
                     // Allocate host memory and initialize
65
                     z = (double *)malloc(threadsPerBlock * sizeof(double));
                     x = (double *)malloc(N * sizeof(double));
67
                     y = (double *)malloc(N * sizeof(double));
68
69
                     for (int i = 0; i < N; i++)
70
71
                              x[i] = 1;
72
73
                              y[i] = 1;
                     }
74
75
                     \ensuremath{//} Allocate device memory and copy host data over
76
77
                     cudaMalloc(&d_x, N * sizeof(double));
                     cudaMalloc(&d_y, N * sizeof(double));
                     \verb|cudaMalloc(\&d_z_partial, threadsPerBlock* size of (double));|\\
79
80
                     cudaMemcpy(d_x, x, N * sizeof(double), cudaMemcpyHostToDevice);
81
                     cudaMemcpy(d_y, y, N * sizeof(double), cudaMemcpyHostToDevice);
82
```

```
// cudaMemcpy(d_z, z, N*sizeof(double), cudaMemcpyHostToDevice);
83
84
                      \ensuremath{//} better to measure time over whole loop than inside
85
                      // MEASURING TIME FROM HERE
86
                      timer.reset();
                      for (int n = 0; n < repeats; n++)
88
89
                               dot_partial <<<256, threadsPerBlock>>>(d_x, d_y, d_z_partial, N);
90
91
                               cudaDeviceSynchronize();
92
                               dot_sum <<<1, threadsPerBlock>>>(d_z_partial);
93
94
                               \verb|cudaMemcpy(z, d_z_partial, threadsPerBlock* size of (double)|, cudaMemcpyDevice|| \\
95
                      }
96
                      time_vec.push_back(timer.get() / repeats);
97
                      // TO HERE
98
                      cudaFree(d_x);
100
                      cudaFree(d_y);
101
                      cudaFree(d_z_partial);
102
                      free(x);
103
                      free(y);
104
                      free(z);
105
             }
106
             printf("Times:\n");
107
             for (const auto &value : time_vec)
108
             {
109
                      std::cout << value << std::endl;</pre>
110
             }
111
112
             return EXIT_SUCCESS;
113
114
```