

**Bootstrap Capacitor**  
 source: <https://www.onsemi.com/pub/Collateral/AN-6076.pdf>  
 $\Delta(V_{boot}) = V_{dd} - V_f - V_{gmin}$   
 $\Delta(V_{boot}) = 17V - (0.22 - 0.45V) - 10V = 6.55 - 6.78V$   
 (10V for min  $R_{dsn}$ )  
 (CUS08F30, H3F for schottky diode)

$$Q_{total} = Q_{gate} + (I_{lkg} + I_{lkcap} + I_{qbs} + I_{lk} + I_{lkd}) \cdot t_{ON} + Q_{ls}$$

$$Q_{total} = (22nC \cdot 2) + (100nA + (\text{neglected for ceramic cap}) + 150\mu A + 50\mu A + 50\mu A) \cdot 25\mu s + 3nC (\text{assumption})$$

$$Q_{total} = 53.2525nC$$

$$C_{boot} = 53.2525nC / (6.55 - 6.78V) = 7.854 - 8.130nF$$

=> Minimum capacitance is 8130pF  
 => Selected 8200pF with 5% tolerance

**Gate Resistor**  
 source: <http://www.ti.com/lit/an/slla385a/slla385a.pdf?ts=1590117117714>

$$Q = \omega(L_s)/R_g$$

$$f = 20kHz \Rightarrow \omega = 2\pi f = 2\pi(20kHz) = 125663.7061 \text{ rad/s}$$

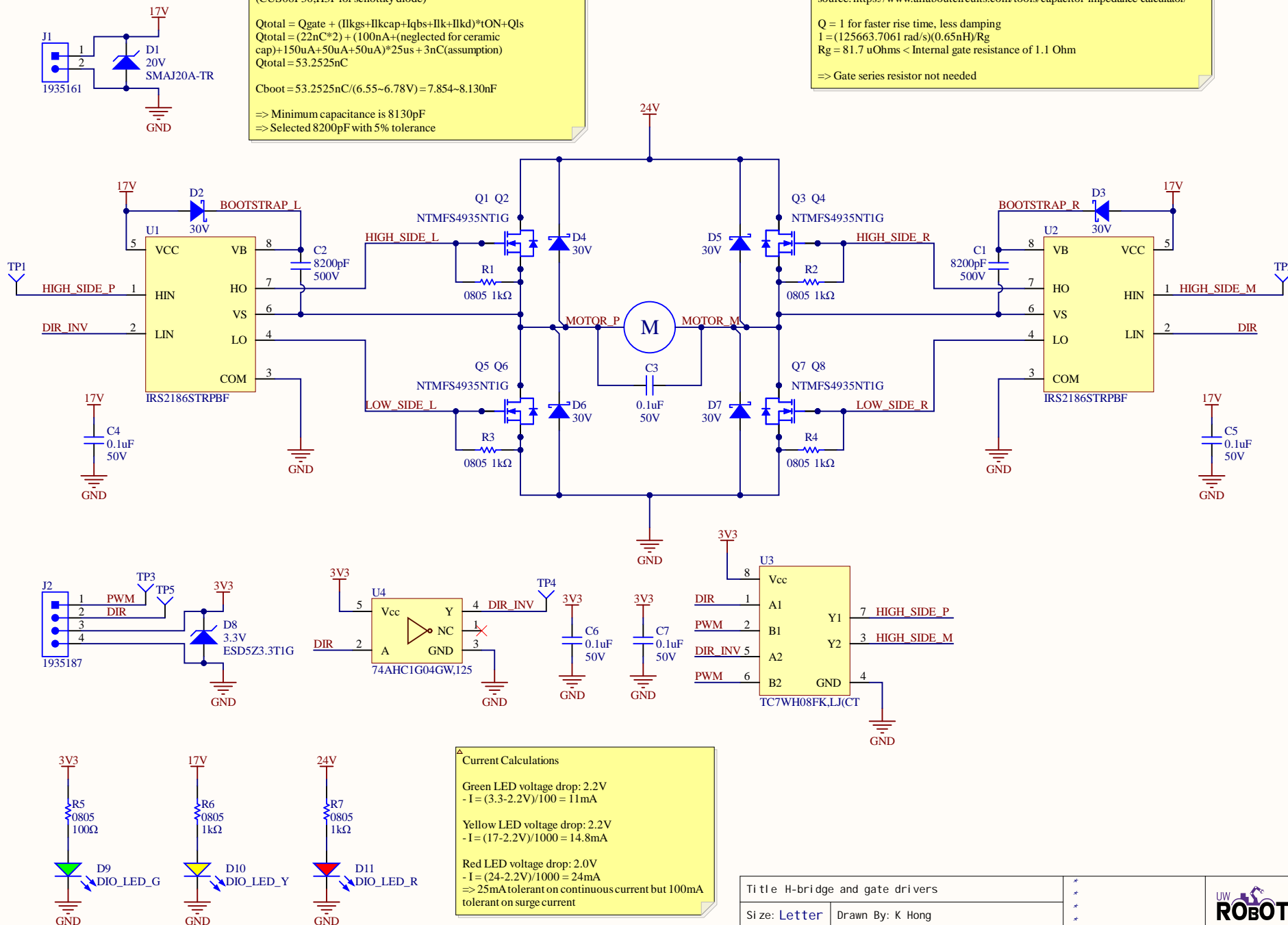
source: <https://www.allaboutcircuits.com/tools/capacitor-impedance-calculator/>

$$Q = 1 \text{ for faster rise time, less damping}$$

$$1 = (125663.7061 \text{ rad/s}) / (0.65nH/R_g)$$

$$R_g = 81.7 \mu\Omega < \text{Internal gate resistance of } 1.1 \Omega$$

=> Gate series resistor not needed



Title H-bridge and gate drivers

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Date: 5/24/2020

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