Name	yh2r9bsz.zip	Id.	40789 - QED OK
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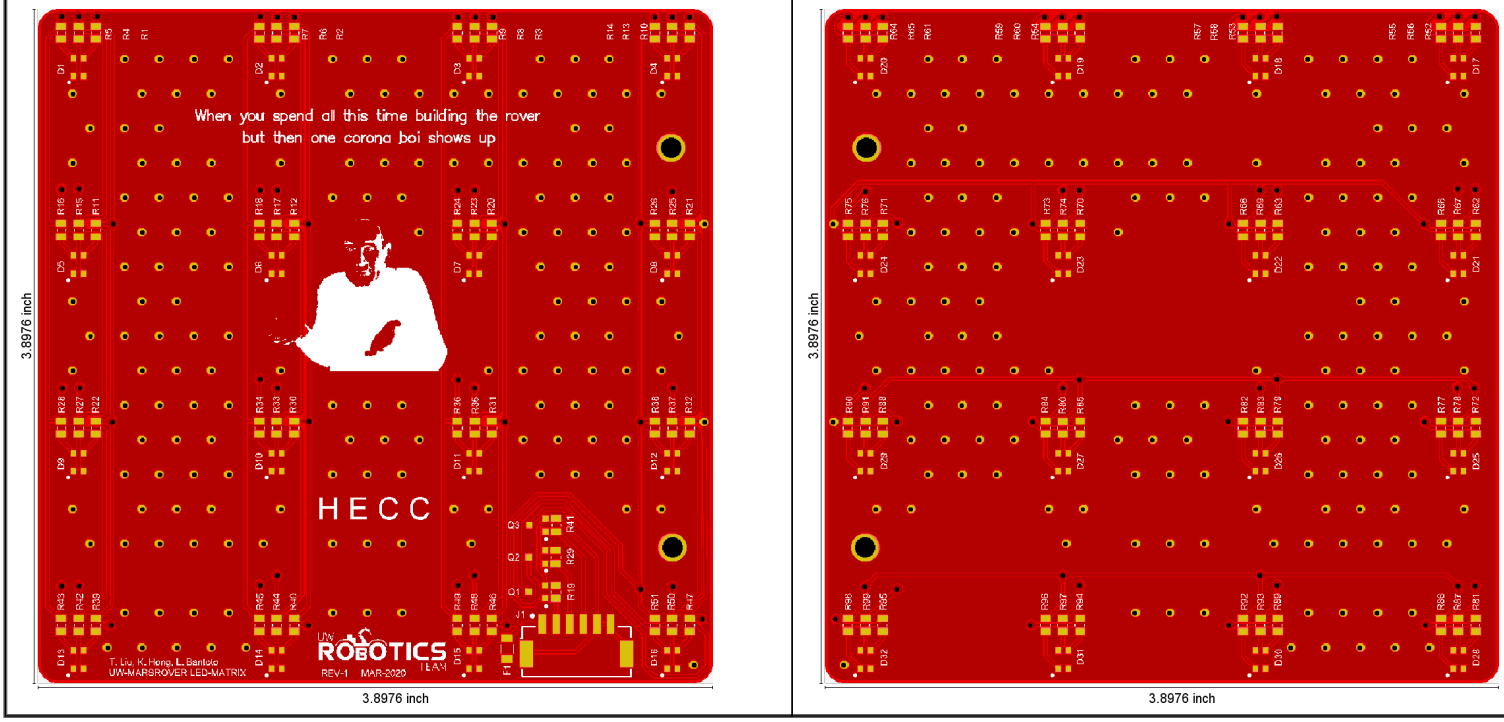
Board Id			

Single PCB View - Original

Top View	Bottom View



Summary - General - Original

PCB Size	3.8976 inch x 3.8976 inch	Surface Finish	unknown
PCB Thickness	62.00 mil	Max. Aspect Ratio on PTH	2.2
Customer Panel Size		Number of Nets	105
Copper Layers	2	Electrical Test	Double Sided
Solder Mask	Both	Drilled SMD Pads	No
Solder Mask Color	Red	SMD Pads Top	185
Legend	Both	SMD Pads Bottom	160
Legend Color	White	BGA Pads Top	0
Peeloff Mask	None	BGA Pads Bottom	0
Carbon Mask	None	Drill Hole Density	14 Holes/inch^2
Edge Connectors	No		

Summary - Copper Layer Minima - Original

Layer Type	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Trace to Trace	Same Net Clr.	Ring	Plated	Copper to NPTH Clr.	Copper to Outline Clr.
	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
Outer	1 4.96	2 7.36	3 15.00	4 15.00	5 5.99	6 8.27	7 5.99	8 10.94	9 16.99	10 25.68	11 15.06

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Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Moves	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.
			mil	mil			mil	mil	mil
PTH	1	1	28.00	28.00	206	0	10.94		16.99
NPTH	1	1	118.11	118.11	2	0	>32.00		25.68
Total	2	2	28.00	118.11	208	0	10.94		16.99

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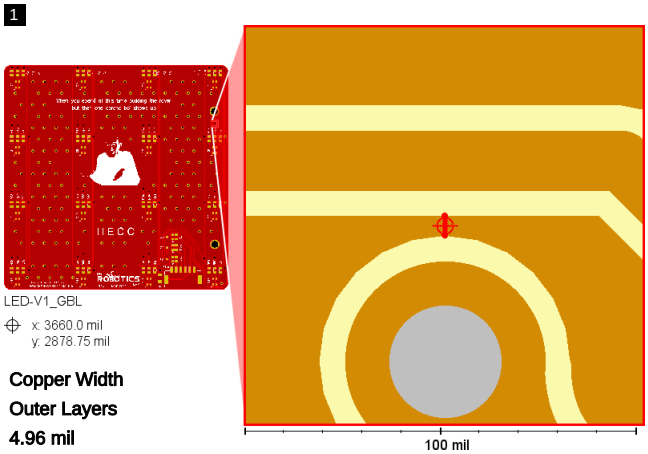
Summary Minimum Design Characteristics - Locations - Original

1



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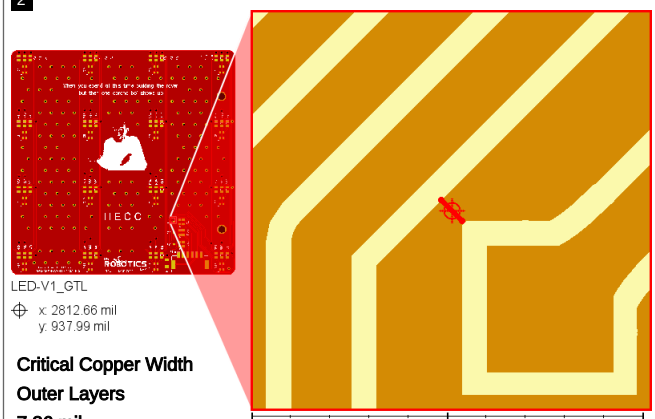

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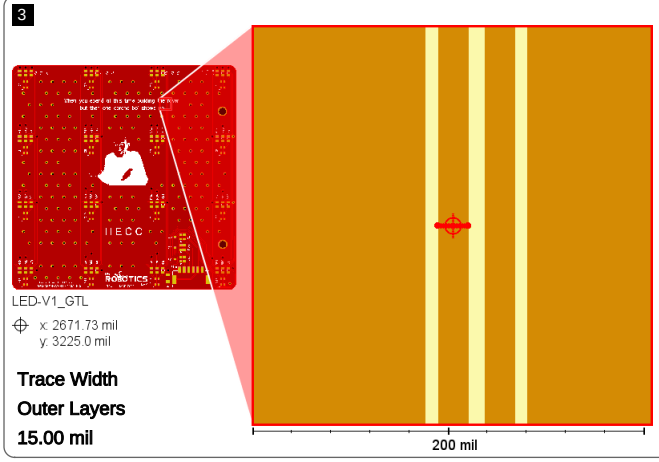

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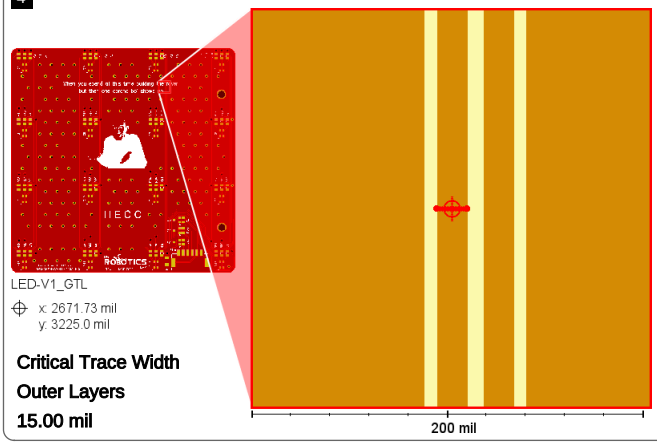

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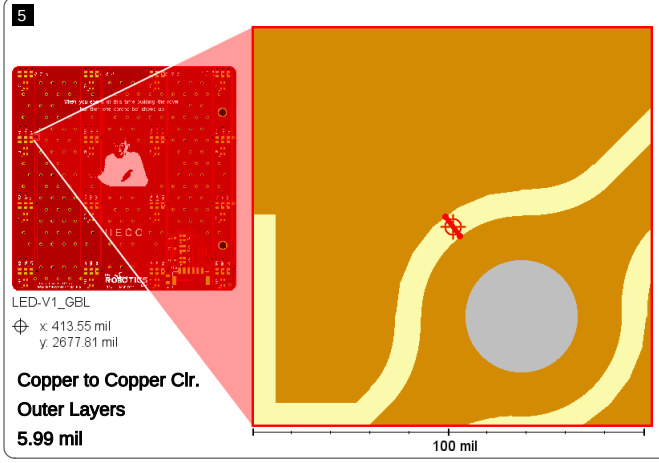

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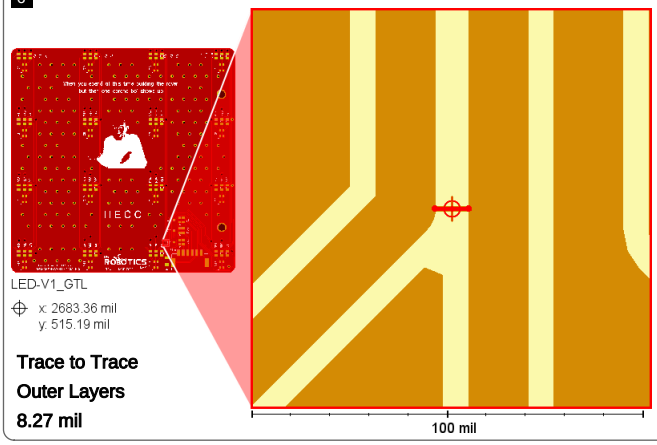

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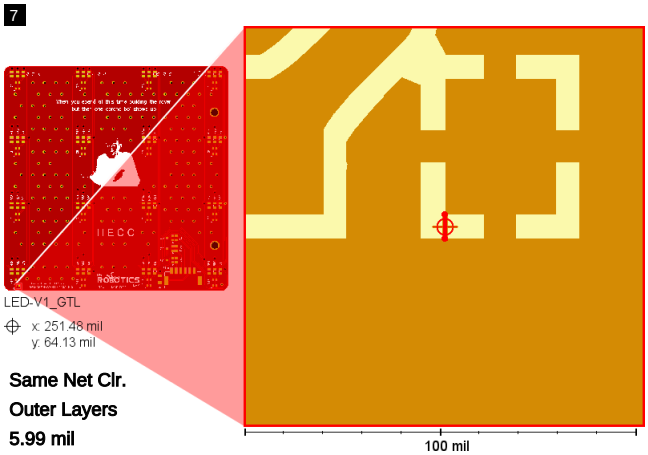
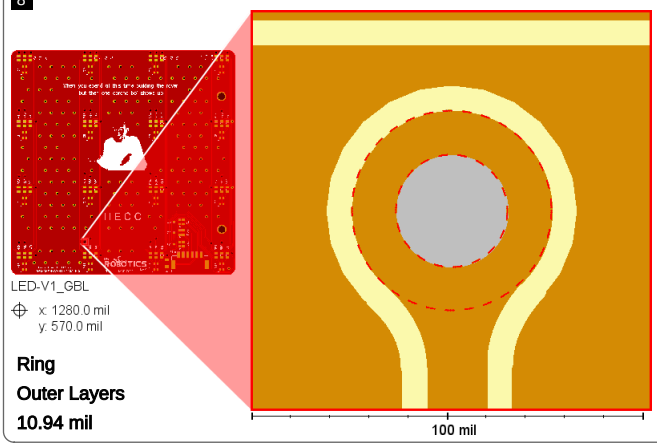
7



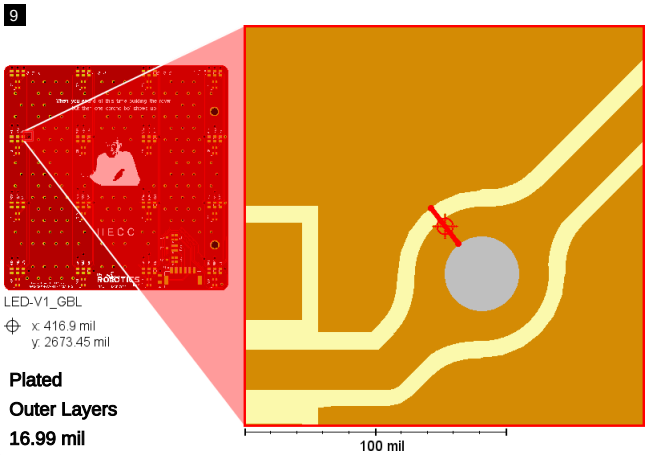
Same Net Clr.
Outer Layers
5.99 mil

8



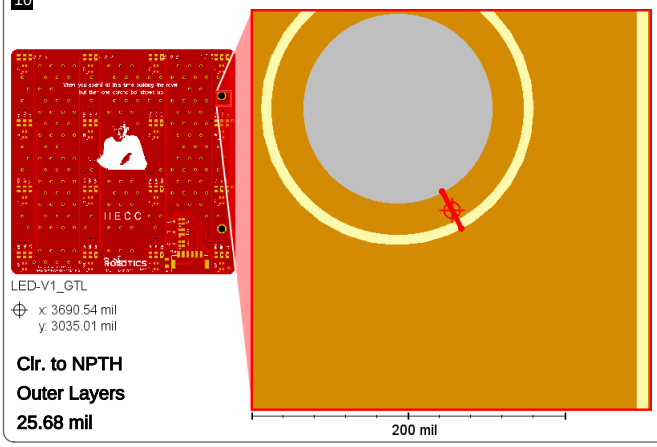
Ring
Outer Layers
10.94 mil

9



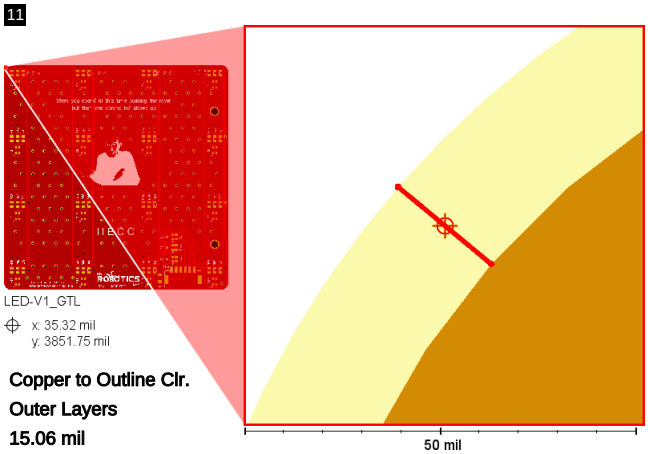
Plated
Outer Layers
16.99 mil

10



Clr. to NPTH
Outer Layers
25.68 mil

11



Copper to Outline Clr.
Outer Layers
15.06 mil

Stackup - Original

Pressing Stages	1

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Copper Layer Minima & Area - Original

File	Pos.	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Same Net Clr.	Copper Area	
		mil	mil	mil	mil	mil	mil	inch^2	%
LED-V1_GTL	1	5.18	7.36	15.00	15.00	5.99	5.99	13.8067	91
LED-V1_GBL	2	4.96	8.00	15.00	15.00	5.99	5.99	14.2427	94

Copper Layer Minima - Copper vs Drill - Original

File	Pos.	Ring					Copper vs Drill Clr.		Copper to Outline Clr.			
		Overall	Via	Laser Via	Comp.	Mech.	Plated	NPTH	Overall	Pad to Outline	Track to Outline	Region to Outline
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
LED-V1_GTL	1	10.97	10.97				16.99	25.68	15.06	16.14	24.66	15.06
LED-V1_GBL	2	10.94	10.94				16.99	25.68	15.06	16.14	32.64	15.06

Drill Tools - Original

File	Tool Nr.	Span	Type	Method	Filled Via	Counter	Dia.	Tol. Min	Tol. Plus	Holes (in PCB)	Moves (in PCB)	Double Hits (in File)	Predrill Hits (in File)
							mil	mil	mil				
LED-V1_TXT	1	1-2	PTH	unknown	unknown	unknown	28.00	0.00	0.00	206	0	0	0
LED-V1_TXT	2	1-2	NPTH	unknown	unknown	unknown	118.11	0.00	0.00	2	0	0	0

Drill Tools - Drill vs Copper - Original

File	Tool Nr.	Span	Type	Method	Dia.	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
					mil	mil	mil	mil
LED-V1_TXT	1	1-2	PTH	unknown	28.00	10.94		49.88
LED-V1_TXT	2	1-2	NPTH	unknown	118.11	>32.00		

Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.	Min. Hole to Outline Clr.	Min. Slot to Outline Clr.
			mil	mil		mil	mil	mil	mil	mil
1-2	PTH	1	28.00	28.00	206	10.94		16.99	27.14	disabled
1-2	NPTH	1	118.11	118.11	2	>32.00		25.68	177.66	disabled
All	All	2	28.00	118.11	208	10.94		16.99	27.14	disabled

Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

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Solder Mask - Original

Side	Min. Mask to Mask Clr.	Min. Web	Min. Ring on Cu Defined Pads	Min. Ring on SM Defined Pads	Min. Mask to Copper Clr.	Fully Covered Via Holes	Partly Covered Via Holes	Half Mask Vias
	mil	mil	mil	mil	mil			
Top	>10.00	>10.00	1.00	>10.00	2.66	Yes	No	
Bottom	>10.00	>10.00	2.95	>10.00	1.82	Yes	No	
Both	>10.00	>10.00	1.00	>10.00	1.82	Yes	No	No

Files - Original

Initial	Renamed	Format	Function	Position	Color
LED-V1.GTP	LED-V1_GTP	ger274x	paste	top	
LED-V1.GTO	LED-V1_GTO	ger274x	silk	top	white
LED-V1.GTS	LED-V1_GTS	ger274x	mask	top	red
LED-V1.GTL	LED-V1_GTL	ger274x	outer	1	
LED-V1.GBL	LED-V1_GBL	ger274x	outer	2	
LED-V1.GBS	LED-V1_GBS	ger274x	mask	bottom	red
LED-V1.GBO	LED-V1_GBO	ger274x	silk	bottom	white
LED-V1.GBP	LED-V1_GBP	ger274x	paste	bottom	
LED-V1.TXT	LED-V1_TXT	excellon2	mixed	1-2	
LED-V1.GM1	LED-V1_GM1	ger274x	mechanical	none	

Input Remarks - Original

Gerber import: Self-intersecting contours are detected, continuing with an interpretation of the contours. 'LED-V1.GTL' (at line 7384)

Comments - Original