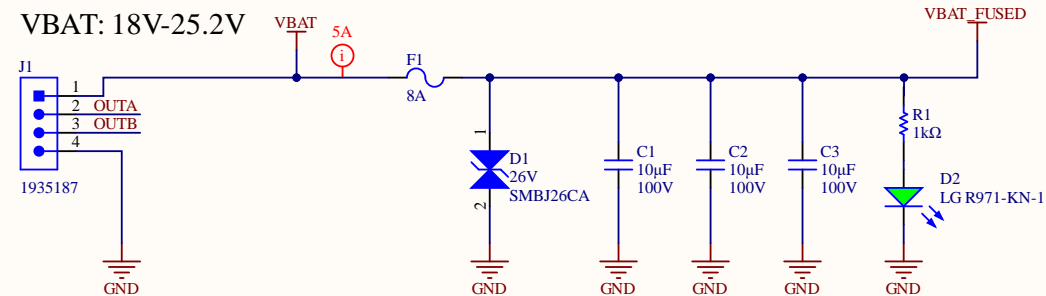


TODO: change D1 to be unidirectional



TODO: figure out how to invert DIR_IN

INA and INB can take up to VBAT

Science PCB Signals
3.3V Logic

