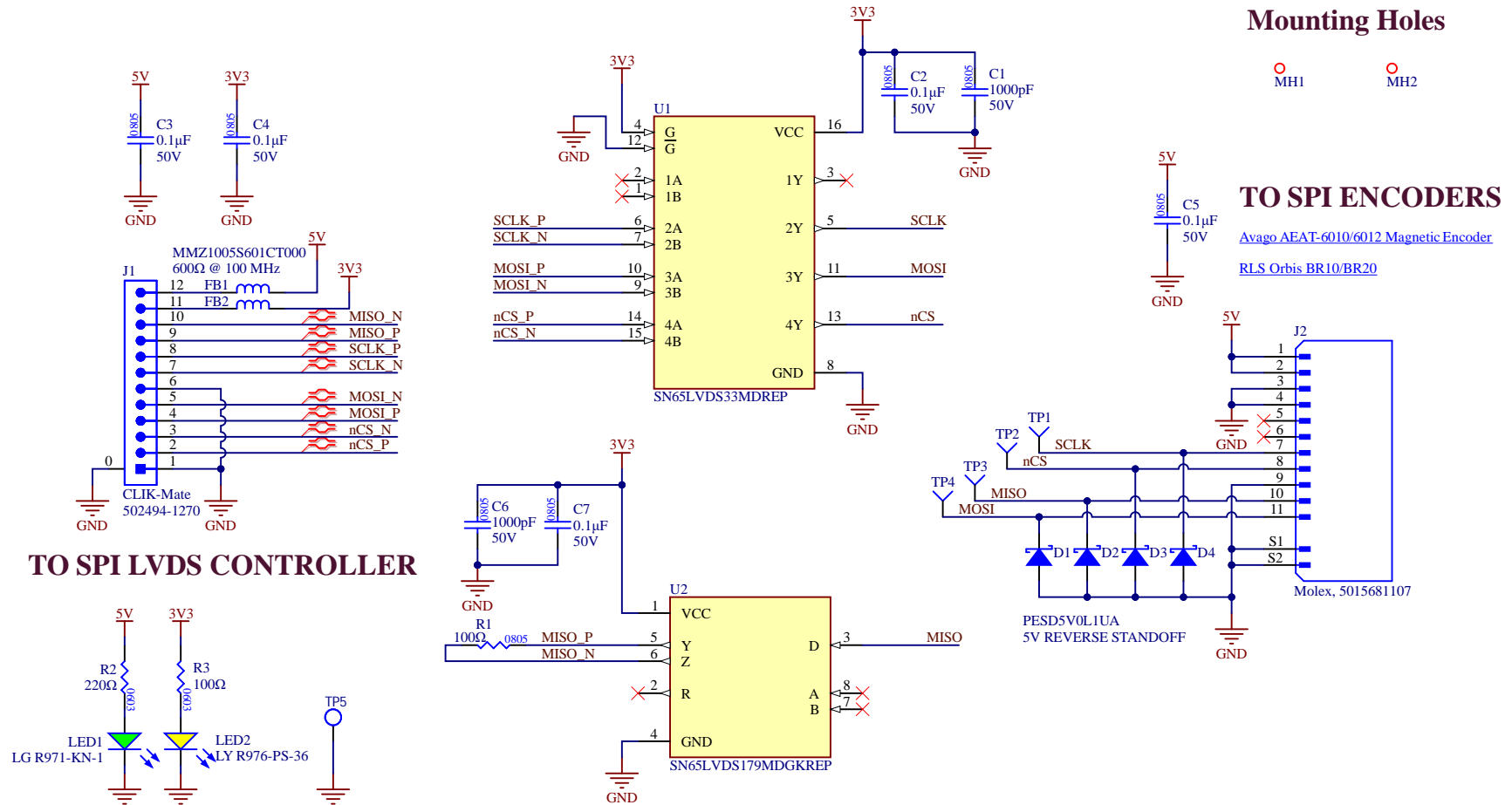


SPI LVDS Peripheral Board



ALL DIFFERENTIAL PAIRS - DIFFERENTIAL IMPEDANCE OF 100Ω

DIFFERENTIAL BUS LINES BUILT IN 12kV ESD PROTECTION

References

[Low Voltage Differential Signaling \(LVDS\) Application Notes](#)

[Transmitting SPI signals over LVDS Interface](#)

UW ROBOTICS TEAM		University of Waterloo Robotics Team 200 University Ave W Waterloo, Ontario, Canada N2L 3G1	REV
PROJECT		SPI LVDS Peripheral.PrjPcb, [No Variations]	
DOCUMENT		SPI LVDS Peripheral.SchDoc	
ENGINEER		REVIEWER	
JGao		*	
		MODIFIED	
		2022-05-04	
		SHEET 1 OF 1	

