Group 2 – Contacts Management System

**MINUTES OF MEETING**

**22-09-2022**

**Agenda**

* Created Multifile Multidirectory in Sprint Files folder
* Draft SRS doc and Fill RTM
* Divide Tasks among team members.
* Assign a person for creating the input data files and testing.
* Assign a person to take care of the daily backup
* Discuss the workflow of the project

**Action:**

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| --- | --- | --- |
| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Create Multifile Multidirectory in Sprint Files folder and add necessary files | Dasari Yashwanthi, Pravesh Jhanwar | **22-09-2022** |
| Draft SRS doc | Samson Paul M, Vivek Kumar | **22-09-2022** |
| Divide Tasks among team members. | Vivek Kumar | **22-09-2022** |
| Assign a person for creating the input data files and testing. | Dasari Yashwanthi | **22-09-2022** |
| Assign a person to take care of the daily backup | Samson Paul M, Vivek Kumar | **22-09-2022** |
| Discuss the workflow of the project | Sreehari PS, Pravesh Jhanwar | **22-09-2022** |

**24-09-2022**

**Agenda**

* Discussed the HLD, LLD, sequence, class and use case diagrams.
* Understood the implementation of the diagrams.
* Discussed the idea of diagrams with mam.
* Started to prepare the diagrams with the necessary output.
* Made sure the flow of the diagrams fits efficiently.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Discussed the HLD, LLD, sequence, class and use case diagrams. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **24-09-2022** |
| Understood the implementation of the diagrams. | Dasari Yashwanthi, Samson Paul M | **24-09-2022** |
| Discussed the idea of diagrams with mam. | Vivek Kumar | **24-09-2022** |
| Started to prepare the diagrams with the necessary output. | Sreehari PS | **24-09-2022** |
| Made sure the flow of the diagrams fits efficiently. | Pravesh Jhanwar | **24-09-2022** |

**27-09-2022**

**Agenda**

* Made the HLD, LLD, sequence, class and use case diagrams.
* Reviewed the diagrams with mam and made the necessary changes in the Class and use case diagrams as suggested by mam.
* Made sure the diagrams were exactly as suggested in the review.
* Got approval from mam for the diagrams.
* Started to discuss the implementation of the code.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Made the HLD, LLD, sequence, class and use case diagrams. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **27-09-2022** |
| Reviewed the diagrams with mam and made the necessary changes in the Class and use case diagrams as suggested by mam. | Vivek Kumar, Sreehari PS | **27-09-2022** |
| Made sure the diagrams were exactly as suggested in the review. | Vivek Kumar, Sreehari PS, Pravesh Jhanwar | **27-09-2022** |
| Got approval from mam for the diagrams. | Vivek Kumar, Sreehari PS, Pravesh Jhanwar | **27-09-2022** |
| Started to discuss the implementation of the code. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **27-09-2022** |

**28-09-2022**

**Agenda**

* Discussed and understood the implementation of the code.
* Every member started to write the basics of the code.
* Distributed and divided the parts of the code.
* Reviewed the basics of code with mam and made the necessary changes suggested by mam.
* Made sure the written code was implemented perfectly.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Discussed and understood the implementation of the code. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **28-09-2022** |
| Every member started to write the basics of the code. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **28-09-2022** |
| Distributed and divided the parts of the code. | Vivek Kumar, Sreehari PS | **28-09-2022** |
| Reviewed the basics of code with mam and made the necessary changes suggested by mam. | Vivek Kumar | **28-09-2022** |
| Made sure the written code was implemented perfectly. | Dasari Yashwanthi, Samson Paul M | **28-09-2022** |

**30-09-2022**

**Agenda**

* Continued Discussing the logic of the code.
* Discussed the logic with mam and made the necessary changes in the implementation as suggested by the mam.
* Started to implement the logic.
* Discussed the workflow of the code among each others.
* Assigned the task among each others.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Continued Discussing the logic of the code. | Dasari Yashwanthi, Pravesh Jhanwar | **30-09-2022** |
| Discussed the logic with mam and made the necessary changes in the implementation as suggested by the mam. | Vivek Kumar | **30-09-2022** |
| Started to implement the logic. | Dasari Yashwanthi, Pravesh Jhanwar | **30-09-2022** |
| Discussed the workflow of the code among each others. | Samson Paul M, Sreehari PS | **30-09-2022** |
| Assigned the task among each others. | Vivek Kumar | **30-09-2022** |

**03-10-2022**

**Agenda**

* Created classes and objects for every module.
* Used all the necessary vectors, Inheritance, and other concepts needed etc in the code.
* Created makefile and. o files for the project and debugged it using gdb command.
* Created CppUnit Files and IT testcases were checked. UT-IT document was filled.
* CUnit suites, test cases and makefile were created and debugged to clear errors.
* Modifications and addition of valid-invalid functions were done.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Created classes and objects for every module. | Vivek Kumar, Sreehari PS, Samson Paul M, Pravesh Jhanwar, Dasari Yashwanthi | **03-10-2022** |
| Creates the final reports | Samson Paul M | **03-10-2022** |
| Created CppUnit Files and IT testcases were checked. | Dasari Yashwanthi, Samson Paul M | **03-10-2022** |
| CppUnit suites, test cases and make file were created and debugged to clear errors. | Vivek Kumar , Sreehari PS,  Pravesh Jhanwar | **03-10-2022** |

**04-10-2022**

**Agenda**

* Reviewed the final code to mam and made necessary changes in Stl, inheritance and classes modules of the code as suggested.
* Reviewed the unit test cases with mam and started making the changes as suggested.
* Started implementing integration test cases.
* Filled the UT-IT document.
* Uploaded files to GitHub.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Reviewed the final code to mam and made necessary changes in Stl, inheritance and classes modules of the code as suggested. | Samson Paul M, Dasari Yashwanthi | **04-10-2022** |
| Reviewed the unit test cases with mam and started making the changes as suggested. | Vivek Kumar | **04-10-2022** |
| Started implementing integration test cases. | Pravesh Jhanwar | **04-10-2022** |
| Filled the UT-IT document | Vivek Kumar, Sreehari PS | **04-10-2022** |
| Uploaded files to GitHub. | Vivek Kumar | **04-10-2022** |

**06-10-2022**

**Agenda**

* Reviewed the updated code to mam and made again the necessary changes in inheritance and getters, setters modules of the code as suggested.
* Reviewed the updated unit test cases with mam and got approved.
* Updated the UT-IT document.
* Started to prepare presentation.

**Action:**

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| **Action Item** | **Responsibility** | **Planned Finish Date** |
| Reviewed the updated code to mam and made again the necessary changes in inheritance and getters, setters modules of the code as suggested. | Samson Paul M, Dasari Yashwanthi | **06-10-2022** |
| Reviewed the updated unit test cases with mam and got approved. | Vivek Kumar | **06-10-2022** |
| Filled the UT-IT document | Vivek Kumar, Sreehari PS | **06-10-2022** |
| Started to prepare presentation. | Vivek Kumar, Sreehari PS, Pravesh Jhanwar, Samson Paul M, Dasari Yashwanthi | **06-10-2022** |