

## SRM Institute of Science and Technology College of Engineering and Technology School of Computing

SET A

SRM Nagar, Kattankulathur – 603203, Chengalpattu District, Tamilnadu

Academic Year: 2024-25 (ODD)

Test: FJ2 Date: 01.10.2024
Course Code & Title: 21CSC202J - Operating Systems Duration: 100 Minutes
Year & Sem: II Year / III Sem Max. Marks: 50

**Course Articulation Matrix:** (to be placed)

|      | Course III | ticuinti | OII IVIALE | 114. (10 | oc pinci |     |     |     |     |     |      |      |      |      |      |      |
|------|------------|----------|------------|----------|----------|-----|-----|-----|-----|-----|------|------|------|------|------|------|
| S.No | Course     | PO1      | PO2        | PO3      | PO4      | PO5 | PO6 | PO7 | PO8 | PO9 | PO10 | PO11 | PO12 | PSO1 | PSO2 | PSO3 |
|      | Outcome    |          |            |          |          |     |     |     |     |     |      |      |      |      |      |      |
| 1    | CO1        | 3        | 3          | 2        | 2        |     |     |     |     |     |      |      | 3    |      |      |      |
| 2    | CO2        | 3        | 3          | 3        | 2        |     |     |     |     |     |      |      | 3    |      |      |      |
| 3    | CO3        | 3        | 3          | 3        | 2        |     |     |     |     |     |      |      | 3    |      |      |      |
| 4    | CO4        | 3        | 3          | 3        | 2        |     |     |     |     |     |      |      | 3    |      |      |      |
| 5    | CO5        | 3        | 2          | 3        | 2        |     |     |     |     |     |      |      | 3    |      |      |      |

# **Questions with Answer Key**

|       | Part – A  |     |    |   |    |       |
|-------|---|-----|----|---|----|-------|
|       | $(10 \times 1 = 10 \text{ Marks})$                                |     |    |   |    |       |
| Instr | uctions: Answer all   |     |    |   |    |       |
| Q.    | Answer with choice variable                                       | Ma  | BL | С | PO | PI    |
| No    |   | rks |    | 0 |    | Code  |
| 1     | Systems that permit only a single process to execute at any given | 1   | L1 | 2 | 2  | 1.2.2 |
|       | time are referred to as   | -   |    | _ | _  |       |
|       |   |     |    |   |    |       |
|       | a) uniprogramming systems   |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | b) uniprocessing systems  |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | c) unitasking systems   |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | d) multiprocessing systems  |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | Answer: b uniprocessing systems                                   |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       |   |     |    |   |    | 4     |
| 2     | The processes that are residing in main memory and are ready      | 1   | L1 | 2 | 2  | 1.5.1 |
|       | and waiting to execute are kept on a list called                  |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | a) job queue  |     |    |   |    |       |
|       |   |     |    |   |    |       |
|       | b) ready queue  |     |    |   |    |       |
|       | a) davias guaya   |     |    |   |    |       |
|       | c) device queue   |     |    |   |    |       |
|       | d) process queue  |     |    |   |    |       |
|       | (a) process queue   |     |    |   |    |       |
|       | Answer : b ready queue  |     |    |   |    |       |
|       | 71115 Wei . o ready quede   |     |    |   |    |       |
|       |   |     |    |   |    |       |

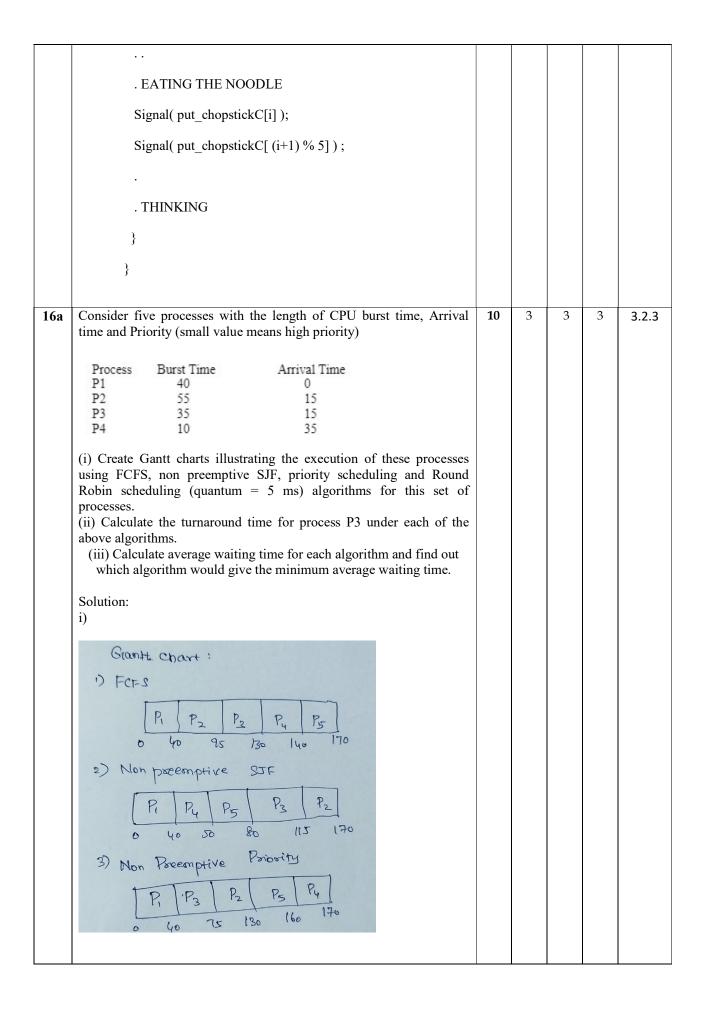
| 3 | Scheduler selects which process should be executed next and allocates CPU  | 1 | L2  | 2 | 3 | 1.6.1 |
|---|--|---|-----|---|---|-------|
|   | a. DMA controller  |   |     |   |   |       |
|   | b. CPU Scheduler   |   |     |   |   |       |
|   | c. Short Term Scheduler  |   |     |   |   |       |
|   | d. Long Term Scheduler   |   |     |   |   |       |
|   | Answer : c Short Term Scheduler  |   |     |   |   |       |
|   |  |   | T 0 |   |   | 4.7.4 |
| 4 | refers to the mechanisms that allow processes to communicate and coordinate their actions when running concurrently in an operating system   | 1 | L2  | 2 | 2 | 1.5.1 |
|   | a. Inter-process communication   |   |     |   |   |       |
|   | b. Inter-system communication  |   |     |   |   |       |
|   | c. Message communication   |   |     |   |   |       |
|   | d. Synchornization   |   |     |   |   |       |
|   | Answer: a Inter-process communication  |   |     |   |   |       |
| - | William and Alama diamondia da la damonia da   | 1 | 1.2 | 2 | 2 | 1.6.1 |
| 5 | When one thread immediately terminates the target thread, it is called   | 1 | L2  | 2 | 3 | 1.0.1 |
|   | a) Asynchronous cancellation   |   |     |   |   |       |
|   | b) Systematic cancellation   |   |     |   |   |       |
|   | c) Sudden Termination  |   |     |   |   |       |
|   | d) Deferred cancellation   |   |     |   |   |       |
|   | Ansewer: a Asynchronous cancellation   |   |     |   |   |       |
|   |  |   |     |   |   | 0.54  |
| 6 | A process is advanced to the ready state in a preemptive scheduling algorithm when:  a) A new process arrives b) A process terminates c) A process switches from running to waiting                                  | 1 | 2   | 3 | 2 | 2.5.1 |
|   | d) A process switches from waiting to ready  |   |     |   |   |       |
|   | Ans: a) A new process arrives  |   |     |   |   |       |
| 7 | What is the main difference between soft real-time systems and hard real-time systems?   | 1 | 2   | 3 | 1 | 1.6.1 |
|   | A) Soft real-time systems can miss deadlines occasionally without severe consequences, whereas hard real-time systems must meet all deadlines.  B) Hard real-time systems provide no guarantee as to when a critical |   |     |   |   |       |

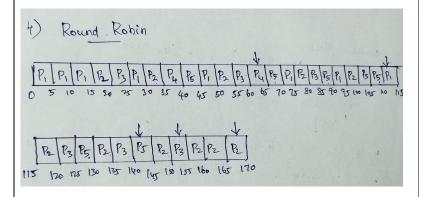
| preference over noncritical processes, whereas hard real-time systems guarantee deadline adherence.  D) Soft real-time systems and hard real-time systems have no significant differences in scheduling.  Answer: C) Soft real-time systems guarantee that processes will be given preference over noncritical processes, whereas hard real-time systems guarantee deadline adherence. | 1  |       |          |       |
|--|----|-------|----------|-------|
| significant differences in scheduling.  Answer: C) Soft real-time systems guarantee that processes will be given preference over noncritical processes, whereas hard real-time   | 1  |       |          |       |
| Answer: C) Soft real-time systems guarantee that processes will be given preference over noncritical processes, whereas hard real-time   | 1  |       |          |       |
|  | 1  |       |          |       |
|  | 1  | -     |          |       |
| 8 In a system with multiple processes and resources, a deadlock 1  |    | 3     | 1        | 1.3.1 |
| detection algorithm primarily checks for   |    |       |          |       |
| A. The maximum number of resources each process can request.  B. Cycles in the resource allocation graph.  |    |       |          |       |
| C. The order of resource requests.   |    |       |          |       |
| D. The time taken by each process.   |    |       |          |       |
| Answer: B) Cycles in the resource allocation graph.  |    |       |          |       |
| 9 Select the type of CPU Scheduling algorithm that has a large average waiting time?   | 1  | 3     | 1        | 1.3.1 |
| a) First come First Serve (FCFS)   |    |       |          |       |
| b) Priority based Scheduling Algorithm   |    |       |          |       |
| c) Round Robin Scheduling Algorithm (RR)   |    |       |          |       |
| d) Multilevel Feedback Queue Algorithm Answer:c) Round Robin Scheduling Algorithm (RR)   |    |       |          |       |
| Allswer.e) Round Room Scheduling Algorithm (RR)  |    |       |          |       |
| 10 Only the process executing the critical section is allowed to 1   | 1  | 1 3L2 | 3        | 1.2.1 |
| access the shared variable and all other processes should be   |    |       |          |       |
| prevented from doing so until the completion of the critical section. This is known as   |    |       |          |       |
| a) Inter-process Communication   |    |       |          |       |
| b) Semaphore   |    |       |          |       |
| c) Deadlock d) Mutual Exclusion  |    |       |          |       |
| Answer: d) Mutual Exclusion  |    |       |          |       |
| Part – B   |    |       | <u> </u> |       |
| $(4 \times 5 = 20 \text{ Marks})$  |    |       | 1 -      | 1     |
| What are the states undergone by a process when a user opens more  | L2 | 2     | 2        | 2.6.2 |
| than one application in a laptop at the same time? Illustrate the same   |    |       |          |       |
| using suitable diagrams.   |    |       |          |       |
| Answer:  |    |       |          |       |
| As a process executes, it changes state  |    |       |          |       |
| new: The process is being created  |    |       |          |       |
| running: Instructions are being executed   |    |       |          |       |
| waiting: The process is waiting for some event to occur  |    |       |          |       |
| ready: The process is waiting to be assigned to a processor  |    |       |          |       |
| terminated: The process has finished execution   |    |       |          |       |

| 12 | Explain the role of semaphore in achieving mutual exclusion. Answer: A semaphore is a more general synchronization primitive than a mutex, capable of controlling access to a resource pool. A binary semaphore (also called a mutex semaphore) functions similarly to a mutex, while a counting semaphore can allow multiple threads to access a finite number of instances of a resource.  Basic Steps:  1. Initialize the Semaphore: Set the initial count of the semaphore. 2. Wait (P) Operation: Decrement the semaphore count. If the count is zero, the thread is blocked. 3. Signal (V) Operation: Increment the semaphore count. If the count is positive, a waiting thread is unblocked.  Advantages:  • Can handle more complex synchronization scenarios, such as allowing a limited number of threads to access a resource concurrently.  • Flexible and powerful for various synchronization problems. Disadvantages:  • More complex to use correctly compared to mutexes.  • Prone to issues like deadlocks, priority inversion, and semaphore leaks if not managed properly. | 5 | L2 | 2 | 2 | 2.6.2 |
|----|--|---|----|---|---|-------|
| 13 | Enumerate the necessary conditions for a deadlock to occur.  Answer:  1. Mutual Exclusion: At least one resource must be held in a non-shareable mode. That is, only one process can use the resource at any given time. If another process requests that resource, the requesting process must be delayed until the resource has been released.  2. Hold and Wait: A process must be holding at least one resource and waiting to acquire additional resources that are currently being held by other processes. This means processes must be able to hold resources while waiting for others.  3. No Preemption: Resources cannot be forcibly taken from a process holding them. A resource can only be released voluntarily by the process holding it after that process has completed its task.  4. Circular Wait: There must be a circular chain of processes such that each process holds at least one resource needed by the next process in the chain. This implies that there is a cycle  | 5 | 2  | 3 | 1 | 1.2.1 |

|     | in the resource allocation graph  |    |    |   |   |       |
|-----|---|----|----|---|---|-------|
|     | C 1   |    |    |   |   |       |
| 14  | Consider four processes with the length of CPU burst time and arrival time. Create Gantt charts illustrating the execution of these processes using SRT.  Process Arrival Time Burst Time $P_1$ 0.0 7 $P_2$ 2.0 4   | 5  | 3  | 3 | 2 | 2.1.3 |
|     | P <sub>3</sub> 4.0 1 P <sub>4</sub> 5.0 4   |    |    |   |   |       |
|     | Answer:    P <sub>1</sub>   P <sub>2</sub>   P <sub>3</sub>   P <sub>2</sub>   P <sub>4</sub>   P <sub>1</sub>     D 2 4 5 7 11 16  |    |    |   |   |       |
|     | Average waiting time  |    |    |   |   |       |
|     | = ([(0-0)+(11-2)]+[(2-2)+(5-4)]+(4-4)+(7-5))/4 $= 9+1+0+2)/4$ $= 3$   |    |    |   |   |       |
|     | Average turn-around time<br>= (16-0) + (7-2) + (5-4) + (11-5))/4<br>= 7   |    |    |   |   |       |
|     | Part – C Either OR Choice Questions (2 X 10 = 20 Marks)   |    |    |   |   |       |
| 15a | Design and describe the activity of the process manager that handles the removal of the running process from the CPU and the selects another process based on any strategy with respect to processes scheduling. Represent the process scheduling using <b>Queueing diagram.</b>  | 10 | L4 | 2 | 3 | 2.6.3 |
|     | Answer:   |    |    |   |   |       |
|     | 1. The OS maintains all Process Control Blocks (PCBs) in Process Scheduling Queues. The OS maintains a separate queue for each of the process states and PCBs of all processes in the same execution state are placed in the same queue. When the state of a process is changed, its PCB is unlinked from its current queue and moved to its new state queue. |    |    |   |   |       |
|     | <ol> <li>Job queue – This queue keeps all the processes in the system.</li> <li>Ready queue – This queue keeps a set of all processes residing in main memory, ready and waiting to execute. A</li> </ol>   |    |    |   |   |       |
|     | new process is always put in this queue.  4. Device queues – The processes which are blocked due to unavailability of an I/O device constitute this queue.  5. Schedulers are special system software which handle  |    |    |   |   |       |

|     | process scheduling in various ways. Their main task is to select the jobs to be submitted into the system and to decide which process to run. Schedulers are of three types  a) Long-Term Scheduler b) Short-Term Scheduler c) Medium-Term Scheduler  ready queue  ready queue  time slice expired  fork a child interrupt occurs  (or)  |    |    |   |   |       |
|-----|--|----|----|---|---|-------|
| 15b | What is a race condition? Describe a solution to the Dining philosopher problem so that no races arise.  Answer:  Race condition: The situation where several processes access – and manipulate shared data concurrently. The final value of the shared data depends upon which process finishes last. To prevent race conditions, concurrent processes must be synchronized. Data consistency requires that only one processes should update the value of a data item at any time. This is ensured through the notion of a critical section.  Dining philosopher solution:  Wait and Signal operations will be used for the solution of the Dining Philosophers Problem, for picking a chopstick wait operation can be executed while for releasing a chopstick signal semaphore can be executed.  Semaphore: A semaphore is an integer variable in S, that apart from initialization is accessed by only two standard atomic operations - wait and signal, whose definitions are as follows: void Philosopher  {     while(1)  {         Wait( take_chopstickC[i] );         Wait( take_chopstickC[i] ); | 10 | L4 | 2 | 3 | 2.6.3 |





## (ii) Turnaround Time for P3 under each algorithm

Turnaround time is calculated as the time from the process's arrival to its completion.

- **FCFS**: P3 arrives at time 15 and finishes at time 130.
  - Turnaround Time = 130 15 = 115 ms
- SJF: P3 arrives at time 15 and finishes at time 115.
  - o Turnaround Time = 115 15 = 100 ms
- **Priority**: P3 arrives at time 40 and finishes at time 75.
  - o Turnaround Time = 75 15 = 60 ms
- **Round Robin**: P3 receives CPU time in slices. P3 arrives at time 15 and finishes at time 155.
  - o Turnaround Time = 155 15 = 140 ms

iii)

### **FCFS**

- Waiting Time P1 = 0 ms (starts immediately)
- Waiting Time P2 = 40 15 = 25 ms
- Waiting Time P3 = 95 15 = 80 ms
- Waiting Time P4 = 130 35 = 95 ms
- Waiting Time P5 = 140 40 = 100 ms

**Average Waiting Time (FCFS)** = (0 + 25 + 80 + 95 + 100) / 5 = 60 ms

#### **SJF**

- Waiting Time P1 = 0 ms
- Waiting Time P2 = 115-15 = 100 ms
- Waiting Time P3 = 80-15 = 65 ms
- Waiting Time P4 = 40-35 = 5 ms
- Waiting Time P5 = 50-40 = 10 ms

**Average Waiting Time (SJF)** = (0 + 100+65+5+10) / 5 = 36 ms

### **Priority Scheduling**

- Waiting Time P1 = 0 ms
- Waiting Time P2 = 75 15 = 60 ms
- Waiting Time P3 = 40 15 = 25 ms
- Waiting Time P4 = 160 35 = 125 ms
- Waiting Time P5 = 130 40 = 90 ms

**Average Waiting Time (Priority)** = (0 + 60 + 25 + 125 + 90) / 5 = 60 ms

## **Round Robin Scheduling**

Walting time

$$P_1 = (0-0)+(25-15)+(45-30)+(70-50)$$
 $+(90-75)+(10-85)$ 
 $= 0+10+15+20+15+15$ 
 $= [T5]$ 
 $P_2 = (15-15)+(30-20)+(50-35)+(15-100)$ 
 $(75-57)+(95-80)+(145-135)+(130-120)+(145-135)+(151-150)$ 
 $= 0+10+15+20+15+15+10+$ 
 $= 100$ 

Waiting time

$$P_3 = (20-15) + (55-25) + (80-60) + (100-85) + (120-105) + (135-125) + (150-140)$$
 $= 5+30+20+15+15+10+10$ 
 $= [05]$ 
 $P_4 = (35-35) + (60-40) = 0+20 = [20]$ 
 $P_5 = (40-40) + (65-45) + (85-70) + (105-90) + (125-110) + (140-130)$ 
 $= 0+20+15+15+15+10 = [75]$ 

Round Robin Average Waiting time = 75+100+105+20+75

 $= 375/5$ 

SJF Scheduling has the minimum average waiting time of 36ms.

|      |  | soui   | ces.   | At 7   | Γ0 w   | oroc<br>e ha   | ve tl  | he fo   | llow  | ing :  | syste                                     | em si  | tate:  |   |               |  |  |  |
|------|--|--|--|--|--|--|--|---|---|--|---|--|--|---|---------------|--|--|--|
|      |  |  | cation   |  |  | Max  |  |   |   | Avai   |   |  |  |   |               |  |  |  |
|      |  | Α  | В  | С  | D  | Α  | В  | С   | D   | Α  | В   | С  | D  | 1   |               |  |  |  |
|      | P <sub>0</sub>   | 0  | 1  | 1  | 0  | 0  | 2  | 1   | 0   | 1  | 5   | 2  | 0  |   |               |  |  |  |
|      | P <sub>1</sub>   | 1  | 2  | 3  | 1  | 1  | 6  | 5   | 2   |  |   |  |  | ĺ   |               |  |  |  |
|      | P <sub>2</sub>   | 1  | 3  | 6  | 5  | 2  | 3  | 6   | 6   |  | Ī   |  |  | Ī   |               |  |  |  |
|      | P <sub>3</sub>   | 0  | 6  | 3  | 2  | 0  | 6  | 5   | 2   |  | Ī   |  |  | Ī   |               |  |  |  |
|      | P <sub>4</sub>   | 0  | 0  | 1  | 4  | 0  | 6  | 5   | 6   |  |   |  |  |   |               |  |  |  |
| Ansv | ver:   |  | _  |  |  |  |  |   | '   |  |   |  | •  |   |               |  |  |  |
| Need |  |  |  |  |  |  |  |   |   |  |   |  |  |   |               |  |  |  |
|      |  |  | 1  | 4  |  | В  |  |   | С   |  | [   | )  |  |   |               |  |  |  |
|      | P <sub>0</sub>   |  | (  | )  |  | 1  |  |   | 0   |  | (   | )  |  | 1   |               |  |  |  |
|      | P <sub>1</sub>   |  | (  | )  |  | 4  |  |   | 2   |  |   |  |  | 1   |               |  |  |  |
|      | P <sub>2</sub>   |  |  | 1  |  | 0  |  |   | 0   |  |   |  |  | +   |               |  |  |  |
|      | P <sub>3</sub>   |  | (  | 1  |  | 0  |  |   | 2   |  | (   | )  |  | -   |               |  |  |  |
|      |  |  | (  |  |  | 6  |  |   |   |  |   |  |  |   |               |  |  |  |
|      | $P_4$  |  |  |  |  |  |  |   |   |  |   | •  |  | -   |               |  |  |  |
| •    |  |  | to s   | ee if  |  | 10 (0  |  |   |   |  |   | equa   |  | vork.   | It            |  |  |  |
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| •    | is, w pr N th ag le N N fin N ca   | , so look look look look look look look l  | to s<br>let's<br>by ac<br>ss to<br>let's<br>e har<br>st the<br>an 0<br>2 (1,0<br>3 (0,0  | ee if set f dding worl check to corn (the corn (the look worl look worl worl world).   | inish g the k. ck to check respon four is n is le k at P k an  | see  | rue freated if ne eme ess the nan veed ish   | eed1<br>element),<br>nan v  | s less<br>nat produce<br>(0,4,<br>ent ont in<br>we n<br>kx, so<br>6,4,2   | es (0<br>2,1)<br>If the<br>work<br>eed to<br>we come   | <pre></pre>                               | d als<br>0) for<br>work<br>tor n<br>ove o<br>mov<br>pdat   | o upor that. Reredlese 1 is on to be one work  | s not P2. to P3rk and                                   | er<br>3.<br>d |  |  |  |
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# Course Outcome (CO) and Bloom's level (BL) Coverage in Questions

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