

[UEFI Practice] SlimBootloader Usage

jiangwei0512

Posted on 2021-10-01 22:51:03

Read 2.9k

Collection 5

Likes 2

Category Column: UEFI Development BasicsArticle Tags: slimbootloader

2048 AI Community

The article has been collected by the community

Join the community

UEFI Development ...

This column includes this content

136 articles

Subscribe to our column

摘要 This article details the latest developments of Intel SlimBootloader, including its features as an open source platform boot loader, source code acquisition, build process, and practical application of compiling using Python scripts and QEMU platform in Windows environment.


The summary is generated in C Know , supported by DeepSeek-R1 full version, go to experience>

illustrate

The basic contents of Slim Bootloader have been introduced in [UEFI Practice] Slim Bootloader Introduction, but because it is a long time ago, many contents have expired and cannot be used, so here is an updated version .


Brief description

Intel® Slim Bootloader, referred to as Intel® SBL (hereinafter referred to as SBL), is a bootloader launched by Intel. A complete introduction to it can be found at the following website: <https://www.intel.com/content/www/us/en/design/products-and-solutions/technologies/slim-bootloader/overview.html> . It can be considered as the Intel version of coreboot, which is more inclined to the x86 platform in terms of platform support, security, and scalability. Its features are as follows:




Fast

Optimized for systems with a critical reliance on boot speed.




Small

Small footprint means lower flash sizes requirements, reducing overall BOM cost. Allows fully redundant images for resilient solutions.



Customizable

Designed with modularity in mind, allowing for easy addition of differentiating features.



Secure

Supports verified boot, measured boot, and secure firmware updates. Build secure boot solutions when paired with Intel® Platform Protection Technology with Boot Guard.

CSDN @jiangwei0512

It supports various common operating systems and is open source (using BSD license).

Source code download

The source code of SBL can be downloaded from <https://github.com/slimbootloader/slimbootloader> . There is also a mirror on Gitee at <https://gitee.com/jiangwei0512/slimbootloader> .

Since the SBL version is constantly evolving, the code may differ due to updates. Please refer to the actual downloaded code. The current version is:

```
Windows PowerShell - git log
commit 7f461c59e0d513d9238622f2ddfa5573dd1d35181 (HEAD -> master, origin/master, origin/HEAD)
Author: Maurice Ma <maurice.ma@intel.com>
Date: Wed Sep 22 14:08:53 2021 -0700

    Enable native GFX initialization support

    On QEMU or Smimics, it might need to do native GFX initialization
    if the GFX is not initialized by FSP. This patch added the native
    GFX support for BOCHS graphics controller.

    Signed-off-by: Maurice Ma <maurice.ma@intel.com>

commit 4d83e1126b5763629dbb2c395c1b2098b057ac6
Author: Maurice Ma <maurice.ma@intel.com>
Date: Wed Sep 22 12:56:07 2021 -0700

    Add BOCHS graphics init support

    On Simics or QEMU, BOCHS graphics is used. This patch provides
    library to initialize BOCHS graphics. It is useful when GFX
    initialization is not provided by FSP. In this case, the native
    code can be used to initialize graphics.

    Signed-off-by: Maurice Ma <maurice.ma@intel.com>

commit b628b95e90daa051749c4b4a33c6f6ebbd629a
Author: Ong Kok Tong <kok.tong.ong@intel.com>
Date: Thu Sep 23 11:19:46 2021 +0800

    [EHL] Update the EHL BoardPkg version to 1.2

    Update EHL BoardPkg version to 1.2 to align with current
    software package version:

    ERINFO_PROJ_MAJOR_VER: 1 PV Quality release
    VERINFO_PROJ_MINOR_VER: 0: PV 1: MR1 2: MR2 etc.

    PROJ_MAJOR_VER -> 1 (Maintenance Release candidate)
    PROJ_MINOR_VER -> 2 (1st revision of MR2 release)

    Signed-off-by: Ong Kok Tong <kok.tong.ong@intel.com>

commit 1e93e7bec2b47f85d877ac49375f61223693cd05
Author: Praveen Hp <praveen.hodagatta.pranesh@intel.com>
Date: Thu Sep 23 11:14:10 2021 +0800

    [CML] Fix chipsec SHM issues in S3 path

    This patch upstreams previous CML patch for RC3 hotfix:

CSDN @jiangwei0512
```

The downloaded source code directory structure is as follows:

名称	修改日期	类型	大小
azurepipelines	2021/9/26 0:51	文件夹	
.git	2021/10/1 19:23	文件夹	
BaseTools	2021/10/1 19:35	文件夹	
BootloaderCommonPkg	2021/9/26 0:51	文件夹	
BootloaderCorePkg	2021/9/26 0:51	文件夹	
IntellFsp2Pkg	2021/9/26 0:51	文件夹	
Licenses	2020/6/27 19:20	文件夹	
MdePkg	2020/6/27 19:20	文件夹	
PayloadPkg	2021/9/26 0:51	文件夹	
Platform	2021/9/26 0:51	文件夹	
Silicon	2021/9/26 0:51	文件夹	
.gitignore	2021/9/26 0:51	文本文档	1 KB
BuildLoader.py	2021/9/26 0:51	Python File	69 KB
CODEOWNERS	2020/6/27 19:20	文件	1 KB
Dockerfile	2021/9/26 0:51	文件	2 KB
LICENSE	2020/6/27 19:20	文件	3 KB
README.rst	2021/9/26 0:51	RST 文档	1 KB

The following is a brief description of the main files or directories:

File or Directory	illustrate
BaseTools	The actual tool used to generate the SBL binary is a part of the tools located in BootloaderCorePkg\Tools.
BootloaderCommonPkg	Contains common library functions for use by SBL.
BootloaderCorePkg	SBL divides the startup into several different stages, and their main codes are included in this directory: Stage1A: The initial stage, spanning from the Reset Vector to the setting of Case As RAM (CAR), it will call FSP-T; Stage1B: From the setting of CAR to the completion of memory initialization, it will call FSP-M; Stage2: Perform other platform initialization (CPU and Chipset-related initialization outside of memory), and jump to Payload, it will call FSP-S.
IntelFsp2Pkg	FSP support package.
MdePkg	EDKII universal code.
PayloadPkg	Contains code for loading OS and upgrading firmware.
Platform	The code supported by the platform.
Silicon	SoC support code.
BuildLoader.py	Generates the script used by the Slim Bootloader binary, which will set up the build chain, precompile, compile, and other operations after the build is completed.

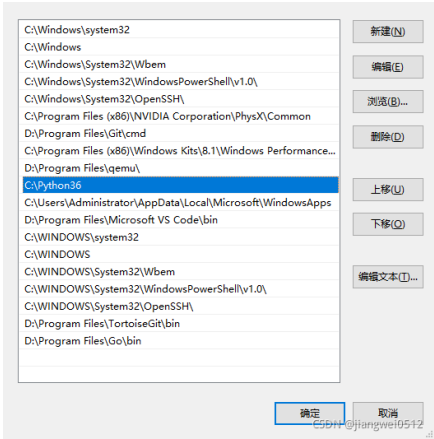
The code organization logic of SBL is to maintain the independence and scalability of the modules. The following requirements must be guaranteed:

- 1. BootloaderCorePkg and PayloadPkg should be independent and unrelated to each other;
- 2. PayloadPkg code should not depend on Platform or Silicon;
- 3. PayloadPkg should only depend on BootloaderCommonPkg.

Building the SBL binary

Windows 10 is used here to build SBL. Usually, the binary generated by compiling under Windows is smaller than that using GCC. For Intel platforms, UEFI development under Windows is more common, which is why Windows is used.

SBL uses Python scripts `BuildLoader.py` to complete the entire build process. The current SBL version uses Python3, so you need to install Python3 and put the execution path in the environment variable:



The version information is as follows (`Ctrl+z + Enter` exit Python interface):

```
Windows PowerShell - python -v
# C:\Python36\lib\encodings\__pycache__\aliases.cpython-36.pyc matches C:\Python36\lib\encodings\aliases.py
# code object from 'C:\Python36\lib\encodings\__pycache__\aliases.cpython-36.pyc'
import 'encodings.aliases' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE2034A8>
import 'encodings' # <frozen_importlib_external.SourceFileLoader object at 0x00000209ADE0D0A0>
# C:\Python36\lib\encodings\__pycache__\utf_8.cpython-36.pyc matches C:\Python36\lib\encodings\utf_8.py
# code object from 'C:\Python36\lib\encodings\__pycache__\utf_8.cpython-36.pyc'
import 'encodings.utf_8' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE213550>
import 'signal' # <class 'frozen_importlib.BuiltinImporter'>
# C:\Python36\lib\encodings\__pycache__\latin_1.cpython-36.pyc matches C:\Python36\lib\encodings\latin_1.py
# code object from 'C:\Python36\lib\encodings\__pycache__\latin_1.cpython-36.pyc'
import 'encodings.latin_1' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE2139B0>
# C:\Python36\lib\__pycache__\io.cpython-36.pyc matches C:\Python36\lib\io.py
# code object from 'C:\Python36\lib\__pycache__\io.cpython-36.pyc'
# C:\Python36\lib\__pycache__\abc.cpython-36.pyc matches C:\Python36\lib\abc.py
# code object from 'C:\Python36\lib\__pycache__\abc.cpython-36.pyc'
import 'weakrefset' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE2139B0>
# code object from 'C:\Python36\lib\__pycache__\weakrefset.cpython-36.pyc'
import 'weakrefset' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE21A908>
import 'abc' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE213F60>
import 'io' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE213EA8>
# C:\Python36\lib\__pycache__\site.cpython-36.pyc matches C:\Python36\lib\site.py
# code object from 'C:\Python36\lib\__pycache__\site.cpython-36.pyc'
# C:\Python36\lib\__pycache__\os.cpython-36.pyc matches C:\Python36\lib\os.py
# code object from 'C:\Python36\lib\__pycache__\os.cpython-36.pyc'
import 'errno' # <class 'frozen_importlib.BuiltinImporter'>
# C:\Python36\lib\__pycache__\stat.cpython-36.pyc matches C:\Python36\lib\stat.py
# code object from 'C:\Python36\lib\__pycache__\stat.cpython-36.pyc'
import 'stat' # <class 'frozen_importlib.BuiltinImporter'>
import 'stat' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE241588>
# C:\Python36\lib\__pycache__\ntpath.cpython-36.pyc matches C:\Python36\lib\ntpath.py
# code object from 'C:\Python36\lib\__pycache__\ntpath.cpython-36.pyc'
# C:\Python36\lib\__pycache__\genericpath.cpython-36.pyc matches C:\Python36\lib\genericpath.py
# code object from 'C:\Python36\lib\__pycache__\genericpath.cpython-36.pyc'
import 'genericpath' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE250470>
import 'ntpath' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE241C50>
# C:\Python36\lib\__pycache__\collections_abc.cpython-36.pyc matches C:\Python36\lib\collections_abc.py
# code object from 'C:\Python36\lib\__pycache__\collections_abc.cpython-36.pyc'
import 'collections_abc' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE250A58>
import 'os' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE230518>
# C:\Python36\lib\__pycache__\sitebuiltins.cpython-36.pyc matches C:\Python36\lib\sitebuiltins.py
# code object from 'C:\Python36\lib\__pycache__\sitebuiltins.cpython-36.pyc'
import 'sitebuiltins' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE230908>
# C:\Python36\lib\__pycache__\sysconfig.cpython-36.pyc matches C:\Python36\lib\sysconfig.py
# code object from 'C:\Python36\lib\__pycache__\sysconfig.cpython-36.pyc'
import 'sysconfig' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE230D08>
import 'site' # <frozen_importlib_external.SourceFileLoader object at 0x00000209AE22B198>
Python 3.6.8 (tags/v3.6.8:3c6b436a57, Dec 24 2018, 00:16:47) [MSC v.1916 64 bit (AMD64)] on win32
Type 'help', 'copyright', 'credits' or 'license()' for more information.
import 'atexit' # <class 'frozen_importlib.BuiltinImporter'>
```

Then you can see how to use it by executing `BuildLoader.py` the script:

```
Windows PowerShell
F:\Gitee\slimbootloader>BuildLoader.py
usage: BuildLoader.py [-h] (build, clean, build_dsc) ...

positional arguments:
  (build, clean, build_dsc)  command
                             build      build SBL firmware
                             clean      clean build dir
                             build_dsc  build a specified dsc file

optional arguments:
  -h, --help                show this help message and exit

F:\Gitee\slimbootloader>BuildLoader.py build
usage: BuildLoader.py build [-h] [-r] [-v] [-fp FSPPATH] [-fd] [-a {ia32,x64}]
                             [-no] [-p PAYLOAD] [-k] [-t TOOLCHAIN]
                             board
BuildLoader.py build: error: the following arguments are required: board
```

The command used to build the SBL binary is (no XXX is specified in the above figure, so an error is reported):

```
bash
1 | BuildLoader.py build XXX
```

XXX Refers to the target platform, which can be determined by looking Platform\xxxBoardPkg\BoardConfig.py at self.BOARD_NAME . For example, in this example, the SBL binary used to build QEMU corresponds to Platform\QemuBoardPkg\BoardConfig.py , where self.BOARD_NAME the value is as follows:

```
bash
1 | self.BOARD_NAME = 'qemu'
```

So the actual build instructions are:

```
bash
1 | BuildLoader.py build qemu
```

The execution results are as follows:

```
Windows PowerShell
F:\Gitee\slimbootloader>BuildLoader.py build qemu
Checking Toolchain Versions...
- C:\Python30\python.exe: Version 3.6.8 (>= 3.6.0) [PASS]
- C:\OpenSSL\openssl.exe: Version 0.9.3h (>= 1.1.0g) [FAIL]
- C:\Nasm\nasm: Version 2.13.03 (>= 2.12.02) [PASS]
- C:\ASL\iasl: Version 20150619 (>= 20160422) [FAIL]
- git: Version 2.21.0. (>= 2.20.0) [PASS]
- vs: Version 2013 (>= 2015) [FAIL]
...Failed! Please check toolchain versions!
```

BuildLoader.py The dependent tools and their version information will be checked, so the whole picture is very important. The above tools need to be installed in the current Windows environment. Because this machine was used for UEFI development before, most of the tools are available, but the versions of openssl, iasl and Visual Studio do not meet the requirements. The latest version of openssl can be downloaded from <https://sourceforge.net/projects/openssl-for-windows/> , and the latest version of iasl can be downloaded from <https://www.acpica.org/downloads/binary-tools> . Visual Studio needs to use version 2015 or above. Here, the 2019 version is selected. You can download it from the official website and install it online. It should be noted that you need to select the following content during installation:



Here we can ensure that the tools required for SBL compilation (such as cl.exe, etc.) can be installed correctly. Execute the compilation command again, and the results are as follows:

After that, you need to put the generated content into the SBL directory. The specific directory is as follows:

slimbootloader > Silicon > QemuSocPkg				
名称	修改日期	类型	大小	
FspBin	2021/9/26 0:51	文件夹		
Include	2021/9/26 0:51	文件夹		
Library	2020/6/27 19:20	文件夹		
QemuSocPkg.dec	2020/6/27 19:20	DEC 文件	1 KB	

Put the header file in the Include directory (if there is already a file with the same name, just overwrite it), and put Fsp.bsf and FspRel.bin in the FspBin directory.

This completes the setup of the FSP binary, and then executes the previous build command to successfully complete the build:

FLASH MAP (RomSize = 0x00400000)			
NAME	OFFSET (BASE)	SIZE	FLAGS
TOP SWAP A			
SG1A	0x3f0000(0xFFFFF000)	0x010000	Uncompressed, TS_A
TOP SWAP B			
SG1A	0x3e0000(0xFFFFE000)	0x010000	Uncompressed, TS_B
REDUNDANT A			
KEYH	0x34f000(0xFFFFD000)	0x001000	Uncompressed, R_A
CNFG	0x34e000(0xFFFFC000)	0x001000	Uncompressed, R_A
FWUP	0x34d000(0xFFFFB000)	0x018000	Compressed, R_A
SG1B	0x396000(0xFFFF96000)	0x030000	Compressed, R_A
SG02	0x37e000(0xFFFF7E000)	0x018000	Compressed, R_A
EMPTY	0x360000(0xFFFF60000)	0x01e000	Uncompressed, R_A
REDUNDANT B			
KEYH	0x35f000(0xFFFFF000)	0x001000	Uncompressed, R_B
CNFG	0x35e000(0xFFFFE000)	0x001000	Uncompressed, R_B
FWUP	0x346000(0xFFFF46000)	0x018000	Compressed, R_B
SG1B	0x316000(0xFF16000)	0x030000	Compressed, R_B
SG02	0x2fe000(0xFFE000)	0x018000	Compressed, R_B
EMPTY	0x2e0000(0xFFE0000)	0x01e000	Uncompressed, R_B
NON REDUNDANT			
PTES	0x24f000(0xFFD000)	0x001000	Uncompressed, NR
IPFW	0x2cf000(0xFFC000)	0x010000	Uncompressed, NR
EP1D	0x0c2000(0xFFC2000)	0x20d000	Uncompressed, NR
PT1D	0x0a2000(0xFFCA2000)	0x020000	Compressed, NR
VARS	0x0a0000(0xFFCA0000)	0x002000	Uncompressed, NR
EMPTY	0x001000(0xFFC01000)	0x09f000	Uncompressed, NR
NON VOLATILE			
RSVD	0x000000(0xFFC00000)	0x001000	Uncompressed, NV

The generated binary is Build\BootloaderCorePkg\DEBUG_VS2019\FV located at SlimBootloader.bin.

USE

Because the SBL binary is built for QEMU, if you want to use it, you can directly start it through the QEMU command. Of course, QEMU needs to be installed separately, which can be downloaded from <https://www.qemu.org/download/>. The command to use is as follows:

```
bash
1 qemu-system-x86_64 -machine q35 -nographic -serial mon:stdio -pflash Build\BootloaderCorePkg\DEBUG_VS2019\FV\SlimBootloader.bin
```

Here are the execution results:

```
bash
1 F:\Gitee\slimbootloader>qemu-system-x86_64 -machine q35 -nographic -serial mon:stdio -pflash Build\BootloaderCorePkg\DEBUG_VS2019\FV\SlimBootloader.bin
2 WARNING: Image format was not specified for 'Build\BootloaderCorePkg\DEBUG_VS2019\FV\SlimBootloader.bin' and probing guessed raw.
3 Automatically detecting the format is dangerous for raw images, write operations on block 0 will be restricted.
4 Specify the 'raw' format explicitly to remove the restrictions.
5
6 ===== Intel Slim Bootloader STAGE1A =====
7 SBID: SB_QEMU
8 ISVN: 001
9 IVER: 001.000.001.001.03646
10 SVER: 7F461C59E0D51309
11 FDBG: BLD(D IA32) FSP(R)
12 FSPV: ID($QENFSP$) REV(00001000)
13 Loader global data @ 0x00001C38
14 Run STAGE1A @ 0x00070000
15 Load STAGE1B @ 0x00040000
16 HASH verification for usage (0x00000001) with Hash Alg (0x2): Success
17
18 ===== Intel Slim Bootloader STAGE1B =====
19 Host Bridge Device ID:0x29C0
20 Board ID:0x1 - Loading QEMU!
twen QEMU Flash: Attempting flash detection at FFC00000
twen QemuFlashDetected => FD behaves as FLASH
twen QemuFlashDetected => Yes
twen SpiInstance = 0000E470
25 Variable region: 0xFFCA0000:0x2000
26 SPI WRITE: FFCa0010 00000004
27 SPI WRITE: FFCa0011 00000001
28 SPI WRITE: FFCa0014 00000008
29 SPI WRITE: FFCa001C 00000004
30 SPI WRITE: FFCa0011 00000001
31 Loading Component KEYH: HS_
32 Registering container KEYH
33 HASH verification for usage (0x0000100) with Hash Alg (0x2): Success
34 SignType (0x2) SignSize (0x180) SignHashAlg (0x2)
35 RSA verification for usage (0x00000100): Success
36 HASH verification for usage (0x00000000) with Hash Alg (0x2): Success
37 Append public key hash into store: Success
38 Load EXT CFG Data @ 0x0000EB54:0x0158 ... Success
39 HASH verification for usage (0x00000200) with Hash Alg (0x2): Success
40 SignType (0x2) SignSize (0x180) SignHashAlg (0x2)
41 RSA verification for usage (0x00000200): Success
42 BOOT: BP0
43 MODE: 0
44 BoardID: 0x01
45 PlatformName: QEMU_01
46 Memory Init
47 Found Config TAG: 0x180 @ 0x0000ECCC
48 MemCfg.Test1=11223344, MemCfg.Test2=11223346
49 Found Config TAG: 0x200 @ 0x0000E CDC
```

```

50 SilCfg.Test1=11223347, SilCfg.Test2=04030201
51 Call FspMemoryInit ... Success
52 Loader global data @ 0x06EBFD70
53 Load page table from memory @ 0x06B74000
54 Remapping Stage to 0x06B77000
55 FSP Resource HOB Range Type Owner
56 =====
57 0000000000000000-0000000000A0000 00 00000000-0000-0000-0000-000000000000
58 0000000000A0000-0000000000100000 05 00000000-0000-0000-0000-000000000000
59 0000000000100000-00000000006F0000 00 00000000-0000-0000-0000-000000000000
60 00000000006F0000-0000000000700000 05 69A79759-1373-4367-A6C4-C7F59EFD986E
61 0000000000700000-0000000000800000 05 D038747C-D00C-4980-B319-490199A47D55
62
63 Switch to memory stack @ 0x06EFFF00
64 Stage1 stack: 0x2000 (0xF28 used)
65 Stage1 heap: 0xE000 (0x1F14 used)
66 Call FspTempRamExit ... Success
67 Memory FSP @ 0x06F00000
68 Memory TOP @ 0x06B74000
69 Loading Component FLMP:SG02
70 HASH verification for usage (0x00000002) with Hash Alg (0x2): Success
71 Loaded STAGE2 @ 0x06E55000
72
73 ===== Intel Slim Bootloader STAGE2 =====
74 Unmapping Stage
75 Board GPIO Init
76 Get base platform GPIO table from board ID 0
77 Programming 7 GPIO entries
78 GPIO GPP_A00 DATA: 0x00000000 0x00000010
79 GPIO GPP_A02 DATA: 0x80000002 0x00000012
80 GPIO GPP_A03 DATA: 0xC0000003 0x00000013
81 GPIO GPP_A04 DATA: 0x01000004 0x00000014
82 GPIO GPP_A05 DATA: 0x41000005 0x00000015
83 GPIO GPP_A06 DATA: 0x81000006 0x00000016
84 GPIO GPP_A07 DATA: 0xC1000007 0x00000017
85 Test variable services
86 SPI WRITE: FFCA0020 00000004
87 SPI WRITE: FFCA0021 00000001
88 SPI WRITE: FFCA0024 00000008
89 SPI WRITE: FFCA002C 00000004
90 SPI WRITE: FFCA0011 00000001
91 SPI WRITE: FFCA0021 00000001
92 SPI WRITE: FFCA0011 00000001
93 Loading Component IPFW:TST3
94 Registering container IPFW
95 HASH verification for usage (0x00001000) with Hash Alg (0x2): Success
96 SignType (0x2) SignSize (0x180) SignHashAlg (0x2)
97 RSA verification for usage (0x00001000): Success
98 HASH verification for usage (0x00000000) with Hash Alg (0x2): Success
99 SignType (0x2) SignSize (0x180) SignHashAlg (0x2)
100 RSA verification for usage (0x00000000): Success
101 Load IP firmware @ 0:0x0 - Bad Buffer Size
102 Silicon Init
103 Select VBT ImageId 0x00000001
104 Call FspSiliconInit ...
105 Success
106 Graphics Info: 800 x 600 x 32 @ 0x80000000
107 MEM: 0000000000000000 0000000000A0000 00 1
108 MEM: 0000000000A0000 00000000006F0000 00 2
109 MEM: 0000000000100000 00000000006A0000 00 1
110 MEM: 00000000006B0000 00000000004000 01 2
111 MEM: 00000000006B4000 000000000068000 00 3
112 MEM: 00000000006B6C000 000000000008000 00 4
113 MEM: 00000000006B74000 000000000038C000 00 2
114 MEM: 00000000006F0000 0000000000100000 00 2
115 MEM: 0000000000700000 0000000000100000 00 2
116 MEM: 00000000FFC00000 0000000000400000 00 2
117 MP Init (Wakeup)
118 MP Init (Run)
119 Detected 1 CPU threads
120 CPU 0 APIC ID: 0
121 SMM rebase done on 1 CPUs
122 PCI Enum
123 Call FspNotifyPhase(20) ... Success
124 ACPI Init
125 Publish ACPI table: FACP
126 Publish ACPI table: HPET
127 Publish ACPI table: APIC
128 Publish ACPI table: MCFG
129 Publish ACPI table: FPDT
130 Publish ACPI table: BGRT
131 Publish ACPI table: TEST
132 ACPI Ret: Success
133 Enable SMRR
134 Loading Payload ID PYLD
135 Loading Component FLMP:PYLD
136 HASH verification for usage (0x00000004) with Hash Alg (0x2): Success
137 Load Payload ID 0x00000000 @ 0x06CB7000
138 PE32 Format Payload
139 HOB @ 0x06EC0000
140 Created 4 OS boot options (Current: 0)
141 Stage2 stack: 0x40000 (stack used 0x9CC, HOB used 0xFB0, 0x3E684 free)
142 Stage2 heap: 0x34C000 (0x209000 used, 0x143000 free)
143 Payload entry: 0x06CB7260
144 Jump to payload
145
146
147 Payload startup
148 ACPI PmTimer Base: 0x408
149 PCI Express Base: 0xE0000000
150
151
152 =====0s Loader=====
153
154
155 Press any key within 1 second(s) to enter the command shell
156 Boot options (in HEX):
157
158 Idx|ImgType|DevType|DevNum|Flags|HwPart|FsType|SwPart|File|Lbaoffset
159 0| | | SD | 0 | 0 | 0 | AUTO | 0 | iasimage.bin
160 1| | | SATA | 0 | 0 | FF | AUTO | 0 | iasimage.bin
161 2| | | NVME | 0 | 0 | 0 | AUTO | 0 | iasimage.bin
162 3| | | USB | 0 | 0 | 0 | AUTO | 0 | iasimage.bin
163
164
165 ===== Try Booting with Boot Option 0 =====

```

```

166 BootMediumPciBase(0x300)
167 Getting boot image from SD
168 MMC global data init
169 Use SDMA instead of ADMA2
170 MMC Phase 1 init
171 SdMmcHcReset: reset done with Time out
172 SdMmcHcReset Fail Status = 0x80000012
173 Failed to init media - Time out
174 Failed to Initialize Boot Device - Type 1, Instance 0
175 Payload normal heap: 0x2000000 (0xB000 used)
176 Payload reserved heap: 0x4000 (0x0 used)
177 Payload stack: 0x10000 (0x4D4 used)
178
179
180 ===== Try Booting with Boot Option 1 =====
181 BootMediumPciBase(0x1F02)
182 Getting boot image from SATA
183 Init AHCI controller E00FA000
184 AHCI port [2] has a [cdrom]
185 Try to find boot partition
186 AhciDeviceRead Status = Device Error
187 Partition type: UNKNOWN (1 logical partitions)
188 Find partition success
189 Init File system
190 AhciDeviceRead Status = Device Error
191 AhciDeviceRead Status = Device Error
192 No partitions found, Status = Device Error
193 Failed to Initialize Boot File System - SwPart 0
194 Payload normal heap: 0x2000000 (0x10F000 used)
195 Payload reserved heap: 0x4000 (0x0 used)
196 Payload stack: 0x10000 (0x664 used)
197
198 Deinit AHCI controller 0
199 Not a AHCI controller or Disabled
200
201 ===== Try Booting with Boot Option 2 =====
202 BootMediumPciBase(0x300)
203 Getting boot image from NVME
204 NvmExpressDriverBindingStart: start
205 NvmeControllerInit: the controller doesn't support NVMe command set
206 NvmExpressDriverBindingStart: end with Unsupported
207 Failed to init media - Unsupported
208 Failed to Initialize Boot Device - Type 6, Instance 0
209 Payload normal heap: 0x2000000 (0x10F000 used)
210 Payload reserved heap: 0x4000 (0x0 used)
211 Payload stack: 0x10000 (0x664 used)
212
213
214 ===== Try Booting with Boot Option 3 =====
215 BootMediumPciBase(0x400)
216 Getting boot image from USB
217 Failed to initialize USB bus !
218 Failed to init media - Unsupported
219 Failed to Initialize Boot Device - Type 5, Instance 0
220 Payload normal heap: 0x2000000 (0x10F000 used)
221 Payload reserved heap: 0x4000 (0x0 used)
222 Payload stack: 0x10000 (0x664 used)
223
224
225 Shell>

```

◀ ● ▶

收起 ^

Finally entered the UEFI Shell.