

# BIOS Practice: Reading and Writing Logical Devices (SIO)

原创

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**摘要** This document introduces SIO (System Input/Output) programming, especially the logic device read and write process for the IT8738 chip. First, enter PNP mode, then select the logic device, then read or write the device register, and finally exit PNP mode. The sample code shows the function implementation of entering and exiting PNP mode, selecting LDN, and reading and writing the logic device register.

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A few days ago, I mentioned something about SIO. I have done some research on it before, but I have only studied a small part of its functions. I also participated in the programming of SIO when I was working on the Loongson BIOS, because this thing is definitely not used in laptops. Laptops use EC, and this SIO is equivalent to the EC on the PC board. Since a big guy asked me these questions, I immediately went back and found a document on the SIO chip. After reading it carefully, I found some missing parts. This article mainly records the principles of reading and writing logic devices and related programming.

Let's take the IT8738 chip as an example (the actual latest chip is unknown, and I only have this SPEC)

The following code has never been put into practice. I just wrote it according to the spec. Please correct me if it is wrong.

Logical Device Number (LDN). LDN is mentioned in the configuration section of the chip documentation. Let's take a look. They are 1, 2, 3, 4, 5, 6, 7, 8, A, F, 10, 11, 12, 19.

8. Configuration

8.1 Configuring Sequence

8.2 Configuration Registers

8.3 Global Configuration Registers (LDN: All)


8.4 Serial Port 1 Configuration Registers (LDN=01h)

8.5 Serial Port 2 Configuration Registers (LDN=02h)

8.6 Parallel Port Configuration Registers (LDN=03h)

8.7 Environment Controller Configuration Registers (LDN=04h)

8.8 KBC(Keyboard) Configuration Registers (LDN=05h)



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Previously, we mentioned how to operate SIO in the SIO related knowledge and the preliminary implementation of Smart Fan. So, let's sort out the process of reading and writing logical devices:

1. Enter PNP mode
2. Select logical device
3. Read the register of the logical device
4. Write the value of a register of the logical device
5. Exit PNP mode

According to this process, we can directly write 5 functions:

1. Enter PNP mode

```

2 | {
  |   3 |   IoWrite8(0x2E, 0x87);
4 |   IoWrite8(0x2E, 0x01);
5 |   IoWrite8(0x2E, 0x55);
6 |   IoWrite8(0x2E, 0x55);
7 |   return;
8 | }

```

## 2. Select logical device

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```

1 void SelectLDN (void)
2 {
3     //LDN Selection
4     unsigned int LDNumber;
5     scanf("%x",&LDNumber);
6     printf("Now LDN: %x",LDNumber);
7     IoWrite8(0x2E,0x07);
8     IoWrite8(0x2F,LDNumber);
9     return;
10 }

```

收起 ^

## 3. Read the register of the logical device

AI generated projects

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```

1 void ReadLdnRegister(void)
2 {
3     unsigned int row,col;
4     unsigned int addr=0;
5     unsigned int value[16][16];
6     printf("-----\n\n");
7     printf("00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F\n");
8
9     for(row=0;row<16;row++)
10     {
11         for(col=0;col<16;col++)
12         {
13             IoWrite8(0x2E,addr);
14             value[row][col]=IoRead8(0x2F);
15
16             if(value[row][col]<16){
17                 printf("0%X ",value[row][col]);
18             }else
19             {
20                 printf("%x ",value[row][col]);
21             }
22             addr++;
23         }
24         printf("\n");
25     }
26     return;
27 }

```

收起 ^

## 4. Write a register value of the logical device

```

1 void WriteLdnRegister(void)
2 {
3     char order;
4     unsigned int index, data;
5     printf("Do you need to write registers? YES please input W NO input Q:");
6     scanf("%c",&order);
7     if(order == 'W'){
8         printf("Please input Index:");
9         scanf("%x",&index);
10        printf("Please input Data:");
11        scanf("%x",&data);
12
13        IoWrite8(0x2E,index);
14        Iowrite8(0x2F,data);
15    } else

```

```
16 |         {17 |             printf("Exit write ld reg:");
18 |             return;
19 |         }
20 |     return;
21 | }
```

收起 ^

## 5. Exit Pnp mode

AI generated projects

登录复制

```
1 void ExitPnpMode (void)
2 {
3     unsigned int  RegData = 0;
4     IoWrite8(0x2E, 0x02);
5     RegData = IoRead8(0x2F) | BIT1;
6     IoWrite8(0x2E, 0x02);
7     IoWrite8(0x2F, RegData);
8     return;
9 }
```

Ok, with the above functions, we can write the main function. I won't write the whole thing, it's very easy, just write it in any way you want. Anyway, it has to be